SPEC CINT95 Results

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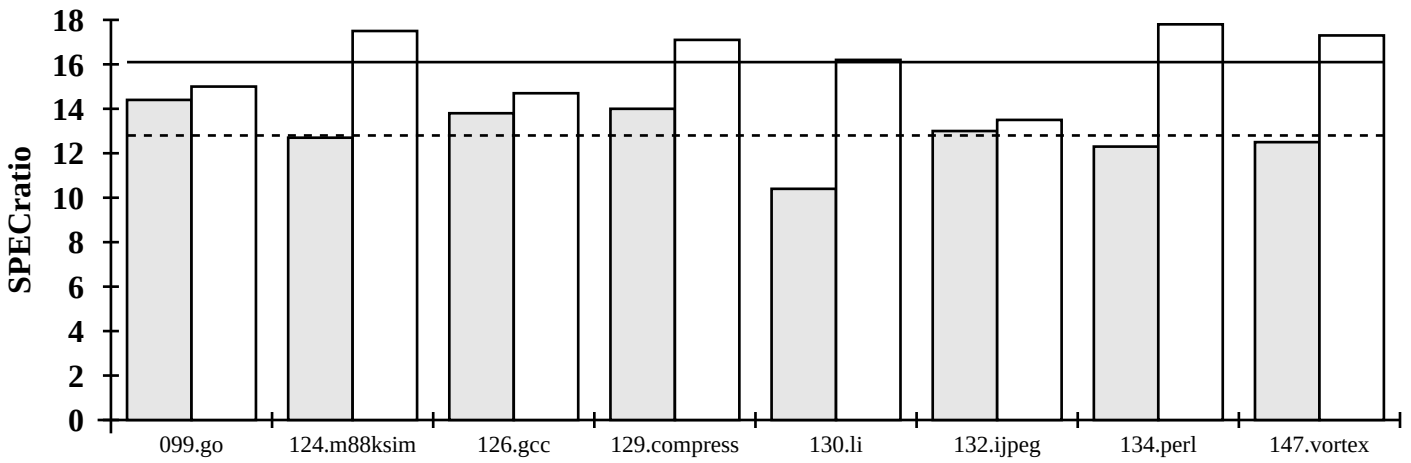
Sun Microsystems
Ultra 60 Model 1360

SPECint95 = 16.1

SPECint_base95 = 12.8

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Apr-98	Hardware Avail: May-98	Software Avail: Oct-98



Hardware/Software Configuration for:
Ultra 60 Model 1360

Hardware

Model Name:	Ultra 60 Model 1360
CPU:	360MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	4MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2x4.0GB
Other Hardware:	Fast Ethernet System clock frequency: 120 MHz

Software

Operating System:	Solaris 2.6 Hardware 3/98
Compiler:	Sun C SC5.0 EFT1 (8/98)
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
099.go	4600	320	14.4	306	15.0
124.m88ksim	1900	150	12.7	109	17.5
126.gcc	1700	123	13.8	115	14.7
129.compress	1800	128	14.0	105	17.1
130.li	1900	183	10.4	118	16.2
132.ijpeg	2400	185	13.0	177	13.5
134.perl	1900	154	12.3	107	17.8
147.vortex	2700	217	12.5	156	17.3
SPECint_base95 (G. Mean)			12.8		
		SPECint95 (G. Mean)			16.1

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback
P2= -xprofile=use:‘pwd‘/../feedback
Baseline: -fast -xO5 -xarch=v8plusa
Baseline: PASS1= ${P1}; PASS2= ${P2}
Peak: -fast -xO4 -xarch=v8plusa -xchip=ultra
099.go: -fast -xO4 -xarch=v8plus -xrestrict -xcrossfile -W2,-O4+ansi_alias+bopt
099.go: PASS1= ${P1}; PASS2= ${P2} ${ALIGN} -dn
124.m88ksim: -fast -xO5 -xsafe=mem -xarch=v8plus -xcrossfile -xrestrict -W2,-O4+cond_elim+pde,-ldstr,-Mt8500 -Wc,-Qiselect-sw_pf_tbl_th=20
124.m88ksim: PASS1= ${P1}; PASS2= ${P2} -dn
126.gcc: -fast -xO4 -xarch=v8plus -W2,-O4+ansi_alias+cond_elim
126.gcc: PASS1= ${P1}; PASS2= ${P2}
129.compress: -fast -xO5 -xsafe=mem -xarch=v8plus -xrestrict -W2,-O4+ansi_alias+pde -Wc,-Qms_pipe+unoovf
129.compress: PASS1= ${P1}; PASS2= ${P2} -dn
130.li: PASS1= -fast -xO4 -xarch=v8plus -xcrossfile ${P1}
130.li: PASS2= -fast -xO5 -xsafe=mem -xarch=v8plus -xcrossfile -W2,-O4+ansi_alias+cond_elim+pde,-Mt350,-whole,-crit ${P2} -dn
132.ijpeg: -fast -xO5 -xsafe=mem  -xarch=v8plus -W2,-O4+ansi_alias+cond_elim
132.ijpeg: PASS1= ${P1}; PASS2= ${P2} -dn
134.perl: -fast -xO4 ${CULTRA} -xcrossfile -W2,-ANSI_S,-Mp1,-O4+cond_elim -Wc,-Qiselect-sw_pf_tbl_th=30
134.perl: PASS1= ${P1}; PASS2= ${P2} -dn
147.vortex: PASS1= -fast -xO4 -xarch=v8plus -xcrossfile ${P1}
147.vortex: PASS2= -fast -xO5 -xsafe=mem -xarch=v8plus -xcrossfile ${P2} -W2,-O4+cond_elim,-Mt500,-Mr6000,-crit -dn
Portability: 124.m88ksim, 132.ijpeg: -DSYSV -Xc, 126.gcc: -Dalloca=__builtin_alloca -Xc
others: -Xc
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Rajaram.Gnanakumar@Eng.Sun.COM
Mon May 11 14:48:00 1998
cpu95-980511-02760.sub
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For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 20109
(703) 331-0180
info@specbench.org
http://www.specbench.org/

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Prepared By: Gnana