# SPEC CFP95rate Results

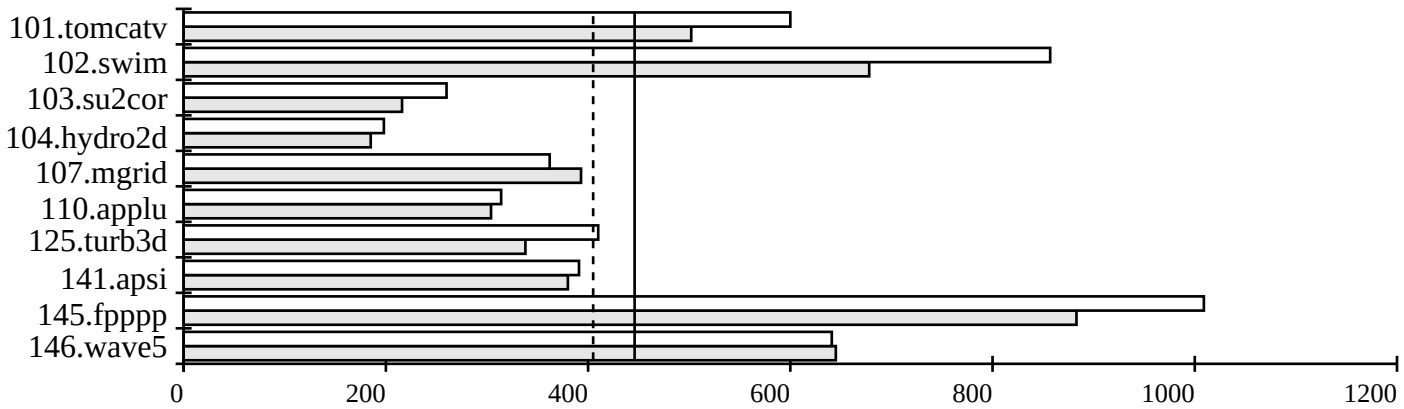
©Copyright 1995, Standard Performance Evaluation Corporation

## SGI origin200

SPECfp_rate95 = 446

SPECfp_rate_base95 = 405

SPEC license # 04 | Tested By: SGI | Test Date: Nov-96 | Hardware Avail: Nov-96 | Software Avail: Nov-96




Hardware/Software Configuration for: origin200

### Hardware

| | |
|---|---|
| Model Name: | origin200 |
| CPU: | 180 MHz MIPS R10000 Processor Chip Revision: 2.6 |
| FPU: | MIPS R10010 Floating Point Chip Revision: 0.0 |
| Number of CPU(s): | 4 |
| Primary Cache: | 32KBI+32KBD |
| Secondary Cache: | 1MB(I+D) |
| Other Cache: | None |
| Memory: | 1024 MB |
| Disk Subsystem: | 1 SCSI 4GB |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | IRIX64 6.4 |
| Compiler: | MipsPRO 7.1 |
| | KAP |
| File System: | XFS |
| System State: | Multi-User |

| Benchmark # and Name | Base Copies | Base Run Time | Base SPEC Ratio | Copies | Run Time | SPEC Ratio |
|---|---|---|---|---|---|---|
| 101.tomcatv | 4 | 265 | 502 | 4 | 222 | 600 |
| 102.swim | 4 | 456 | 678 | 4 | 361 | 857 |
| 103.su2cor | 4 | 234 | 216 | 4 | 194 | 260 |
| 104.hydro2d | 4 | 466 | 185 | 4 | 436 | 198 |
| 107.mgrid | 4 | 229 | 393 | 4 | 249 | 362 |
| 110.applu | 4 | 261 | 304 | 4 | 252 | 314 |
| 125.turb3d | 4 | 437 | 338 | 4 | 360 | 410 |
| 141.apsi | 4 | 199 | 380 | 4 | 193 | 391 |
| 145.fpppp | 4 | 391 | 883 | 4 | 343 | 1009 |
| 146.wave5 | 4 | 168 | 645 | 4 | 168 | 641 |
| SPECfp_rate_base95 (G. Mean) | | | 405 | | | |
| SPECfp_rate95 (G. Mean) | | | | | | 446 |

## Notes/Tuning Information

```
Base Flags:  -Ofast -LNO:fusion=2
Peak Flags:
tomcatv:  -O3 -n32 -mips4 -LNO:prefetch_ahead=1:pf1=off -OPT:IEEE_arithmetic=3:reorg_common=ON -TENV:X=4
swim: -O3 -n32 -mips4 -Wl,-ivpad -LNO:fusion=2:ou_max=1 -TENV:X=4 -OPT:IEEE_arithmetic=3:reorg_common=ON
su2cor: -O3 -n32 -mips4 -TENV:X=3 -OPT:ro=3:IEEE_arith=3:unroll_times_max=2:unroll_size=0:unroll_analysis=off
.       -LNO:prefetch=0:fusion=2
hydro2d: -O3 -mips4 -n32 -TENV:X=4 -OPT:ro=3:IEEE_arith=3:reorg_common=on -LNO:prefetch=2:fusion=2:blocking=off -IPA
mgrid: -O3 -n32 -mips4 -LNO:fusion=2:prefetch=2:prefetch_ahead=1
applu: -O3 -n32 -mips4 -IPA -OPT:ro=3:div_split -LNO:fission=0:fusion=2 -CG:ld_latency=5
turb3d: -O3 -64 -mips4 -OPT:pad_common:ro=3:IEEE_arith=3 -Wl,-ivpad -LNO:pf2=0:interchange=OFF:cs2=4m
.       -OPT:unroll_times_max=2:reorg_common=ON -CG:ld_latency=3
apsi: -O3 -n32 -mips4 -IPA -OPT:div_split -LNO:fusion=2:prefetch=0:interchange=off
fpppp: -O2 -n32 -mips4 -v6 -TARG:processor=t5 -OPT:fold_arith_limit=4000 -WK,-so=1,-r=2,-o=2 -lfastm
wave5: -Ofast -LNO:fusion=2:fission=2:prefetch=1:prefetch_ahead=2 -OPT:reorg_common=on
```

For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 22110
(703) 331-0180
info@specbench.org
http://www.specbench.org