SPEC CINT95rate Results

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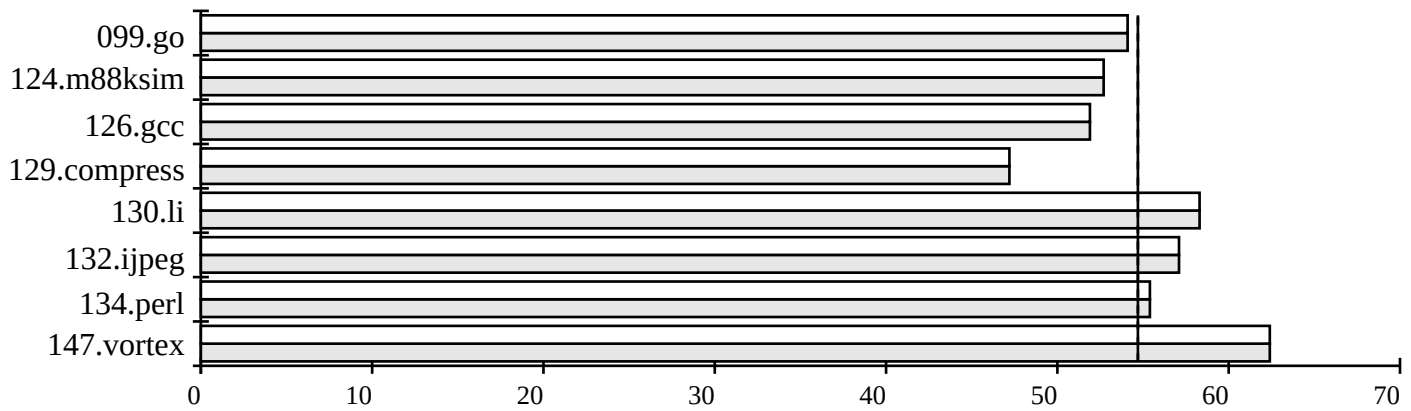
Intel Corporation
Alder System (150MHz)

SPECint_rate95 = 54.7

SPECint_base_rate95 = 54.7

SPEC license # 14 | Tested By: Intel | Test Date: Sep-95 | Hardware Avail: Jan-96 | Software Avail: Feb-96



Hardware/Software Configuration for:
Alder System (150MHz)

Hardware

Model Name:	Alder
CPU:	150Mhz Pentium Pro Processor
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD
Secondary Cache:	256KB(I+D)
Other Cache:	None
Memory:	128MB (60ns fast page)
Disk Subsystem:	2GB ST32550W
Other Hardware:	AHA-2940W Controller

Software

Operating System:	UnixWare 2.0, SDK
Compiler:	Intel C Reference Compiler 2.2 Beta
File System:	ufs, vxfs (/tmp as 8MB /tmpfs)
System State:	Single user (root + killall)

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
099.go	1	766	54.1	1	766	54.1
124.m88ksim	1	325	52.7	1	325	52.7
126.gcc	1	295	51.9	1	295	51.9
129.compress	1	343	47.2	1	343	47.2
130.li	1	293	58.3	1	293	58.3
132.ijpeg	1	378	57.1	1	378	57.1
134.perl	1	309	55.4	1	309	55.4
147.vortex	1	389	62.4	1	389	62.4
SPECint_base_rate95 (G. Mean)			54.7			
SPECint_rate95 (G. Mean)						54.7

Notes/Tuning Information

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Base and non-base flags are the same and use Feedback Directed Optimization
Pass1: -tp p6 -ipo -xi -prof_gen -ircdb_dir /tmp/IRCDB
Pass2: -tp p6 -ipo -xi -prof_use -ircdb_dir /tmp/IRCDB
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
Memory subsystem is four-way interleaved.
```