SPEC CINT95rate Results

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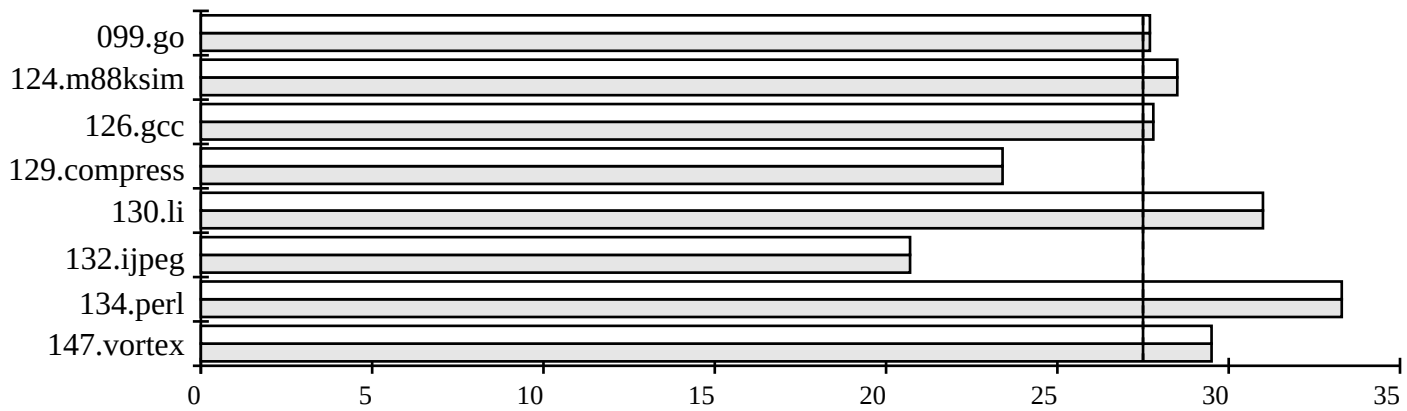
Gateway 2000
Gateway P5-100

SPECint_rate95 = 27.5

SPECint_base_rate95 = 27.5

SPEC license # 14 | Tested By: Intel | Test Date: Sep-95 | Hardware Avail: May-95 | Software Avail: Feb-96



Hardware/Software Configuration for:
Gateway P5-100

Hardware

Model Name:	P5-100
CPU:	100MHz Pentium Processor
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD
Secondary Cache:	256KB(I+D)
Other Cache:	None
Memory:	64MB (EDO)
Disk Subsystem:	2x973MB Seagate ST 31250W
Other Hardware:	AHA 2940W PCI SCSI

Software

Operating System:	UnixWare 2.0, SDK
Compiler:	Intel C Reference Compiler 2.2 Beta
File System:	ufs, vxfs (/tmp as 8MB /tmpfs)
System State:	Single user (root + killall)

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
099.go	1	1496	27.7	1	1496	27.7
124.m88ksim	1	601	28.5	1	601	28.5
126.gcc	1	550	27.8	1	550	27.8
129.compress	1	692	23.4	1	692	23.4
130.li	1	552	31.0	1	552	31.0
132.ijpeg	1	1044	20.7	1	1044	20.7
134.perl	1	514	33.3	1	514	33.3
147.vortex	1	823	29.5	1	823	29.5
SPECint_base_rate95 (G. Mean)			27.5			
SPECint_rate95 (G. Mean)						27.5

Notes/Tuning Information

```
Base and non-base flags are the same and use Feedback Directed Optimization
Pass1: -tp p5 -ipo -prof_gen -ircdb_dir /tmp/IRCDB -dn
Pass2: -tp p5 -ipo -prof_use -ircdb_dir /tmp/IRCDB -dn
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
```

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