SPEC CINT95 Results

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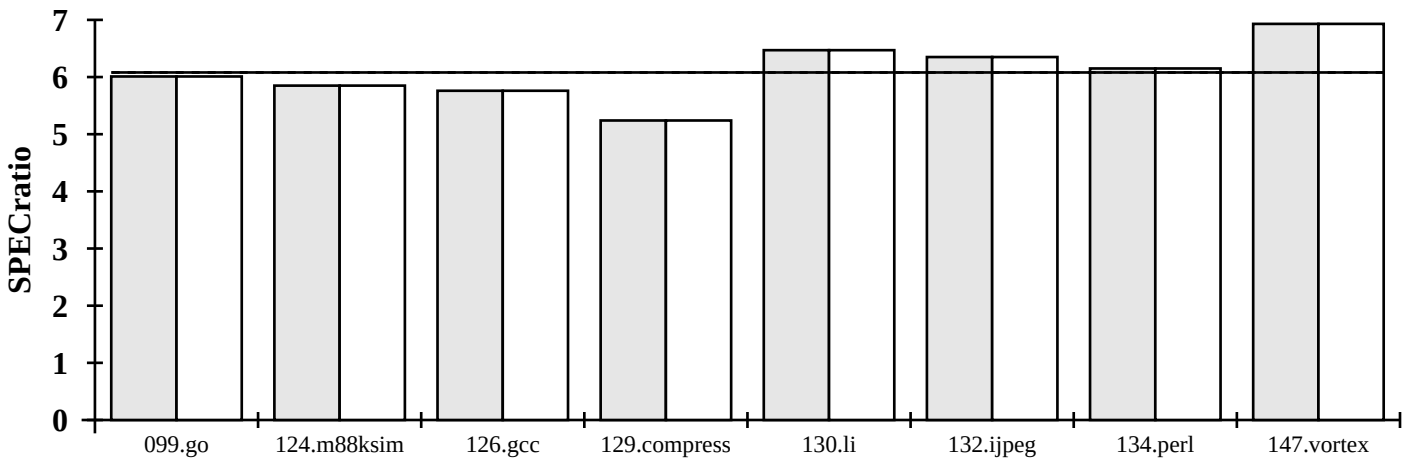
Intel Corporation
Alder System (150MHz)

SPECint95 = 6.08

SPECint_base95 = 6.08

SPEC license # 14	Tested By: Intel	Test Date: Sep-95	Hardware Avail: Jan-96	Software Avail: Feb-96



Hardware/Software Configuration for:
Alder System (150MHz)

Hardware

Model Name:	Alder
CPU:	150Mhz Pentium Pro Processor
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD
Secondary Cache:	256KB(I+D)
Other Cache:	None
Memory:	128MB (60ns fast page)
Disk Subsystem:	2GB ST32550W
Other Hardware:	AHA-2940W Controller

Software

Operating System:	UnixWare 2.0, SDK
Compiler:	Intel C Reference Compiler 2.2 Beta
File System:	ufs, vxfs (/tmp as 8MB /tmpfs)
System State:	Single user (root + killall)

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
099.go	4600	766	6.01	766	6.01
124.m88ksim	1900	325	5.85	325	5.85
126.gcc	1700	295	5.76	295	5.76
129.compress	1800	343	5.24	343	5.24
130.li	1900	293	6.47	293	6.47
132.ijpeg	2400	378	6.35	378	6.35
134.perl	1900	309	6.15	309	6.15
147.vortex	2700	389	6.93	389	6.93
SPECint_base95 (G. Mean)			6.08		
		SPECint95 (G. Mean)			6.08

Notes/Tuning Information

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Base and non-base flags are the same and use Feedback Directed Optimization
Pass1: -tp p6 -ipo -xi -prof_gen -ircdb_dir /tmp/IRCDB
Pass2: -tp p6 -ipo -xi -prof_use -ircdb_dir /tmp/IRCDB
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
Memory subsystem is four-way interleaved.
```

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