SPEC CINT95 Results

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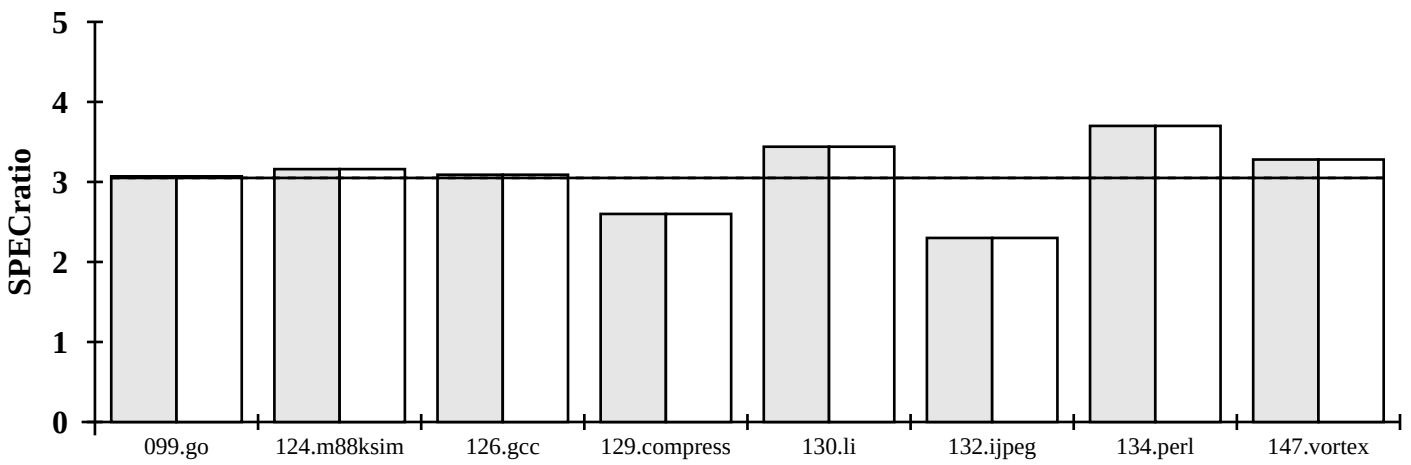
Gateway 2000
Gateway P5-100

SPECint95 = 3.05

SPECint_base95 = 3.05

SPEC license # 14	Tested By: Intel	Test Date: Sep-95	Hardware Avail: May-95	Software Avail: Feb-96



Hardware/Software Configuration for:
Gateway P5-100

Hardware

Model Name:	P5-100
CPU:	100MHz Pentium Processor
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD
Secondary Cache:	256KB(I+D)
Other Cache:	None
Memory:	64MB (EDO)
Disk Subsystem:	2x973MB Seagate ST 31250W
Other Hardware:	AHA 2940W PCI SCSI

Software

Operating System:	UnixWare 2.0, SDK
Compiler:	Intel C Reference Compiler 2.2 Beta
File System:	ufs, vxfs (/tmp as 8MB /tmpfs)
System State:	Single user (root + killall)

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
099.go	4600	1496	3.07	1496	3.07
124.m88ksim	1900	601	3.16	601	3.16
126.gcc	1700	550	3.09	550	3.09
129.compress	1800	692	2.60	692	2.60
130.li	1900	552	3.44	552	3.44
132.ijpeg	2400	1044	2.30	1044	2.30
134.perl	1900	514	3.70	514	3.70
147.vortex	2700	823	3.28	823	3.28
SPECint_base95 (G. Mean)			3.05		
		SPECint95 (G. Mean)			3.05

Notes/Tuning Information

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Base and non-base flags are the same and use Feedback Directed Optimization
Pass1: -tp p5 -ipo -prof_gen -ircdb_dir /tmp/IRCDB -dn
Pass2: -tp p5 -ipo -prof_use -ircdb_dir /tmp/IRCDB -dn
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
```