



SPEChpc™ 2021 Small Result

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Intel

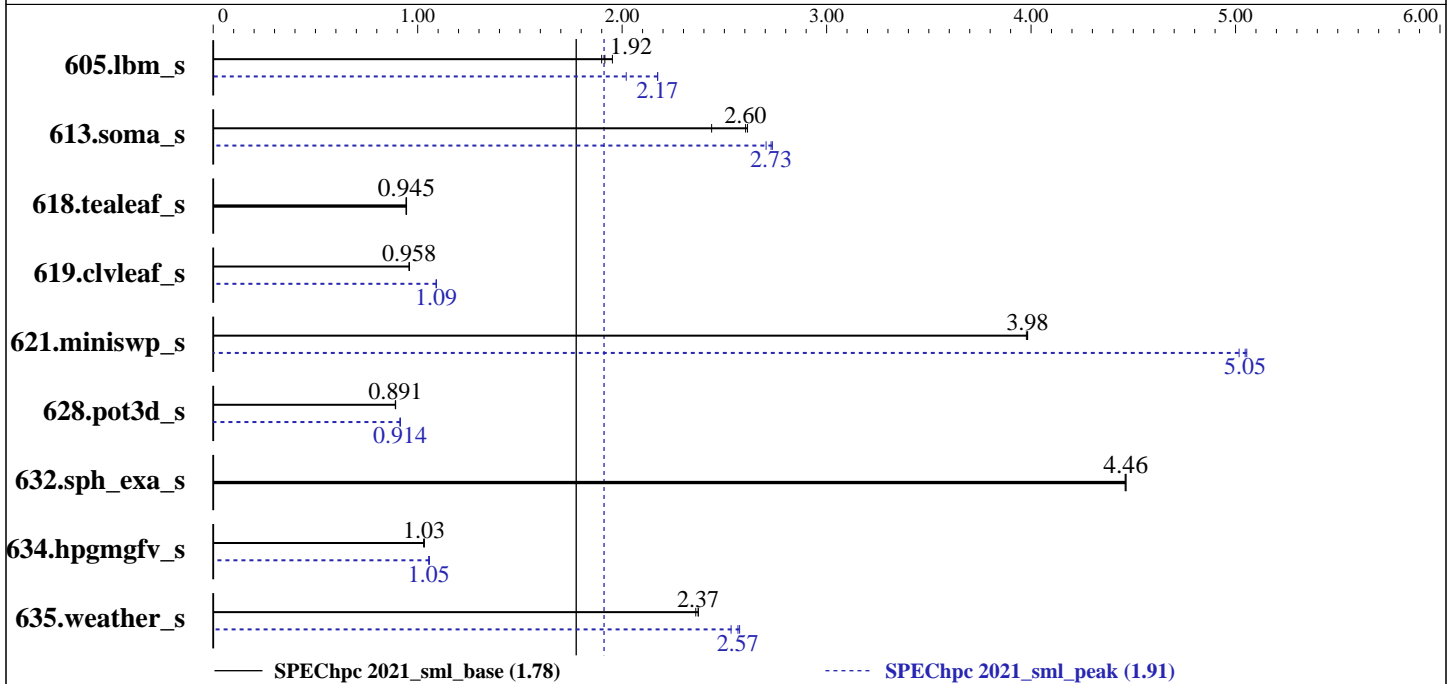
Endeavour: Intel Server D50DNP2MFALACB (Intel Xeon Platinum 8480+)

SPEChpc 2021_sml_base = 1.78

SPEChpc 2021_sml_peak = 1.91

hpc2021 License: 13
Test Sponsor: Intel
Tested by: Intel

Test Date: Jul-2023
Hardware Availability: Jan-2023
Software Availability: Apr-2023



Results Table

Benchmark	Base									Peak								
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
605.lbm_s	OMP	32	7	794	1.95	816	1.90	809	1.92	OMP	32	7	767	2.02	713	2.17	713	2.17
613.soma_s	OMP	32	7	612	2.61	614	2.60	656	2.44	OMP	8	28	586	2.73	585	2.73	592	2.70
618.tealeaf_s	OMP	32	7	2174	0.943	2168	0.946	2169	0.945	OMP	32	7	2174	0.943	2168	0.946	2169	0.945
619.civleaf_s	OMP	32	7	1722	0.958	1722	0.958	1720	0.959	OMP	32	7	1513	1.09	1512	1.09	1512	1.09
621.miniswp_s	OMP	32	7	276	3.98	276	3.98	277	3.98	OMP	8	28	219	5.02	218	5.06	218	5.05
628.pot3d_s	OMP	32	7	1879	0.891	1881	0.891	1877	0.892	OMP	112	2	1833	0.914	1833	0.914	1829	0.916
632.sph_exa_s	OMP	32	7	515	4.46	515	4.46	516	4.46	OMP	32	7	515	4.46	515	4.46	516	4.46
634.hpgmgfv_s	OMP	32	7	945	1.03	945	1.03	947	1.03	OMP	32	7	922	1.06	924	1.05	925	1.05
635.weather_s	OMP	32	7	1096	2.37	1102	2.36	1096	2.37	OMP	32	7	1010	2.57	1013	2.57	1027	2.53

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Hardware Summary

Type of System: Homogenous Cluster
Compute Node: Intel Server D50DNP2MFALACB (Xeon 8480+)
Interconnect: Mellanox HDR
Compute Nodes Used: 2
Total Chips: 4
Total Cores: 224
Total Threads: 448
Total Memory: 1 TB
Max. Peak Threads: 28

Software Summary

Compiler: Intel oneAPI Compiler 2023.1.0
MPI Library: Intel MPI Library 2021.9 for Linux OS
Other MPI Info: None
Other Software: None
Base Parallel Model: OMP
Base Ranks Run: 32
Base Threads Run: 7
Peak Parallel Models: OMP
Minimum Peak Ranks: 8
Maximum Peak Ranks: 112
Max. Peak Threads: 28
Min. Peak Threads: 2

Node Description: Intel Server D50DNP2MFALACB (Xeon 8480+)

Hardware

Number of nodes: 2
Uses of the node: Compute
Vendor: Intel
Model: Intel Server D50DNP2MFALACB (Xeon 8480+)
CPU Name: Intel Xeon Platinum 8480+
CPU(s) orderable: 1, 2 chips
Chips enabled: 2
Cores enabled: 112
Cores per chip: 56
Threads per core: 2
CPU Characteristics: Turbo Boost Technology up to 3.8 GHz
CPU MHz: 2000
Primary Cache: 32 KB I + 48 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per core
L3 Cache: 105 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B)
Disk Subsystem: 1 x 1 TB NVMe U.2 2.5" SSD
Other Hardware: None
Accel Count: None
Accel Model: None
Accel Vendor: None
Accel Type: None
Accel Connection: None
Accel ECC enabled: None
Accel Description: None
Adapter: Mellanox ConnectX-6 HDR
Number of Adapters: 1
Slot Type: PCI-Express 4.0 x16
Data Rate: 200Gbit/s
Ports Used: 1
Interconnect Type: Mellanox HDR

Software

Accelerator Driver: None
Adapter: Mellanox ConnectX-6 HDR
Adapter Driver: 5.9-0.5.5
Adapter Firmware: 20.36.1010
Operating System: Rocky Linux 8.7 (Green Obsidian)
4.18.0-372.32.1.el8_6.crt3.x86_64
Local File System: NFS
Shared File System: PANASAS FS
System State: Multi-user
Other Software: None



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Interconnect Description: Mellanox HDR

Hardware

Software

Vendor: Mellanox
Model: Mellanox HDR
Switch Model: Mellanox MQM8790-HS2F Quantum HDR InfiniBand Switch
Number of Switches: 18
Number of Ports: 40
Data Rate: 200 Gbit/s
Firmware: 20.36.1010
Topology: Fat-tree
Primary Use: MPI Traffic

: --

Submit Notes

The config file option 'submit' was used.

General Notes

The PANASAS filesystem as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC HPG Policy document, <http://www.spec.org/hpg/policy.html>

Compiler Version Notes

=====
CXXC 632.sph_exa_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler 2023.1.0 (2023.1.0.20230320)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir:
/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm
Configuration file:
/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm/./bin/icpx.cfg

=====
CC 605.lbm_s(base, peak) 613.soma_s(base, peak) 618.tealeaf_s(base, peak)
621.miniswp_s(base) 634.hpgmgfv_s(base)

Intel(R) oneAPI DPC++/C++ Compiler 2023.1.0 (2023.1.0.20230320)
Target: x86_64-unknown-linux-gnu

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Compiler Version Notes (Continued)

Thread model: posix
InstalledDir:

/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm
Configuration file:
/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm/./bin/icx.cfg

=====
CC 621.miniswp_s(peak) 634.hpgmgfv_s(peak)
=====

icc: remark #10441: The Intel(R) C++ Compiler Classic (ICC) is deprecated and will be removed from product release in the second half of 2023. The Intel(R) oneAPI DPC++/C++ Compiler (ICX) is the recommended compiler moving forward. Please transition to use this compiler. Use '-diag-disable=10441' to disable this message.

icc: command line warning #10148: option '-Wno-incompatible-function-pointer-types' not supported

icc (ICC) 2021.9.0 20230302
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
FC 619.clvleaf_s(peak)
=====

ifort (IFORT) 2021.9.0 20230302
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
FC 619.clvleaf_s(base) 628.pot3d_s(base, peak) 635.weather_s(base, peak)
=====

ifx (IFX) 2023.1.0 20230320
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

mpiicc -cc=icx

C++ benchmarks:

mpicpc -cxx=icpx

Fortran benchmarks:

mpiifort -fc=ifx



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Base Portability Flags

```
605.lbm_s: -lstdc++ -std=c++14
613.soma_s: -lstdc++ -std=c++14
618.tealeaf_s: -lstdc++ -std=c++14
621.miniswp_s: -lstdc++ -std=c++14
634.hpgmgfv_s: -lstdc++ -std=c++14
```

Base Optimization Flags

C benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops
```

C++ benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops
```

Fortran benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops -nostandard-realloc-lhs -align array64byte
```

Base Other Flags

C benchmarks:

```
-Ispecmpitime -Wno-incompatible-function-pointer-types
```

C++ benchmarks:

```
-Ispecmpitime
```

Fortran benchmarks:

```
619.clvleaf_s: -Ispecmpitime
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
mpicc -cc=icx
```

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Peak Compiler Invocation (Continued)

621.miniswp_s: mpiicc

634.hpgmgfv_s: mpiicc

C++ benchmarks:

mpiicpc -cxx=icpx

Fortran benchmarks (except as noted below):

mpiifort -fc=ifx

619.clvleaf_s: mpiifort

Peak Portability Flags

605.lbm_s: -lstdc++ -std=c++14

613.soma_s: -lstdc++ -std=c++14

618.tealeaf_s: -lstdc++ -std=c++14

621.miniswp_s: -lstdc++ -std=c++14

634.hpgmgfv_s: -lstdc++ -std=c++14

Peak Optimization Flags

C benchmarks:

605.lbm_s: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512

-qopt-multiple-gather-scatter-by-shuffles -fiopenmp

-ffast-math -flto -funroll-loops

-qopt-streaming-stores=always

613.soma_s: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512

-qopt-multiple-gather-scatter-by-shuffles -fiopenmp

-ffast-math -flto -funroll-loops

618.tealeaf_s: basepeak = yes

621.miniswp_s: -O3 -Ofast -xCORE-AVX512 -ansi-alias -qopenmp -ipo

-qopt-zmm-usage=high

-qopt-multiple-gather-scatter-by-shuffles

634.hpgmgfv_s: Same as 621.miniswp_s

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Peak Optimization Flags (Continued)

C++ benchmarks:

632.sph_exa_s: basepeak = yes

Fortran benchmarks:

619.clvleaf_s: -O3 -Ofast -xCORE-AVX512 -ansi-alias -qopenmp -ipo
-qopt-zmm-usage=high
-qopt-multiple-gather-scatter-by-shuffles
-nostandard-realloc-lhs -align array64byte

628.pot3d_s: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp
-ffast-math -flto -funroll-loops -nostandard-realloc-lhs
-align array64byte

635.weather_s: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp
-ffast-math -flto -funroll-loops
-qopt-streaming-stores=always -nostandard-realloc-lhs
-align array64byte

Peak Other Flags

C benchmarks:

-Ispecmpitime -Wno-incompatible-function-pointer-types

C++ benchmarks:

-Ispecmpitime

Fortran benchmarks:

619.clvleaf_s: -Ispecmpitime

The flags file that was used to format this result can be browsed at

http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2023-08-16.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2023-08-16.xml



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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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