



SPEChpc™ 2021 Tiny Result

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Intel

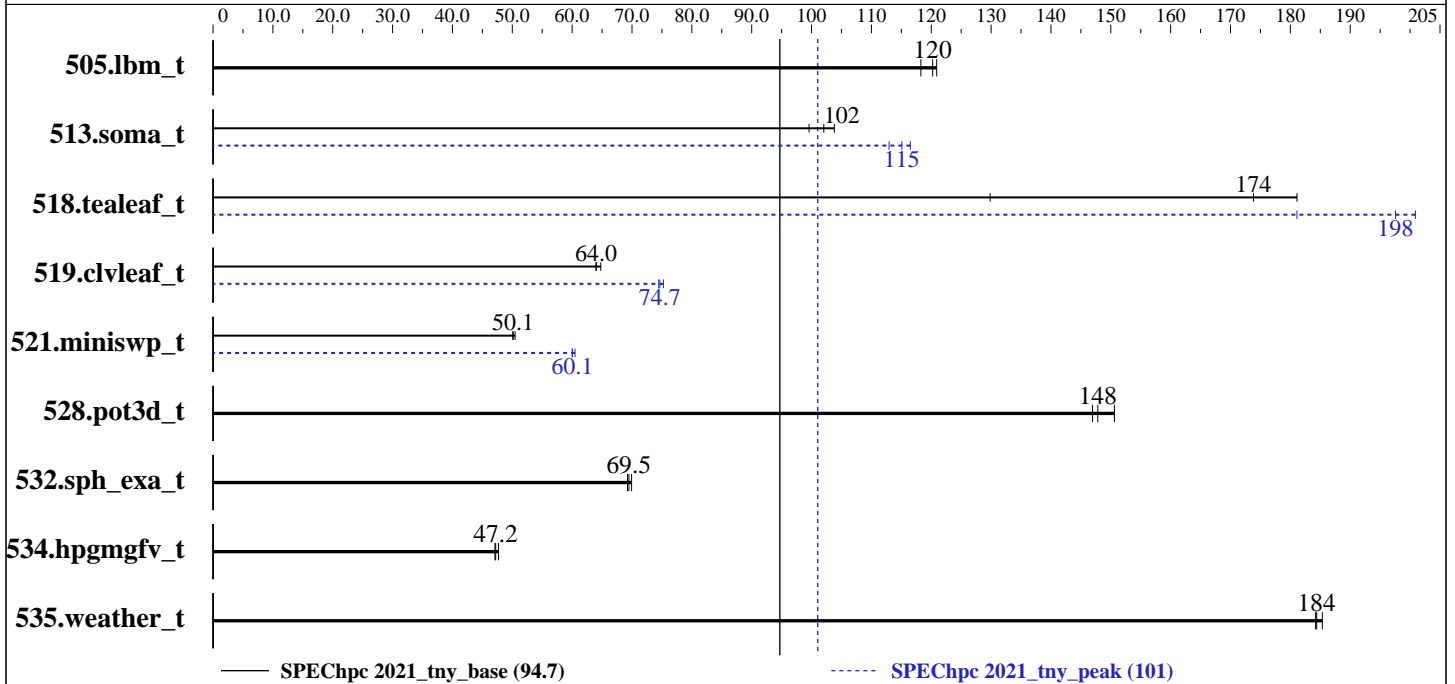
Endeavour: Intel Server D50DNP2MFALACB (Intel Xeon Platinum 8480+)

SPEChpc 2021_tny_base = 94.7

SPEChpc 2021_tny_peak = 101

hpc2021 License: 13
Test Sponsor: Intel
Tested by: Intel

Test Date: Jul-2023
Hardware Availability: Jan-2023
Software Availability: Apr-2023



Results Table

Benchmark	Base								Peak									
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
505.lbm_t	OMP	128	14	19.0	118	18.6	121	18.7	120	OMP	128	14	19.0	118	18.6	121	18.7	120
513.soma_t	OMP	128	14	37.2	99.6	35.6	104	36.3	102	OMP	64	28	31.8	117	32.8	113	32.1	115
518.tealeaf_t	OMP	128	14	9.11	181	12.7	130	9.49	174	OMP	64	28	9.11	181	8.21	201	8.35	198
519.civleaf_t	OMP	128	14	25.5	64.8	25.8	64.0	25.8	64.0	OMP	64	28	21.9	75.3	22.2	74.5	22.1	74.7
521.miniswp_t	OMP	128	14	31.7	50.5	31.9	50.1	31.9	50.1	OMP	64	28	26.5	60.5	26.6	60.1	26.7	60.0
528.pot3d_t	OMP	128	14	14.1	151	14.5	147	14.4	148	OMP	128	14	14.1	151	14.5	147	14.4	148
532.sph_exa_t	OMP	128	14	27.9	69.9	28.1	69.5	28.2	69.2	OMP	128	14	27.9	69.9	28.1	69.5	28.2	69.2
534.hpgmgfv_t	OMP	128	14	24.6	47.7	24.9	47.2	24.9	47.1	OMP	128	14	24.6	47.7	24.9	47.2	24.9	47.1
535.weather_t	OMP	128	14	17.4	185	17.5	184	17.5	184	OMP	128	14	17.4	185	17.5	184	17.5	184

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Hardware Summary

Type of System: Homogenous Cluster
Compute Node: Intel Server D50DNP2MFALACB (Xeon 8480+)
Interconnect: Mellanox HDR
Compute Nodes Used: 16
Total Chips: 32
Total Cores: 1792
Total Threads: 3584
Total Memory: 8 TB
Max. Peak Threads: 28

Software Summary

Compiler: Intel oneAPI Compiler 2023.1.0
MPI Library: Intel MPI Library 2021.9 for Linux OS
Other MPI Info: None
Other Software: None
Base Parallel Model: OMP
Base Ranks Run: 128
Base Threads Run: 14
Peak Parallel Models: OMP
Minimum Peak Ranks: 64
Maximum Peak Ranks: 128
Max. Peak Threads: 28
Min. Peak Threads: 14

Node Description: Intel Server D50DNP2MFALACB (Xeon 8480+)

Hardware

Number of nodes: 16
Uses of the node: Compute
Vendor: Intel
Model: Intel Server D50DNP2MFALACB (Xeon 8480+)
CPU Name: Intel Xeon Platinum 8480+
CPU(s) orderable: 1, 2 chips
Chips enabled: 2
Cores enabled: 112
Cores per chip: 56
Threads per core: 2
CPU Characteristics: Turbo Boost Technology up to 3.8 GHz
CPU MHz: 2000
Primary Cache: 32 KB I + 48 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per core
L3 Cache: 105 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B)
Disk Subsystem: 1 x 1 TB NVMe U.2 2.5" SSD
Other Hardware: None
Accel Count: None
Accel Model: None
Accel Vendor: None
Accel Type: None
Accel Connection: None
Accel ECC enabled: None
Accel Description: None
Adapter: Mellanox ConnectX-6 HDR
Number of Adapters: 1
Slot Type: PCI-Express 4.0 x16
Data Rate: 200Gbit/s
Ports Used: 1
Interconnect Type: Mellanox HDR

Software

Accelerator Driver: None
Adapter: Mellanox ConnectX-6 HDR
Adapter Driver: 5.9-0.5.5
Adapter Firmware: 20.36.1010
Operating System: Rocky Linux 8.7 (Green Obsidian)
4.18.0-372.32.1.el8_6.crt3.x86_64
Local File System: NFS
Shared File System: PANASAS FS
System State: Multi-user
Other Software: None



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Interconnect Description: Mellanox HDR

Hardware

Software

Vendor: Mellanox
Model: Mellanox HDR
Switch Model: Mellanox MQM8790-HS2F Quantum HDR InfiniBand Switch
Number of Switches: 18
Number of Ports: 40
Data Rate: 200 Gbit/s
Firmware: 20.36.1010
Topology: Fat-tree
Primary Use: MPI Traffic

: --

Submit Notes

The config file option 'submit' was used.

General Notes

The PANASAS filesystem as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC HPG Policy document, <http://www.spec.org/hpg/policy.html>

Compiler Version Notes

=====
CXXC 532.sph_exa_t(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler 2023.1.0 (2023.1.0.20230320)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir:
/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm
Configuration file:
/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm/./bin/icpx.cfg

=====
CC 505.lbm_t(base, peak) 513.soma_t(base, peak) 518.tealeaf_t(base, peak)
521.miniswp_t(base, peak) 534.hpgmgfv_t(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler 2023.1.0 (2023.1.0.20230320)
Target: x86_64-unknown-linux-gnu

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Compiler Version Notes (Continued)

Thread model: posix
InstalledDir:

/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm
Configuration file:
/global/panfs01/admin/opt/intel/oneAPI/2023.1.0.46401/compiler/2023.1.0/linux/bin-llvm/./bin/icx.cfg

=====
FC 519.clvleaf_t(peak)
=====

ifort (IFORT) 2021.9.0 20230302
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

=====
FC 519.clvleaf_t(base) 528.pot3d_t(base, peak) 535.weather_t(base, peak)
=====

ifx (IFX) 2023.1.0 20230320
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

Base Compiler Invocation

C benchmarks:
mpiicc -cc=icx

C++ benchmarks:
mpiicpc -cxx=icpx

Fortran benchmarks:
mpiifort -fc=ifx

Base Portability Flags

505.lbm_t: -lstdc++ -std=c++14
513.soma_t: -lstdc++ -std=c++14
518.tealeaf_t: -lstdc++ -std=c++14
521.miniswp_t: -lstdc++ -std=c++14
534.hpgmgfv_t: -lstdc++ -std=c++14



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Base Optimization Flags

C benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-gopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops
```

C++ benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-gopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops
```

Fortran benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-gopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops -nostandard-realloc-lhs -align array64byte
```

Base Other Flags

C benchmarks (except as noted below):

```
-Ispecmpitime -Wno-incompatible-function-pointer-types
```

```
521.miniswp_t: -Ispecmpitime/ -Wno-incompatible-function-pointer-types
```

```
534.hpgmgfv_t: -Ispecmpitime -Wno-incompatible-function-pointer-types
```

C++ benchmarks:

```
-Ispecmpitime
```

Fortran benchmarks:

```
519.clvleaf_t: -Ispecmpitime
```

Peak Compiler Invocation

C benchmarks:

```
mpiicc -cc=icx
```

C++ benchmarks:

```
mpicpc -cxx=icpx
```

Fortran benchmarks (except as noted below):

```
mpiifort -fc=ifx
```

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Peak Compiler Invocation (Continued)

519.clvleaf_t: mpiifort

Peak Portability Flags

505.lbm_t: -lstdc++ -std=c++14
513.soma_t: -lstdc++ -std=c++14
518.tealeaf_t: -lstdc++ -std=c++14
521.miniswp_t: -lstdc++ -std=c++14
534.hpgmgfv_t: -lstdc++ -std=c++14

Peak Optimization Flags

C benchmarks:

505.lbm_t: basepeak = yes

513.soma_t: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp
-ffast-math -flto -funroll-loops

518.tealeaf_t: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp
-ffast-math -flto -funroll-loops
-qopt-streaming-stores=always

521.miniswp_t: Same as 513.soma_t

534.hpgmgfv_t: basepeak = yes

C++ benchmarks:

532.sph_exa_t: basepeak = yes

Fortran benchmarks:

519.clvleaf_t: -O3 -Ofast -xCORE-AVX512 -ansi-alias -qopenmp -ipo
-qopt-zmm-usage=high
-qopt-multiple-gather-scatter-by-shuffles
-nostandard-realloc-lhs -align array64byte

528.pot3d_t: basepeak = yes

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Peak Optimization Flags (Continued)

535.weather_t: basepeak = yes

Peak Other Flags

C benchmarks (except as noted below):

-Ispecmpitime -Wno-incompatible-function-pointer-types

521.miniswp_t: -Ispecmpitime/ -Wno-incompatible-function-pointer-types

534.hpgmgfv_t: -Ispecmpitime -Wno-incompatible-function-pointer-types

C++ benchmarks:

-Ispecmpitime

Fortran benchmarks:

519.clvleaf_t: -Ispecmpitime

The flags file that was used to format this result can be browsed at

http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2023-08-16.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2023-08-16.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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