SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

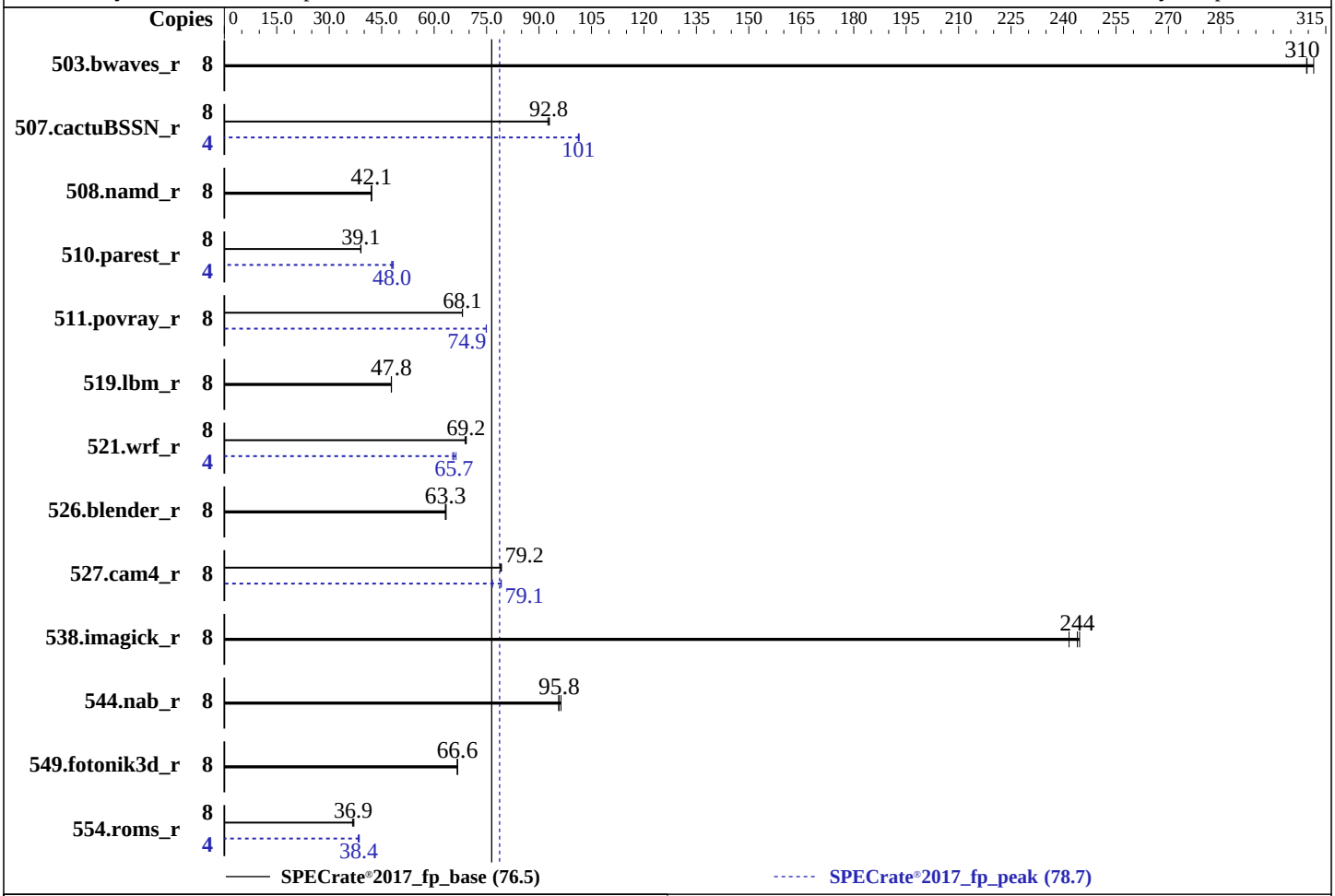
Express5800/R110m-1 (Intel Xeon 6325P)

SPECrate®2017_fp_base = 76.5

SPECrate®2017_fp_peak = 78.7

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Jul-2025
Hardware Availability: Jun-2025
Software Availability: Apr-2024



Hardware

CPU Name:	Intel Xeon 6325P
Max MHz:	5200
Nominal:	3500
Enabled:	4 cores, 1 chip, 2 threads/core
Orderable:	1 chip
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	12 MB I+D on chip per chip
Other:	None
Memory:	64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 4400)
Storage:	1 x 1.92 TB SATA, SSD
Other:	CPU Cooling: Air

Software

OS:	Red Hat Enterprise Linux 9.4 (Plow) 5.14.0-427.13.1.el9_4.x86_64
Compiler:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	NEC BIOS Version U65 v2.10 12/06/2024 released Jun-2025
File System:	ext4
System State:	Run level 5 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS set to prefer performance at the cost of additional power usage

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R110m-1 (Intel Xeon 6325P)

SPECrate®2017_fp_base = 76.5

SPECrate®2017_fp_peak = 78.7

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Jul-2025
Hardware Availability: Jun-2025
Software Availability: Apr-2024

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	8	257	312	**259**	**310**	259	310	8	257	312	**259**	**310**	259	310
507.cactuBSSN_r	8	109	93.0	**109**	**92.8**	109	92.5	4	**50.0**	**101**	49.9	101	50.0	101
508.namd_r	8	**181**	**42.1**	181	42.0	180	42.1	8	**181**	**42.1**	181	42.0	180	42.1
510.parest_r	8	537	39.0	535	39.1	**536**	**39.1**	4	**218**	**48.0**	216	48.3	219	47.9
511.povray_r	8	274	68.1	274	68.1	**274**	**68.1**	8	**249**	**74.9**	249	75.0	249	74.9
519.lbm_r	8	177	47.7	**177**	**47.8**	176	47.8	8	177	47.7	**177**	**47.8**	176	47.8
521.wrf_r	8	260	68.8	259	69.2	**259**	**69.2**	4	135	66.3	**136**	**65.7**	137	65.3
526.blender_r	8	193	63.2	192	63.4	**193**	**63.3**	8	193	63.2	192	63.4	**193**	**63.3**
527.cam4_r	8	177	79.3	177	78.9	**177**	**79.2**	8	182	76.7	177	79.2	**177**	**79.1**
538.imagick_r	8	82.4	242	**81.6**	**244**	81.3	245	8	82.4	242	**81.6**	**244**	81.3	245
544.nab_r	8	140	96.3	**141**	**95.8**	141	95.6	8	140	96.3	**141**	**95.8**	141	95.6
549.fotonik3d_r	8	**468**	**66.6**	468	66.7	468	66.6	8	**468**	**66.6**	468	66.7	468	66.6
554.roms_r	8	343	37.1	**345**	**36.9**	347	36.7	4	165	38.6	166	38.3	**165**	**38.4**

SPECrate®2017_fp_base = 76.5

SPECrate®2017_fp_peak = 78.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon 6369P CPU + 64GB RAM
 memory using Red Hat Enterprise Linux 9.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
```

(Continued on next page)

General Notes (Continued)

```
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
 BIOS Settings:
  Workload Profile set to General Throughput Compute
  Thermal configuration set to Maximum Cooling
  Enhanced Processor Performance Profile set to Enabled
  Workload Profile set to Custom
  Minimum Processor Idle Power Package C-State set to Package C6 (non-retention) State

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost.localdomain Thu Jul  3 19:46:44 2025

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 252 (252-32.el9_4)
 12. Failed units, from systemctl list-units --state=failed
 13. Services, from systemctl list-unit-files
 14. Linux kernel boot-time arguments, from /proc/cmdline
 15. cpupower frequency-info
 16. tuned-adm active
 17. sysctl
 18. /sys/kernel/mm/transparent_hugepage
 19. /sys/kernel/mm/transparent_hugepage/khugepaged
 20. OS release
 21. Disk information
 22. /sys/devices/virtual/dmi/id
 23. dmidecode
 24. BIOS
 ------------------------------------------------------------

 ------------------------------------------------------------
 1. uname -a
   Linux localhost.localdomain 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT
   2024 x86_64 x86_64 x86_64 GNU/Linux

 ------------------------------------------------------------
 2. w
    19:46:44 up 2 min,  2 users,  load average: 0.08, 0.04, 0.01
   USER     TTY        LOGIN@   IDLE   JCPU   PCPU WHAT
   root     seat0     19:45    0.00s  0.00s  0.00s /usr/libexec/gdm-wayland-session --register-session
```

(Continued on next page)

Platform Notes (Continued)

```
  gnome-session
  root     tty2      19:45    2:26   0.01s  0.01s /usr/libexec/gnome-session-binary

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  real-time non-blocking time  (microseconds, -R) unlimited
  core file size              (blocks, -c) 0
  data seg size               (kbytes, -d) unlimited
  scheduling priority                 (-e) 0
  file size                   (blocks, -f) unlimited
  pending signals                     (-i) 254521
  max locked memory           (kbytes, -l) 8192
  max memory size             (kbytes, -m) unlimited
  open files                          (-n) 1024
  pipe size                (512 bytes, -p) 8
  POSIX message queues         (bytes, -q) 819200
  real-time priority                  (-r) 0
  stack size                  (kbytes, -s) unlimited
  cpu time                   (seconds, -t) unlimited
  max user processes                  (-u) 254521
  virtual memory              (kbytes, -v) unlimited
  file locks                          (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
 /usr/lib/systemd/systemd --user
 /usr/libexec/gnome-terminal-server
 bash
 bash
 runcpu --nobuild --action validate --flagsurl=Intel-ic2024-official-linux64.xml,Default-Platform-Flags.xml
   --define numcopies=8 -c ic2024.1-lin-core-avx2-rate-20240308n.cfg --define smt-on --define cores=4
   --define physicalfirst --define no-numa --define drop_caches --tune base,peak -o all --nopower --runmode
   rate --tune base:peak --size refrate fprate
 runcpu --nobuild --action validate --flagsurl Intel-ic2024-official-linux64.xml,Default-Platform-Flags.xml
   --define numcopies=8 --configfile ic2024.1-lin-core-avx2-rate-20240308n.cfg --define smt-on --define
   cores=4 --define physicalfirst --define no-numa --define drop_caches --tune base,peak --output_format all
   --nopower --runmode rate --tune base:peak --size refrate --nopower --runmode rate --tune base:peak --size
   refrate fprate --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.022/templogs/preenv.fprate.022.0.log
   --lognum 022.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) 6325P
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 183
    stepping        : 1
    microcode       : 0x12c
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 4
    siblings        : 8
    1 physical ids (chips)
    8 processors (hardware threads)
```

(Continued on next page)

Platform Notes (Continued)

```
    physical id 0: core ids 0-3
    physical id 0: apicids 0-7
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                       x86_64
  CPU op-mode(s):                     32-bit, 64-bit
  Address sizes:                      46 bits physical, 48 bits virtual
  Byte Order:                         Little Endian
  CPU(s):                             8
  On-line CPU(s) list:                0-7
  Vendor ID:                          GenuineIntel
  BIOS Vendor ID:                     Intel(R) Corporation
  Model name:                         Intel(R) Xeon(R) 6325P
  BIOS Model name:                    Intel(R) Xeon(R) 6325P
  CPU family:                         6
  Model:                              183
  Thread(s) per core:                 2
  Core(s) per socket:                 4
  Socket(s):                          1
  Stepping:                           1
  BogoMIPS:                           6988.80
  Flags:                              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                                      rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                                      xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq
                                      dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                                      sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
                                      rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd ibrs ibpb stibp
                                      ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase
                                      tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt
                                      clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect
                                      avx_vnni dtherm ida arat pln pts hfi vnmi umip pku ospke waitpkg gfni
                                      vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize
                                      pconfig arch_lbr ibt flush_l1d arch_capabilities
  Virtualization:                     VT-x
  L1d cache:                          192 KiB (4 instances)
  L1i cache:                          128 KiB (4 instances)
  L2 cache:                           8 MiB (4 instances)
  L3 cache:                           12 MiB (1 instance)
  NUMA node(s):                       1
  NUMA node0 CPU(s):                  0-7
  Vulnerability Gather data sampling: Not affected
  Vulnerability Itlb multihit:        Not affected
  Vulnerability L1tf:                 Not affected
  Vulnerability Mds:                  Not affected
  Vulnerability Meltdown:             Not affected
  Vulnerability Mmio stale data:      Not affected
  Vulnerability Retbleed:             Not affected
  Vulnerability Spec rstack overflow: Not affected
  Vulnerability Spec store bypass:    Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:           Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:           Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
                                      PBRSB-eIBRS SW sequence
  Vulnerability Srbds:                Not affected
  Vulnerability Tsx async abort:      Not affected
```

(Continued on next page)

Platform Notes (Continued)

```
From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     192K   12 Data            1    64        1             64
     L1i       32K     128K    8 Instruction     1    64        1             64
     L2         2M       8M   16 Unified         2  2048        1             64
     L3        12M      12M    6 Unified         3 32768        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-7
  node 0 size: 63690 MB
  node 0 free: 62256 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       65218744 kB

------------------------------------------------------------
10. who -r
  run-level 5 Jul 3 19:44

------------------------------------------------------------
11. Systemd service manager version: systemd 252 (252-32.el9_4)
  Default Target  Status
  graphical       degraded

------------------------------------------------------------
12. Failed units, from systemctl list-units --state=failed
    UNIT         LOAD   ACTIVE SUB    DESCRIPTION
  * sep5.service loaded failed failed systemd script to load sep5 driver at boot time

------------------------------------------------------------
13. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
                   accounts-daemon ahslog amsd atd auditd avahi-daemon bluetooth chronyd cpqFca cpqIde
                   cpqScsi crond cups dbus-broker firewalld gdm getty@ insights-client-boot irqbalance
                   iscsi-onboot iscsi-starter kdump libstoragemgmt lm_sensors low-memory-monitor lvm2-monitor
                   mcelog mdmonitor microcode mr_cpqScsi multipathd nis-domainname nvmefc-boot-connections
                   ostree-remount pmcd pmie pmlogger power-profiles-daemon qemu-guest-agent rhsmcertd rpcbind
                   rsyslog rtkit-daemon selinux-autorelabel-mark sep5 smad smartd sshd sssd
                   switcheroo-control sysstat systemd-boot-update systemd-network-generator tuned udisks2
                   upower vgauthd vmtoolsd
  enabled-runtime  systemd-fsck-root systemd-remount-fs
  disabled         amsd_rev arp-ethers autofs blk-availability brltty canberra-system-bootup
                   canberra-system-shutdown canberra-system-shutdown-reboot chrony-wait chronyd-restricted
                   cni-dhcp console-getty cpqFca_rev cpqIde_rev cpqScsi_rev cpqiScsi cpupower cups-browsed
                   dbus-daemon debug-shell dnf-system-upgrade dnsmasq dovecot fancontrol fcoe grafana-server
                   gssproxy iprdump iprinit iprupdate ipsec iscsi-init iscsid iscsiuio kpatch kvm_stat ledmon
                   lldpad man-db-restart-cache-update mr_cpqScsi_rev netavark-dhcp-proxy
                   netavark-firewalld-reload nfs-blkmap nfs-server nftables nmb nvmf-autoconnect
                   ostree-readonly-sysroot-migration ostree-state-overlay@ pesign pmfind pmie_farm
                   pmlogger_farm pmproxy podman podman-auto-update podman-clean-transient podman-kube@
                   podman-restart postfix powertop psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts
```

(Continued on next page)

Platform Notes (Continued)

```
                  rpmdb-rebuild rrdcached saslauthd selinux-check-proper-disable serial-getty@ smad_rev smb
                  snmpd snmptrapd spamassassin speech-dispatcherd sshd-keygen@
                  systemd-boot-check-no-failures systemd-pstore systemd-sysext target targetclid tog-pegasus
                  vsftpd wpa_supplicant
  indirect        iscsi spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
                  systemd-sysupdate systemd-sysupdate-reboot vsftpd@

------------------------------------------------------------
14. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
  root=/dev/mapper/rhel-root
  ro
  crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
  resume=/dev/mapper/rhel-swap
  rd.lvm.lv=rhel/root
  rd.lvm.lv=rhel/swap
  rhgb
  quiet

------------------------------------------------------------
15. cpupower frequency-info
  analyzing CPU 3:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
16. tuned-adm active
  It seems that tuned daemon is not running, preset profile is not activated.
  Preset profile: throughput-performance

------------------------------------------------------------
17. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
19. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
20. OS release
  From /etc/*-release /etc/*-version
  os-release     Red Hat Enterprise Linux 9.4 (Plow)
  redhat-release Red Hat Enterprise Linux release 9.4 (Plow)
  system-release Red Hat Enterprise Linux release 9.4 (Plow)

------------------------------------------------------------
21. Disk information
SPEC is set to: /home/cpu2017
  Filesystem            Type  Size  Used Avail Use% Mounted on
  /dev/mapper/rhel-root ext4  1.8T   68G  1.6T   5% /

------------------------------------------------------------
22. /sys/devices/virtual/dmi/id
    Vendor:         NEC
    Product:        Express5800/R110m-1
    Product Family: Express5800
    Serial:         SGH404JTQ8

------------------------------------------------------------
23. dmidecode
  Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    2x Samsung M324R4GA3DB0-CWMOL 32 GB 2 rank 5600, configured at 4400


------------------------------------------------------------
24. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       NEC
   BIOS Version:      2.10
   BIOS Date:         12/06/2024
   BIOS Revision:     2.10
   Firmware Revision: 1.68
```

Compiler Version Notes

```
==============================================================================================================
C              | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

(Continued on next page)

Compiler Version Notes (Continued)

```
==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

(Continued on next page)

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto

(Continued on next page)

Peak Optimization Flags (Continued)

511.povray_r (continued):
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R110m-1-RevF.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R110m-1-RevF.xml

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-07-03 06:46:43-0400.
Report generated on 2026-04-14 10:01:49 by CPU2017 PS formatter v6716.
Originally published on 2026-04-13.