SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Cisco Systems

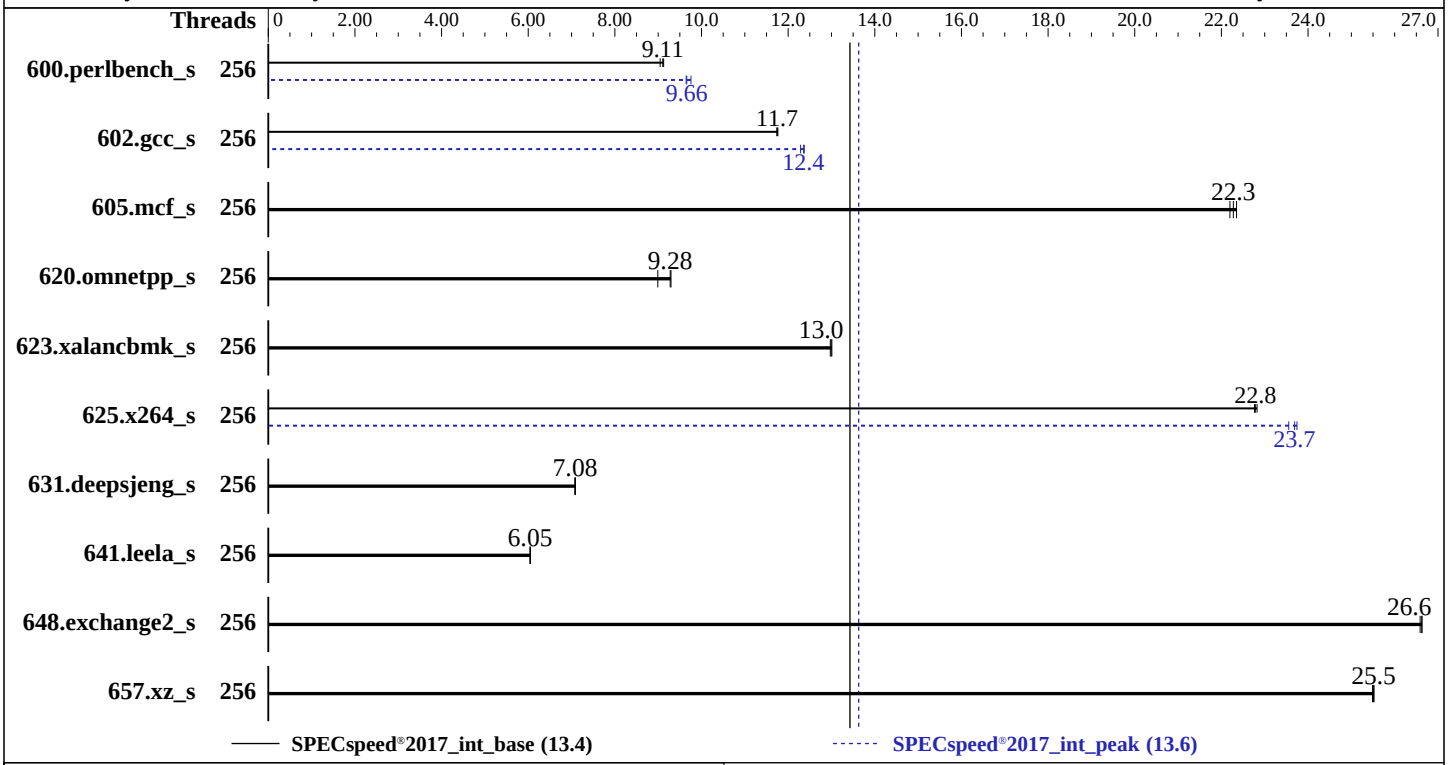
Cisco UCS X210c M8 (Intel Xeon 6767P 2.40 GHz processor)

SPECspeed®2017_int_base = 13.4

SPECspeed®2017_int_peak = 13.6

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024



Hardware

CPU Name:	Intel Xeon 6767P
Max MHz:	3900
Nominal:	2400
Enabled:	128 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	64 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	336 MB I+D on chip per chip
Other:	None
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-6400B-R)
Storage:	1 x 960 GB SSD
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP6 6.4.0-150600.21-default
Compiler:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel:	Yes
Firmware:	Version 6.0.1a released Jul-2025
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS X210c M8 (Intel Xeon 6767P 2.40 GHz processor)

SPECspeed®2017_int_base = 13.4

SPECspeed®2017_int_peak = 13.6

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	256	**195**	**9.11**	195	9.13	196	9.05	256	182	9.76	**184**	**9.66**	184	9.65
602.gcc_s	256	339	11.8	**339**	**11.7**	339	11.7	256	324	12.3	322	12.4	**322**	**12.4**
605.mcf_s	256	**212**	**22.3**	211	22.4	213	22.2	256	**212**	**22.3**	211	22.4	213	22.2
620.omnetpp_s	256	175	9.30	**176**	**9.28**	181	8.99	256	175	9.30	**176**	**9.28**	181	8.99
623.xalancbmk_s	256	109	13.0	**109**	**13.0**	109	13.0	256	109	13.0	**109**	**13.0**	109	13.0
625.x264_s	256	**77.4**	**22.8**	77.3	22.8	77.5	22.8	256	**74.5**	**23.7**	74.9	23.6	74.3	23.7
631.deepsjeng_s	256	**202**	**7.08**	203	7.08	202	7.09	256	**202**	**7.08**	203	7.08	202	7.09
641.leela_s	256	**282**	**6.05**	282	6.05	282	6.05	256	**282**	**6.05**	282	6.05	282	6.05
648.exchange2_s	256	**110**	**26.6**	110	26.6	111	26.6	256	**110**	**26.6**	110	26.6	111	26.6
657.xz_s	256	**242**	**25.5**	243	25.5	242	25.5	256	**242**	**25.5**	243	25.5	242	25.5

SPECspeed®2017_int_base = **13.4**

SPECspeed®2017_int_peak = **13.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Redhat Enterprise Linux 8.0
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS settings:
Sub NUMA clustering set to Enabled
Hardware prefetcher set to Enabled
Adjacent cache line prefetcher set to Disabled
XPT prefetch set to Auto
LLC prefetch set to Enabled
Enhanced CPU performance set to Auto

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Wed Oct  8 12:01:08 2025

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
 ------------------------------------------------------------

 ------------------------------------------------------------
 1. uname -a
   Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
   x86_64 x86_64 x86_64 GNU/Linux

 ------------------------------------------------------------
 2. w
    12:01:08 up  9:04,  2 users,  load average: 191.98, 222.20, 218.85
   USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
   root     tty1     -                03:09   50:43   1.15s  0.25s -bash

 ------------------------------------------------------------
 3. Username
   From environment variable $USER:  root

 ------------------------------------------------------------
 4. ulimit -a
   core file size          (blocks, -c) unlimited
```

(Continued on next page)

Platform Notes (Continued)

```
   data seg size           (kbytes, -d) unlimited
   scheduling priority             (-e) 0
   file size               (blocks, -f) unlimited
   pending signals                 (-i) 4125257
   max locked memory       (kbytes, -l) 8192
   max memory size         (kbytes, -m) unlimited
   open files                      (-n) 1024
   pipe size            (512 bytes, -p) 8
   POSIX message queues     (bytes, -q) 819200
   real-time priority              (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time               (seconds, -t) unlimited
   max user processes              (-u) 4125257
   virtual memory          (kbytes, -v) unlimited
   file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=42
 login -- root
 -bash
 -bash
 runcpu --define default-platform-flags -c ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=256
   --tune all -o all --define drop_caches intspeed
 runcpu --define default-platform-flags --configfile ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define
   cores=256 --tune all --output_format all --define drop_caches --nopower --runmode speed --tune base:peak
   --size refspeed intspeed --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.059/templogs/preenv.intspeed.059.0.log --lognum 059.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) 6767P
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 173
    stepping        : 1
    microcode       : 0x10003c2
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
    cpu cores       : 64
    siblings        : 128
    2 physical ids (chips)
    256 processors (hardware threads)
    physical id 0: core ids 0-31,64-95
    physical id 1: core ids 0-31,64-95
    physical id 0: apicids 0-63,128-191
    physical id 1: apicids 256-319,384-447
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        46 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               256
```

(Continued on next page)

Platform Notes (Continued)

```
On-line CPU(s) list:                0-255
Vendor ID:                          GenuineIntel
BIOS Vendor ID:                     Intel(R) Corporation
Model name:                         Intel(R) Xeon(R) 6767P
BIOS Model name:                    Intel(R) Xeon(R) 6767P  CPU @ 2.4GHz
BIOS CPU family:                    179
CPU family:                         6
Model:                              173
Thread(s) per core:                 2
Core(s) per socket:                 64
Socket(s):                          2
Stepping:                           1
CPU(s) scaling MHz:                 23%
CPU max MHz:                        3900.0000
CPU min MHz:                        800.0000
BogoMIPS:                           4800.00
Flags:                              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                    pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                    pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                    nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                    pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr
                                    pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
                                    aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb
                                    cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
                                    ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms
                                    invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                                    clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt
                                    xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                    cqm_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16
                                    wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
                                    hwp_pkg_req hfi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
                                    vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
                                    rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                                    serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
                                    amx_int8 flush_l1d arch_capabilities
L1d cache:                          6 MiB (128 instances)
L1i cache:                          8 MiB (128 instances)
L2 cache:                           256 MiB (128 instances)
L3 cache:                           672 MiB (2 instances)
NUMA node(s):                       4
NUMA node0 CPU(s):                  0-31,128-159
NUMA node1 CPU(s):                  32-63,160-191
NUMA node2 CPU(s):                  64-95,192-223
NUMA node3 CPU(s):                  96-127,224-255
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:        Not affected
Vulnerability L1tf:                 Not affected
Vulnerability Mds:                  Not affected
Vulnerability Meltdown:             Not affected
Vulnerability Mmio stale data:      Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:             Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass:    Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:           Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:           Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                    PBRSB-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:                Not affected
Vulnerability Tsx async abort:      Not affected
```

(Continued on next page)

Platform Notes (Continued)

```
From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
     L1d       48K       6M   12 Data            1     64        1             64
     L1i       64K       8M   16 Instruction     1     64        1             64
     L2         2M     256M   16 Unified         2   2048        1             64
     L3       336M     672M   16 Unified         3 344064        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0-31,128-159
  node 0 size: 257327 MB
  node 0 free: 255155 MB
  node 1 cpus: 32-63,160-191
  node 1 size: 258031 MB
  node 1 free: 257072 MB
  node 2 cpus: 64-95,192-223
  node 2 size: 258031 MB
  node 2 free: 257036 MB
  node 3 cpus: 96-127,224-255
  node 3 size: 257951 MB
  node 3 free: 257183 MB
  node distances:
  node   0   1   2   3
    0:  10  12  21  21
    1:  12  10  21  21
    2:  21  21  10  12
    3:  21  21  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1056093148 kB

------------------------------------------------------------
10. who -r
  run-level 3 Oct 8 02:57

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
                   issue-generator kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections
                   nvmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                   virtqemud wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info
                   firewalld fsidd gpm grub2-once haveged hv_fcopy_daemon hv_kvp_daemon hv_vss_daemon ipmi
                   ipmievd iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests
                   lunmask man-db-create multipathd nfs nfs-blkmap nfs-server nfsserver rpcbind
                   rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts snmpd snmptrapd
                   strongswan strongswan-starter svnserve systemd-boot-check-no-failures systemd-confext
                   systemd-network-generator systemd-nspawn@ systemd-sysext systemd-time-wait-sync
                   systemd-timesyncd tcsd udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodedevd
```

(Continued on next page)

Platform Notes (Continued)

```
                  virtnwfilterd virtsecretd virtstoraged
  indirect        pcscd systemd-userdbd tftp wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=8e9f907e-efe9-44a3-b843-a3c1cced3852
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    current policy: frequency should be within 800 MHz and 3.90 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing              1
  kernel.randomize_va_space          2
  vm.compaction_proactiveness       20
  vm.dirty_background_bytes          0
  vm.dirty_background_ratio         10
  vm.dirty_bytes                     0
  vm.dirty_expire_centisecs       3000
  vm.dirty_ratio                    20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000
```

(Continued on next page)

Platform Notes (Continued)

```
 ------------------------------------------------------------
 18. OS release
   From /etc/*-release /etc/*-version
   os-release SUSE Linux Enterprise Server 15 SP6

 ------------------------------------------------------------
 19. Disk information
 SPEC is set to: /home/cpu2017
   Filesystem     Type   Size  Used Avail Use% Mounted on
   /dev/nvme1n1p2 btrfs  370G   20G  347G   6% /home

 ------------------------------------------------------------
 20. /sys/devices/virtual/dmi/id
     Vendor:         Cisco Systems Inc
     Product:        UCSX-210C-M8
     Serial:         FCH2842725V

 ------------------------------------------------------------
 21. dmidecode
   Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     9x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400
     7x 0xCE00 M321R8GA0PB2-CCPPC 64 GB 2 rank 6400


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Cisco Systems, Inc.
    BIOS Version:      X210M8.6.0.1a.3.0718251042
    BIOS Date:         07/18/2025
    BIOS Revision:     5.35
```

Compiler Version Notes

```
==============================================================================================================
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
        | 657.xz_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
        | 641.leela_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran | 648.exchange2_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
```

(Continued on next page)

Compiler Version Notes (Continued)

```
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

(Continued on next page)

Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

(Continued on next page)

Peak Optimization Flags (Continued)

605.mcf_s: `basepeak = yes`

625.x264_s: `-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

657.xz_s: `basepeak = yes`

C++ benchmarks:

620.omnetpp_s: `basepeak = yes`

623.xalancbmk_s: `basepeak = yes`

631.deepsjeng_s: `basepeak = yes`

641.leela_s: `basepeak = yes`

Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.2025-06-17.html
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V2.0-GNR-revM.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.2025-06-17.xml
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V2.0-GNR-revM.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-10-08 12:01:08-0400.
Report generated on 2025-11-06 11:42:11 by CPU2017 PS formatter v6716.
Originally published on 2025-11-06.