SPEC CPU®2017 Floating Point Speed Result

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Dell Inc.

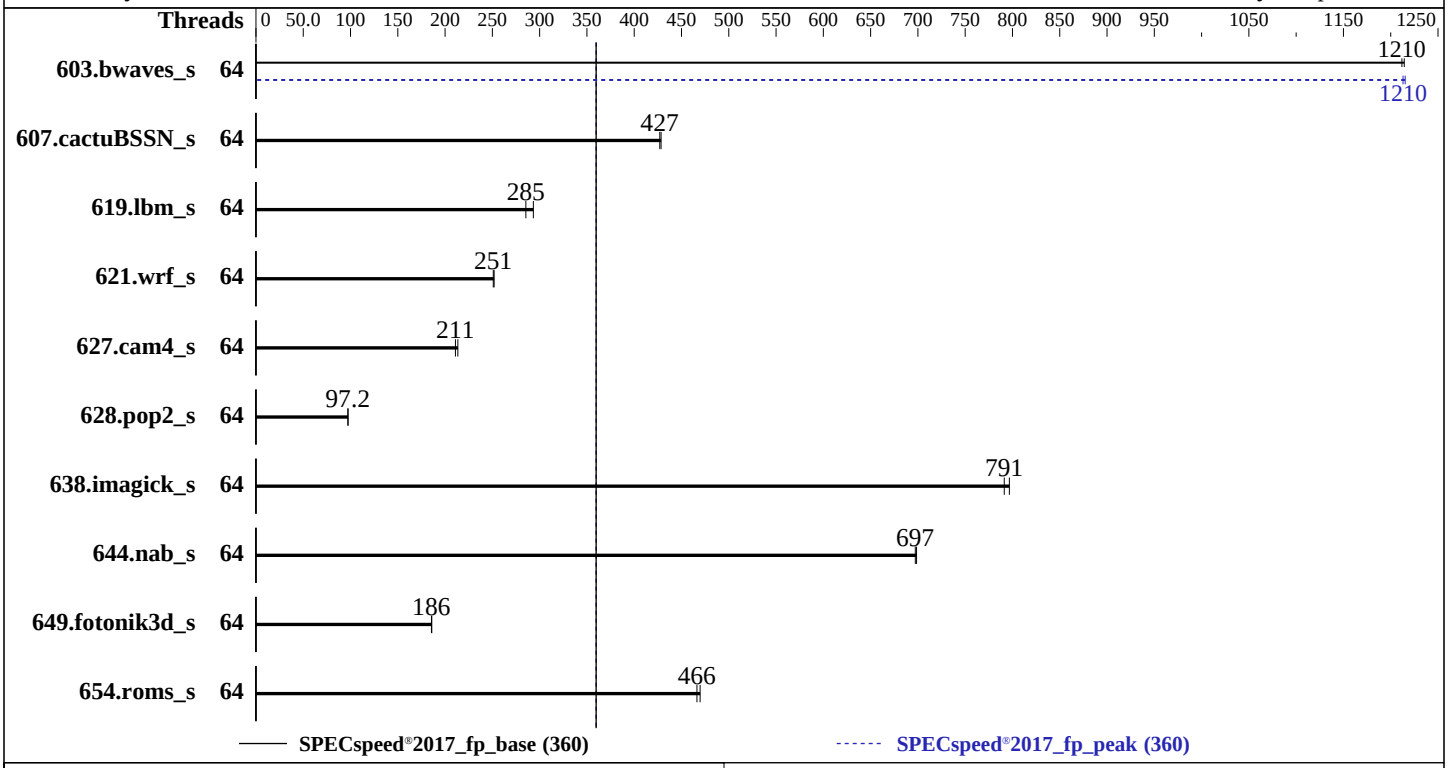
PowerEdge XE9680L (Intel Xeon Platinum 8562Y+)

SPECspeed®2017_fp_base = 360

SPECspeed®2017_fp_peak = 360

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Aug-2025
Hardware Availability: Jul-2025
Software Availability: Apr-2024



Hardware

CPU Name:	Intel Xeon Platinum 8562Y+
Max MHz:	4100
Nominal:	2800
Enabled:	64 cores, 2 chips
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	60 MB I+D on chip per chip
Other:	None
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-5600B-R)
Storage:	60 GB on tmpfs
Other:	CPU Cooling: DLC

Software

OS:	Red Hat Enterprise Linux 9.4 (Plow) 5.14.0-427.13.1.el9_4.x86_64
Compiler:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel:	Yes
Firmware:	Version 2.7.5 released Jul-2025
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	48.6	1210	**48.7**	**1210**			64	**48.6**	**1210**	48.5	1220		
607.cactuBSSN_s	64	**39.0**	**427**	38.9	428			64	**39.0**	**427**	38.9	428		
619.lbm_s	64	**18.4**	**285**	17.9	293			64	**18.4**	**285**	17.9	293		
621.wrf_s	64	52.5	252	**52.7**	**251**			64	52.5	252	**52.7**	**251**		
627.cam4_s	64	**42.0**	**211**	41.5	213			64	**42.0**	**211**	41.5	213		
628.pop2_s	64	**122**	**97.2**	122	97.4			64	**122**	**97.2**	122	97.4		
638.imagick_s	64	**18.2**	**791**	18.1	797			64	**18.2**	**791**	18.1	797		
644.nab_s	64	**25.1**	**697**	25.0	699			64	**25.1**	**697**	25.0	699		
649.fotonik3d_s	64	**49.1**	**186**	49.0	186			64	**49.1**	**186**	49.0	186		
654.roms_s	64	33.5	470	**33.8**	**466**			64	33.5	470	**33.8**	**466**		

SPECspeed®2017_fp_base = 360

SPECspeed®2017_fp_peak = 360

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

Benchmark run from a 60 GB ramdisk created with the cmd: "mount -t tmpfs -o size=60G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS Settings:
        Logical Processor : Disabled
 Virtualization Technology : Disabled
             LLC Prefetch : Enabled
```

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Platform Notes (Continued)

```
              x2APIC Mode : Disabled
           Optimizer Mode : Enabled

           System Profile : Custom
    Energy Efficient Turbo : Enabled
                C-States : Autonomous
       Memory Patrol Scrub : Disabled
          Uncore Frequency : Dynamic
       DIMM Self Healing -
on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 1234567-XE9680L Thu Aug 21 08:08:37 2025

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
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------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux 1234567-XE9680L 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT 2024
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   08:08:37 up  2:14,  1 user,  load average: 6.51, 5.26, 3.09
  USER     TTY        LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1      05:55    2:09m  0.92s  0.00s /bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh
  speed --define DL-VERS=6.3a --output_format html,pdf,txt

------------------------------------------------------------
3. Username
  From environment variable $USER:  root
```

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Platform Notes (Continued)

```
------------------------------------------------------------
4. ulimit -a
  real-time non-blocking time  (microseconds, -R) unlimited
  core file size              (blocks, -c) 0
  data seg size               (kbytes, -d) unlimited
  scheduling priority                 (-e) 0
  file size                   (blocks, -f) unlimited
  pending signals                     (-i) 4126151
  max locked memory           (kbytes, -l) 64
  max memory size             (kbytes, -m) unlimited
  open files                          (-n) 1024
  pipe size                (512 bytes, -p) 8
  POSIX message queues         (bytes, -q) 819200
  real-time priority                  (-r) 0
  stack size                  (kbytes, -s) unlimited
  cpu time                   (seconds, -t) unlimited
  max user processes                  (-u) 4126151
  virtual memory              (kbytes, -v) unlimited
  file locks                          (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
 login -- root
 -bash
 /bin/bash /home/DellFiles/bin/DELL_speed.sh
 /bin/bash /home/DellFiles/bin/dell-run-main.sh speed
 /bin/bash /home/DellFiles/bin/dell-run-main.sh speed
 /bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-VERS=6.3a --output_format
   html,pdf,txt
 /bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-VERS=6.3a --output_format
   html,pdf,txt
 runcpu --nobuild --action validate --define default-platform-flags -c
   ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=64 --tune base,peak -o all --define
   drop_caches --iterations 2 --define DL-VERS=6.3a --output_format html,pdf,txt fpspeed
 runcpu --nobuild --action validate --define default-platform-flags --configfile
   ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=64 --tune base,peak --output_format all
   --define drop_caches --iterations 2 --define DL-VERS=6.3a --output_format html,pdf,txt --nopower --runmode
   speed --tune base:peak --size refspeed fpspeed --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.002/templogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.1

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : INTEL(R) XEON(R) PLATINUM 8562Y+
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 207
    stepping        : 2
    microcode       : 0x210002b3
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 32
    siblings        : 32
    2 physical ids (chips)
    64 processors (hardware threads)
    physical id 0: core ids 0-31
    physical id 1: core ids 0-31
    physical id 0: apicids
```

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Platform Notes (Continued)

```
    0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62
    physical id 1: apicids
    128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
    80,182,184,186,188,190
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        46 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               64
  On-line CPU(s) list:                  0-63
  Vendor ID:                            GenuineIntel
  BIOS Vendor ID:                       Intel
  Model name:                           INTEL(R) XEON(R) PLATINUM 8562Y+
  BIOS Model name:                      INTEL(R) XEON(R) PLATINUM 8562Y+
  CPU family:                           6
  Model:                                207
  Thread(s) per core:                   1
  Core(s) per socket:                   32
  Socket(s):                            2
  Stepping:                             2
  BogoMIPS:                             5600.00
  Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                                        rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                                        xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq
                                        dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid
                                        dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
                                        f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2
                                        cdp_l3 cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase
                                        tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
                                        rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
                                        avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                                        cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni avx512_bf16
                                        wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip pku ospke waitpkg
                                        avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                                        avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                                        enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16
                                        avx512_fp16 amx_tile amx_int8 flush_l1d arch_capabilities
  L1d cache:                            3 MiB (64 instances)
  L1i cache:                            2 MiB (64 instances)
  L2 cache:                             128 MiB (64 instances)
  L3 cache:                             120 MiB (2 instances)
  NUMA node(s):                         2
  NUMA node0 CPU(s):                    0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,5
                                        0,52,54,56,58,60,62
  NUMA node1 CPU(s):                    1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,5
                                        1,53,55,57,59,61,63
  Vulnerability Gather data sampling:   Not affected
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
```

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Platform Notes (Continued)

```
  Vulnerability Retbleed:             Not affected
  Vulnerability Spec rstack overflow: Not affected
  Vulnerability Spec store bypass:    Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:           Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:           Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
                                      PBRSB-eIBRS SW sequence
  Vulnerability Srbds:                Not affected
  Vulnerability Tsx async abort:      Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K       3M   12 Data            1    64        1             64
     L1i       32K       2M    8 Instruction     1    64        1             64
     L2         2M     128M   16 Unified         2  2048        1             64
     L3        60M     120M   15 Unified         3 65536        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62
  node 0 size: 515540 MB
  node 0 free: 499452 MB
  node 1 cpus: 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63
  node 1 size: 516040 MB
  node 1 free: 513756 MB
  node distances:
  node   0   1
    0:  10  21
    1:  21  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1056338556 kB

------------------------------------------------------------
10. who -r
  run-level 3 Aug 21 05:54

------------------------------------------------------------
11. Systemd service manager version: systemd 252 (252-32.el9_4)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
                   chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance iscsi-onboot
                   iscsi-starter kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmonitor
                   microcode multipathd nis-domainname nvmefc-boot-connections rhsmcertd rsyslog rtkit-daemon
                   selinux-autorelabel-mark smartd sshd sssd systemd-boot-update systemd-network-generator
                   tuned udisks2 upower
  enabled-runtime  systemd-remount-fs
  disabled         arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
                   canberra-system-shutdown-reboot chrony-wait chronyd-restricted console-getty cpupower
                   debug-shell dnf-system-upgrade hwloc-dump-hwdata iprdump iprinit iprupdate ipsec
                   iscsi-init iscsid iscsiuio kpatch kvm_stat ledmon man-db-restart-cache-update nftables
                   nvmf-autoconnect pesign psacct rdisc rhcd rhsm rhsm-facts rpmdb-rebuild
                   selinux-check-proper-disable serial-getty@ sshd-keygen@ systemd-boot-check-no-failures
```

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Platform Notes (Continued)

```
                  systemd-pstore systemd-sysext
  indirect        iscsi sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
                  systemd-sysupdate-reboot

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=(hd2,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
  root=/dev/mapper/rhel-root
  ro
  resume=/dev/mapper/rhel-swap
  rd.lvm.lv=rhel/root
  rd.lvm.lv=rhel/swap
  rhgb
  quiet

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 42:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. tuned-adm active
  Current active profile: throughput-performance

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     40
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      10
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
```

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Platform Notes (Continued)

```
   max_ptes_none             511
   max_ptes_shared           256
   max_ptes_swap              64
   pages_to_scan            4096
   scan_sleep_millisecs    10000

 ------------------------------------------------------------
 19. OS release
   From /etc/*-release /etc/*-version
   os-release     Red Hat Enterprise Linux 9.4 (Plow)
   redhat-release Red Hat Enterprise Linux release 9.4 (Plow)
   system-release Red Hat Enterprise Linux release 9.4 (Plow)

 ------------------------------------------------------------
 20. Disk information
 SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.1
   Filesystem     Type   Size  Used Avail Use% Mounted on
   tmpfs          tmpfs   60G   11G   50G  19% /mnt/ramdisk

 ------------------------------------------------------------
 21. /sys/devices/virtual/dmi/id
     Vendor:         Dell Inc.
     Product:        PowerEdge XE9680L
     Product Family: PowerEdge
     Serial:         1234567

 ------------------------------------------------------------
 22. dmidecode
   Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     4x 00CE063200CE M321R8GA0EB0-CWMKH 64 GB 2 rank 5600
     12x 00CE069D00CE M321R8GA0EB0-CWMXJ 64 GB 2 rank 5600


 ------------------------------------------------------------
 23. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Dell Inc.
    BIOS Version:      2.7.5
    BIOS Date:         07/31/2025
    BIOS Revision:     2.7
```

Compiler Version Notes

```
==============================================================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

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Compiler Version Notes (Continued)

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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------------------------------------------------------------------------------------------------------------

============================================================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------

============================================================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64

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Base Portability Flags (Continued)

649.fotonik3d_s: `-DSPEC_LP64`
654.roms_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:
```
icx
```

Fortran benchmarks:
```
ifx
```

Benchmarks using both Fortran and C:
```
ifx icx
```

(Continued on next page)

Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.17x.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.17x.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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