SPEC CPU®2017 Integer Rate Result

ZTE Corporation

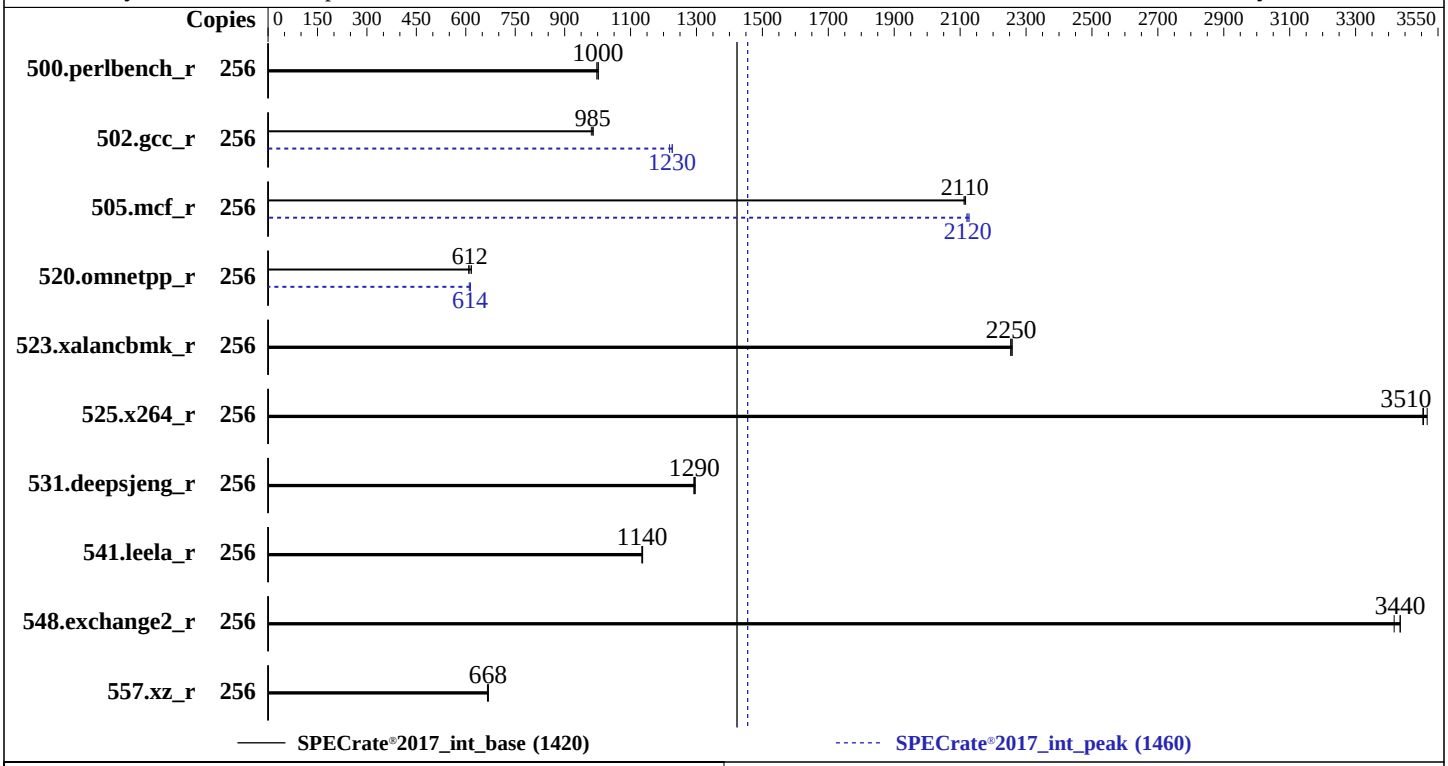
ZTE R5350G5 Server System
(3.10 GHz, AMD EPYC 9554)

SPECrate®2017_int_base = 1420

SPECrate®2017_int_peak = 1460

CPU2017 License: 9061
Test Sponsor: ZTE Corporation
Tested by: ZTE Corporation

Test Date: Jun-2025
Hardware Availability: Feb-2024
Software Availability: Oct-2024



Hardware

CPU Name:	AMD EPYC 9554
Max MHz:	3750
Nominal:	3100
Enabled:	128 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	256 MB I+D on chip per chip, 32 MB shared / 8 cores
Other:	None
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)
Storage:	1 x 3.84 TB NVMe SSD
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP6 kernel version 6.4.0-150600.21-default
Compiler:	C/C++/Fortran: Version 5.0.0 of AOCC
Parallel:	No
Firmware:	Version 24.25.02.20 released Jun-2025
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	None
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	256	409	998	**407**	**1000**	407	1000	256	409	998	**407**	**1000**	407	1000
502.gcc_r	256	**368**	**985**	367	987	370	981	256	298	1220	296	1230	**296**	**1230**
505.mcf_r	256	196	2110	**196**	**2110**	195	2120	256	**195**	**2120**	195	2120	194	2130
520.omnetpp_r	256	544	617	552	609	**549**	**612**	256	547	614	**547**	**614**	550	611
523.xalancbmk_r	256	120	2250	**120**	**2250**	120	2260	256	120	2250	**120**	**2250**	120	2260
525.x264_r	256	127	3520	**128**	**3510**	128	3500	256	127	3520	**128**	**3510**	128	3500
531.deepsjeng_r	256	**227**	**1290**	227	1290	226	1300	256	**227**	**1290**	227	1290	226	1300
541.leela_r	256	374	1130	**373**	**1140**	373	1140	256	374	1130	**373**	**1140**	373	1140
548.exchange2_r	256	196	3420	**195**	**3440**	195	3440	256	196	3420	**195**	**3440**	195	3440
557.xz_r	256	414	668	**414**	**668**	415	666	256	414	668	**414**	**668**	415	666

SPECrate®2017_int_base = 1420

SPECrate®2017_int_peak = 1460

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
 BIOS Configuration:
 Determinism Control= Manual
 Determinism Enable = Power
 TDP Control = Manual
 TDP = 400
 PPT Control = Manual
 PPT = 400
 NUMA nodes per socket = NPS4

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost.localdomain Sat Jun 28 10:20:14 2025

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
```

(Continued on next page)

Platform Notes (Continued)

```
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost.localdomain 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024
  (36c1e09/lp) x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   10:20:14 up 19:13,  2 users,  load average: 0.08, 0.10, 0.04
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     pts/0    192.2.211.171    22Apr24 12.00s  1.88s  0.54s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
  root     pts/1    192.2.211.171    22Apr24  2:33   3.33s  0.31s -bash

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 6189056
  max locked memory       (kbytes, -l) 2097152
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 6189056
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=42
 sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
 sshd: root [priv]
 sshd: root@pts/0
 -bash
 python3 ./run_amd_rate_aocc500_znver5_A1.py
 /bin/bash ./amd_rate_aocc500_znver5_A1.sh
 runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune peak --reportable --iterations 3 intrate
 runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune peak --reportable --iterations 3 --nopower
   --runmode rate --tune peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9554 64-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 25
```

(Continued on next page)

Platform Notes (Continued)

```
     model           : 17
     stepping        : 1
     microcode       : 0xa101154
     bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
     TLB size        : 3584 4K pages
     cpu cores       : 64
     siblings        : 128
     2 physical ids (chips)
     256 processors (hardware threads)
     physical id 0: core ids 0-63
     physical id 1: core ids 0-63
     physical id 0: apicids 0-127
     physical id 1: apicids 128-255
   Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
   Architecture:                         x86_64
   CPU op-mode(s):                       32-bit, 64-bit
   Address sizes:                        52 bits physical, 57 bits virtual
   Byte Order:                           Little Endian
   CPU(s):                               256
   On-line CPU(s) list:                  0-255
   Vendor ID:                            AuthenticAMD
   BIOS Vendor ID:                       Advanced Micro Devices, Inc.
   Model name:                           AMD EPYC 9554 64-Core Processor
   BIOS Model name:                      AMD EPYC 9554 64-Core Processor                 Unknown CPU @ 3.1GHz
   BIOS CPU family:                      107
   CPU family:                           25
   Model:                                17
   Thread(s) per core:                   2
   Core(s) per socket:                   64
   Socket(s):                            2
   Stepping:                             1
   Frequency boost:                      enabled
   CPU(s) scaling MHz:                   83%
   CPU max MHz:                          3762.9880
   CPU min MHz:                          1500.0000
   BogoMIPS:                             6200.14
   Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                         pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                         rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                         extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                         sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                         cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                         osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                         perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                         ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2
                                         erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                                         clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                                         xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                         user_shstk avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
                                         amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean
                                         flushbyasid decodeassists pausefilter pfthreshold avic
                                         v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku
                                         ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                                         avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
```


(Continued on next page)

Platform Notes (Continued)

```
                                    debug_swap
  Virtualization:                   AMD-V
  L1d cache:                        4 MiB (128 instances)
  L1i cache:                        4 MiB (128 instances)
  L2 cache:                         128 MiB (128 instances)
  L3 cache:                         512 MiB (16 instances)
  NUMA node(s):                     8
  NUMA node0 CPU(s):                0-15,128-143
  NUMA node1 CPU(s):                16-31,144-159
  NUMA node2 CPU(s):                32-47,160-175
  NUMA node3 CPU(s):                48-63,176-191
  NUMA node4 CPU(s):                64-79,192-207
  NUMA node5 CPU(s):                80-95,208-223
  NUMA node6 CPU(s):                96-111,224-239
  NUMA node7 CPU(s):                112-127,240-255
  Vulnerability Gather data sampling:   Not affected
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:               Not affected
  Vulnerability Spec rstack overflow:   Mitigation; Safe RET
  Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                        always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
  Vulnerability Srbds:                  Not affected
  Vulnerability Tsx async abort:        Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       32K       4M    8 Data            1    64        1             64
     L1i       32K       4M    8 Instruction     1    64        1             64
     L2         1M     128M    8 Unified         2  2048        1             64
     L3        32M     512M   16 Unified         3 32768        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 8 nodes (0-7)
  node 0 cpus: 0-15,128-143
  node 0 size: 193143 MB
  node 0 free: 191301 MB
  node 1 cpus: 16-31,144-159
  node 1 size: 193527 MB
  node 1 free: 192195 MB
  node 2 cpus: 32-47,160-175
  node 2 size: 193527 MB
  node 2 free: 192054 MB
  node 3 cpus: 48-63,176-191
  node 3 size: 193527 MB
  node 3 free: 192364 MB
  node 4 cpus: 64-79,192-207
  node 4 size: 193527 MB
  node 4 free: 192392 MB
  node 5 cpus: 80-95,208-223
  node 5 size: 193527 MB
  node 5 free: 192101 MB
```

(Continued on next page)

Platform Notes (Continued)

```
  node 6 cpus: 96-111,224-239
  node 6 size: 193527 MB
  node 6 free: 192412 MB
  node 7 cpus: 112-127,240-255
  node 7 size: 192986 MB
  node 7 free: 191762 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  12  12  12  32  32  32  32
    1:  12  10  12  12  32  32  32  32
    2:  12  12  10  12  32  32  32  32
    3:  12  12  12  10  32  32  32  32
    4:  32  32  32  32  10  12  12  12
    5:  32  32  32  32  12  10  12  12
    6:  32  32  32  32  12  12  10  12
    7:  32  32  32  32  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584429820 kB

------------------------------------------------------------
10. who -r
  run-level 3 Apr 22 20:32 last=5

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  graphical       running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
                   YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron
                   display-manager getty@ irqbalance issue-generator kbdsettings kdump kdump-early
                   kdump-notify klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd
                   systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
                   wpa_supplicant
  enabled-runtime  systemd-remount-fs
  disabled         accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl
                   ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell
                   dmraid-activation dnsmasq ebtables exchange-bmc-os-info firewalld fsidd gpm grub2-once
                   haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load ksm kvm_stat lunmask
                   man-db-create multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount rpcbind rpmconfigcheck
                   rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd
                   speech-dispatcherd systemd-boot-check-no-failures systemd-confext
                   systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
                   update-system-flatpaks upower vncserver@ wpa_supplicant@
  indirect         pcscd saned@ systemd-userdbd wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=8d6f0b40-31c8-46cc-b981-5f7af9274aa5
  splash=silent
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=383M,high
```

(Continued on next page)

Platform Notes (Continued)

```
  crashkernel=72M,low

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 86:
    current policy: frequency should be within 1.50 GHz and 3.10 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing              1
  kernel.randomize_va_space          0
  vm.compaction_proactiveness       20
  vm.dirty_background_bytes          0
  vm.dirty_background_ratio         10
  vm.dirty_bytes                     0
  vm.dirty_expire_centisecs       3000
  vm.dirty_ratio                     8
  vm.dirty_writeback_centisecs     500
  vm.dirtytime_expire_seconds    43200
  vm.extfrag_threshold             500
  vm.min_unmapped_ratio              1
  vm.nr_hugepages                    0
  vm.nr_hugepages_mempolicy          0
  vm.nr_overcommit_hugepages         0
  vm.swappiness                      1
  vm.watermark_boost_factor      15000
  vm.watermark_scale_factor         10
  vm.zone_reclaim_mode               1

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017
  Filesystem     Type   Size  Used Avail Use% Mounted on
```

(Continued on next page)

Platform Notes (Continued)

```
  /dev/sda3      btrfs  3.4T   16G  3.4T   1% /home

 ------------------------------------------------------------
 20. /sys/devices/virtual/dmi/id
     Vendor:         ZTE
     Product:        R5350 G5
     Product Family: Server
     Serial:         219535501329

 ------------------------------------------------------------
 21. dmidecode
   Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     8x Samsung M321R8GA0PB0-CWMCH 64 GB 2 rank 5600, configured at 4800
     16x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 4800


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       American Megatrends Inc.
    BIOS Version:      24.25.02.20
    BIOS Date:         06/18/2025
    BIOS Revision:     24.25
```

Compiler Version Notes

```
==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
```

(Continued on next page)

Compiler Version Notes (Continued)

```
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

========================================================================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
        | 541.leela_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

========================================================================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Base Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`

(Continued on next page)

Base Portability Flags (Continued)

541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc-ext -ldl

C++ benchmarks:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext
-ldl

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamdlibm -lflang -lamdalloc -ldl

Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument

(Continued on next page)

Base Other Flags (Continued)

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

Peak Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Peak Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `basepeak = yes`

502.gcc_r: `-m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner`

(Continued on next page)

Peak Optimization Flags (Continued)

502.gcc_r (continued):
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc

505.mcf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl

525.x264_r: basepeak = yes

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -fno-PIE
-no-pie -fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lamdalloc-ext
-ldl

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

Peak Other Flags

C benchmarks (except as noted below):
`-Wno-unused-command-line-argument`

502.gcc_r: `-L/usr/lib32 -Wno-unused-command-line-argument`
`-L/home/work/cpu2017/v119/aocc5/1316/amd_rate_aocc500_znver5_A_lib/lib32`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/ZTE-Platform-Settings-AMD-V3.1.html`
`http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/ZTE-Platform-Settings-AMD-V3.1.xml`
`http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-06-27 22:20:13-0400.
Report generated on 2025-07-30 15:12:36 by CPU2017 PDF formatter v6716.
Originally published on 2025-07-29.