



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS C245 M8 (AMD EPYC 9175F  
4.20 GHz Processor)

SPECrate®2017\_int\_base = 595

SPECrate®2017\_int\_peak = 605

CPU2017 License: 9019

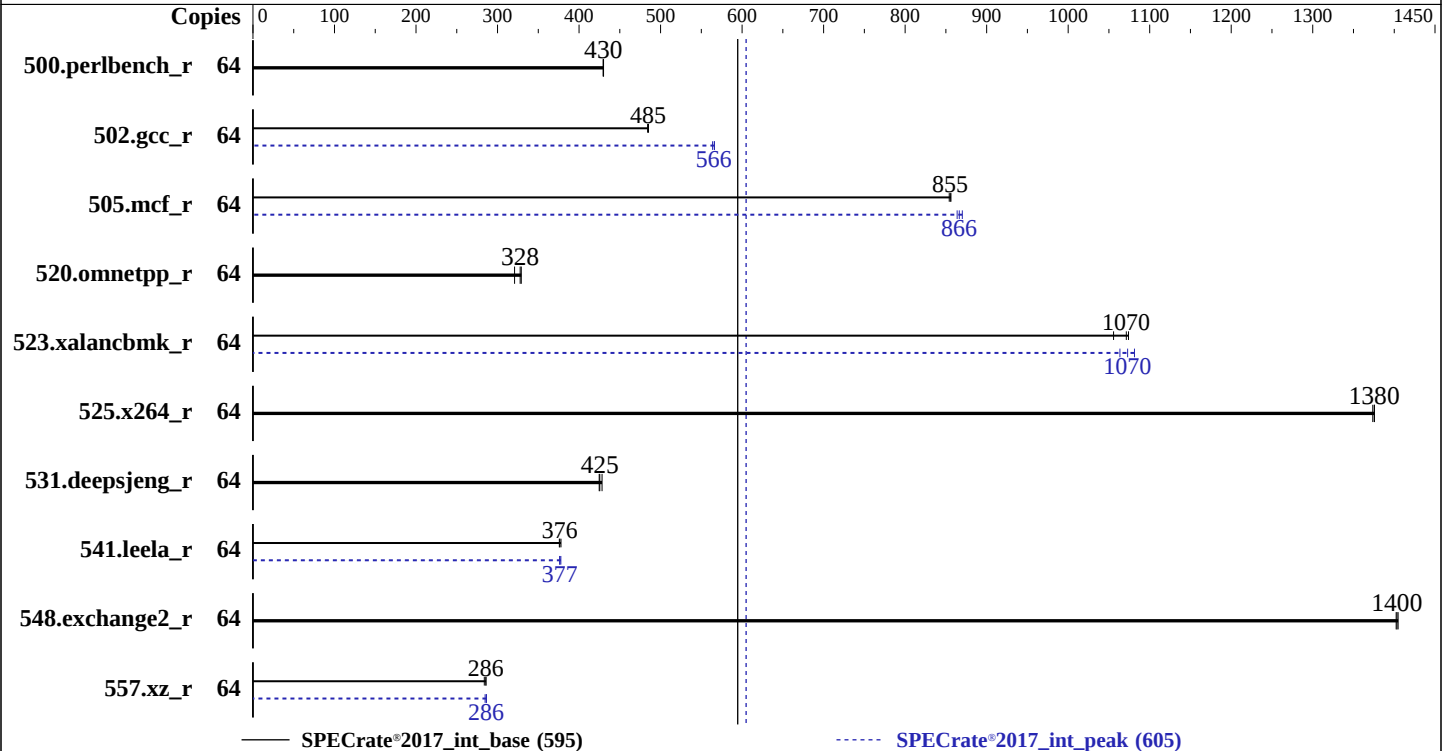
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Oct-2024

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9175F  
Max MHz: 5000  
Nominal: 4200  
Enabled: 32 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 512 MB I+D on chip per chip, 32 MB shared / 1 cores  
Other: None  
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R, running at 6000)  
Storage: 1 x 960 GB SATA SSD  
Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
kernel version 6.4.0-150600.21-default  
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
Parallel: No  
Firmware: Version 4.3.5c released Dec-2024  
File System: btrfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

| Benchmark       | Base   |             |             |            |            |             |             | Peak   |             |             |            |            |             |             |
|-----------------|--------|-------------|-------------|------------|------------|-------------|-------------|--------|-------------|-------------|------------|------------|-------------|-------------|
|                 | Copies | Seconds     | Ratio       | Seconds    | Ratio      | Seconds     | Ratio       | Copies | Seconds     | Ratio       | Seconds    | Ratio      | Seconds     | Ratio       |
| 500.perlbench_r | 64     | 237         | 430         | <u>237</u> | <u>430</u> | 237         | 430         | 64     | 237         | 430         | <u>237</u> | <u>430</u> | 237         | 430         |
| 502.gcc_r       | 64     | 187         | 484         | <u>187</u> | <u>485</u> | 187         | 485         | 64     | 160         | 566         | 161        | 564        | <u>160</u>  | <u>566</u>  |
| 505.mcf_r       | 64     | <u>121</u>  | <u>855</u>  | 121        | 856        | 121         | 854         | 64     | 119         | 870         | 120        | 864        | <u>119</u>  | <u>866</u>  |
| 520.omnetpp_r   | 64     | 262         | 321         | <u>256</u> | <u>328</u> | 255         | 329         | 64     | 262         | 321         | <u>256</u> | <u>328</u> | 255         | 329         |
| 523.xalancbmk_r | 64     | <u>63.1</u> | <u>1070</u> | 62.9       | 1070       | 64.0        | 1060        | 64     | <u>63.0</u> | <u>1070</u> | 63.5       | 1060       | 62.5        | 1080        |
| 525.x264_r      | 64     | 81.6        | 1370        | 81.5       | 1380       | <u>81.5</u> | <u>1380</u> | 64     | 81.6        | 1370        | 81.5       | 1380       | <u>81.5</u> | <u>1380</u> |
| 531.deepsjeng_r | 64     | <u>172</u>  | <u>425</u>  | 171        | 428        | 173         | 425         | 64     | <u>172</u>  | <u>425</u>  | 171        | 428        | 173         | 425         |
| 541.leela_r     | 64     | 282         | 376         | 280        | 378        | <u>282</u>  | <u>376</u>  | 64     | 281         | 378         | <u>281</u> | <u>377</u> | 282         | 376         |
| 548.exchange2_r | 64     | <u>119</u>  | <u>1400</u> | 119        | 1400       | 120         | 1400        | 64     | <u>119</u>  | <u>1400</u> | 119        | 1400       | 120         | 1400        |
| 557.xz_r        | 64     | 244         | 284         | 242        | 286        | <u>242</u>  | <u>286</u>  | 64     | 243         | 285         | 241        | 286        | <u>241</u>  | <u>286</u>  |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH =

"/home/cpu2017/amd\_rate\_aocc500\_znver5\_A\_lib/lib:/home/cpu2017/amd\_rate\_aocc500\_znver5\_A\_lib/lib32:"

MALLOC\_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalancbmk\_r peak run:

MALLOC\_CONF = "thp:always"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS settings:

NUMA nodes per socket set to NPS4

Determinism Slider set to Power

DF C-States set to Disabled

Enhanced CPU performance set to Auto

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Thu May 29 20:36:06 2025

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged

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## Platform Notes (Continued)

19. OS release  
20. Disk information  
21. /sys/devices/virtual/dmi/id  
22. dmidecode  
23. BIOS

-----

1. `uname -a`  
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT\_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)  
x86\_64 x86\_64 x86\_64 GNU/Linux

-----

2. `w`  
20:36:06 up 10 min, 3 users, load average: 0.15, 0.25, 0.24  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 20:27 20.00s 0.79s 0.08s /bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh

-----

3. Username  
From environment variable \$USER: root

-----

4. `ulimit -a`  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 6190766  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 6190766  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

-----

5. `sysinfo process ancestry`  
/usr/lib/systemd/systemd --switched-root --system --deserialize=42  
login -- root  
-bash  
python3 ./run\_amd\_rate\_aocc500\_znver5\_A1.py -b intrate  
/bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh  
runcpu --config amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 intrate  
runcpu --configfile amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 --nopower  
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile  
\$SPEC/tmp/CPU2017.001/temlogs/preenv.intrate.001.0.log --lognum 001.0 --from\_runcpu 2  
specperl \$SPEC/bin/sysinfo  
\$SPEC = /home/cpu2017

-----

6. `/proc/cpuinfo`  
model name : AMD EPYC 9175F 16-Core Processor  
vendor\_id : AuthenticAMD  
cpu family : 26

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## Platform Notes (Continued)

```
model          : 2
stepping       : 1
microcode      : 0xb00211a
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 192 4K pages
cpu cores      : 16
siblings       : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120
physical id 1: core ids 0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120
physical id 0: apicids
0-1,16-17,32-33,48-49,64-65,80-81,96-97,112-113,128-129,144-145,160-161,176-177,192-193,208-209,224-225,2
40-241
physical id 1: apicids
256-257,272-273,288-289,304-305,320-321,336-337,352-353,368-369,384-385,400-401,416-417,432-433,448-449,4
64-465,480-481,496-497
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 64
On-line CPU(s) list:   0-63
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:             AMD EPYC 9175F 16-Core Processor
BIOS Model name:       AMD EPYC 9175F 16-Core Processor
BIOS CPU family:       107
CPU family:             26
Model:                  2
Thread(s) per core:    2
Core(s) per socket:    16
Socket(s):              2
Stepping:               1
Frequency boost:        enabled
CPU(s) scaling MHz:    85%
CPU max MHz:            5003.9058
CPU min MHz:            1500.0000
BogoMIPS:               8386.86
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
```

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## Platform Notes (Continued)

nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v\_vmsave\_vmload vgif x2avic v\_spec\_ctrl vnmi  
avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq  
avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid bus\_lock\_detect  
movdiri movdir64b overflow\_recov succor smca fsrm avx512\_vp2intersect  
flush\_l1d debug\_swap

AMD-V

Virtualization:  
L1d cache: 1.5 MiB (32 instances)  
L1i cache: 1 MiB (32 instances)  
L2 cache: 32 MiB (32 instances)  
L3 cache: 1 GiB (32 instances)  
NUMA node(s): 8

NUMA node0 CPU(s): 0-3,32-35

NUMA node1 CPU(s): 4-7,36-39

NUMA node2 CPU(s): 8-11,40-43

NUMA node3 CPU(s): 12-15,44-47

NUMA node4 CPU(s): 16-19,48-51

NUMA node5 CPU(s): 20-23,52-55

NUMA node6 CPU(s): 24-27,56-59

NUMA node7 CPU(s): 28-31,60-63

Vulnerability Gather data sampling: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability L1tf: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Reg file data sampling: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

always-on; RSB filling; PBRBS-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 1.5M     | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 1M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 32M      | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 32M      | 1G       | 16   | Unified     | 3     | 32768 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-3,32-35

node 0 size: 193117 MB

node 0 free: 192624 MB

node 1 cpus: 4-7,36-39

node 1 size: 193533 MB

node 1 free: 193270 MB

node 2 cpus: 8-11,40-43

node 2 size: 193533 MB

node 2 free: 193345 MB

node 3 cpus: 12-15,44-47

node 3 size: 193495 MB

node 3 free: 193134 MB

node 4 cpus: 16-19,48-51

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```
node 4 size: 193533 MB
node 4 free: 193331 MB
node 5 cpus: 20-23,52-55
node 5 size: 193533 MB
node 5 free: 193342 MB
node 6 cpus: 24-27,56-59
node 6 size: 193533 MB
node 6 free: 193344 MB
node 7 cpus: 28-31,60-63
node 7 size: 193437 MB
node 7 free: 193165 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10  12  12  12  32  32  32  32
1:  12  10  12  12  32  32  32  32
2:  12  12  10  12  32  32  32  32
3:  12  12  12  10  32  32  32  32
4:  32  32  32  32  10  12  12  12
5:  32  32  32  32  12  10  12  12
6:  32  32  32  32  12  12  10  12
7:  32  32  32  32  12  12  12  10
```

```
9. /proc/meminfo
MemTotal:      1584863232 kB
```

```
10. who -r
run-level 3 May 29 20:27
```

```
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target    Status
multi-user        degraded
```

```
12. Failed units, from systemctl list-units --state=failed
UNIT          LOAD    ACTIVE SUB    DESCRIPTION
* logrotate.service loaded failed failed Rotate log files
```

```
13. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
issue-generator kbdsettings klog lvm2-monitor nsd postfix purge-kernels rollback rsyslog
smartd sshd systemd-pstore virtqemud wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
wickedd-nanny
enabled-runtime systemd-remount-fs
disabled       autofs autyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info
firewalld fsidd gpm grub2-once haveged hv_fcopy_daemon hv_kv_daemon hv_vss_daemon
hwloc-dump-hwdata ipmi ipmiev d iscsi-init iscsid issue-add-ssh-keys kexec-load ksm
kvm_stat libvirt-guests lunmask man-db-create multipathd nfs nfs-blkmap nfs-server
nfsserver rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts
snmpd snmptrapd strongswan strongswan-starter svnserve systemd-boot-check-no-failures
systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysex
systemd-time-wait-sync systemd-timesyncd tcsd udisks2 virtinterfaced virtlockd virtlogd
virtnetworkd virtnodedevd virtnwfilterd virtsecret virtstoraged
indirect       pcsd systemd-userdbd tftp wickedd
```

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## Platform Notes (Continued)

### 14. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=c83597cb-e37b-43a6-bd1c-58cd7f1f9316  
splash=silent  
mitigations=auto  
quiet  
security=apparmor
```

### 15. cpupower frequency-info

```
analyzing CPU 50:  
  current policy: frequency should be within 1.50 GHz and 4.20 GHz.  
                  The governor "performance" may decide which speed to use  
                  within this range.  
  boost state support:  
    Supported: yes  
    Active: yes
```

### 16. sysctl

```
kernel.numa_balancing          1  
kernel.randomize_va_space      0  
vm.compaction_proactiveness    20  
vm.dirty_background_bytes      0  
vm.dirty_background_ratio      10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs      3000  
vm.dirty_ratio                 8  
vm.dirty_writeback_centisecs   500  
vm.dirtytime_expire_seconds    43200  
vm.extfrag_threshold           500  
vm.min_unmapped_ratio          1  
vm.nr_hugepages                0  
vm.nr_hugepages_mempolicy      0  
vm.nr_overcommit_hugepages     0  
vm.swappiness                   1  
vm.watermark_boost_factor      15000  
vm.watermark_scale_factor      10  
vm.zone_reclaim_mode           1
```

### 17. /sys/kernel/mm/transparent\_hugepage

```
defrag      [always] defer defer+madvise madvise never  
enabled     [always] madvise never  
hpage_pmd_size 2097152  
shmem_enabled always within_size advise [never] deny force
```

### 18. /sys/kernel/mm/transparent\_hugepage/khugepaged

```
alloc_sleep_millisecs 60000  
defrag                 1  
max_ptes_none          511  
max_ptes_shared        256  
max_ptes_swap          64  
pages_to_scan          4096  
scan_sleep_millisecs   10000
```

### 19. OS release

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Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Oct-2024

Software Availability: Oct-2024

## Platform Notes (Continued)

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP6

### 20. Disk information

SPEC is set to: /home/cpu2017

| Filesystem | Type  | Size | Used | Avail | Use% | Mounted on |
|------------|-------|------|------|-------|------|------------|
| /dev/sda2  | btrfs | 224G | 9.2G | 211G  | 5%   | /home      |

### 21. /sys/devices/virtual/dmi/id

Vendor: Cisco Systems Inc  
Product: UCSC-C245-M8SX  
Product Family: Cisco UCS Rack Server  
Serial: WZP2750Z0CS

### 22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

22x 0xCE00 M321R8GA0PB2-CCPEC 64 GB 2 rank 6400, configured at 6000  
1x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 6000  
1x 0xCE00 M321R8GA0PB2-CCPPC 64 GB 2 rank 6400, configured at 6000

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Cisco Systems, Inc.  
BIOS Version: C245M8.4.3.5c.0.1202241033  
BIOS Date: 12/02/2024  
BIOS Revision: 5.35

## Compiler Version Notes

C | 502.gcc\_r(peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C | 502.gcc\_r(peak)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

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## Cisco Systems

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## Compiler Version Notes (Continued)

-----  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

=====

|   |  |                             |                 |                       |                        |
|---|--|-----------------------------|-----------------|-----------------------|------------------------|
| C |  | 500.perlbench_r(base, peak) | 502.gcc_r(base) | 505.mcf_r(base, peak) | 525.x264_r(base, peak) |
|   |  | 557.xz_r(base, peak)        |                 |                       |                        |

-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

=====

|     |  |                           |                             |                             |
|-----|--|---------------------------|-----------------------------|-----------------------------|
| C++ |  | 520.omnetpp_r(base, peak) | 523.xalancbmk_r(base, peak) | 531.deepsjeng_r(base, peak) |
|     |  | 541.leela_r(base, peak)   |                             |                             |

-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

=====

|         |  |                             |
|---------|--|-----------------------------|
| Fortran |  | 548.exchange2_r(base, peak) |
|---------|--|-----------------------------|

-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

(Continued on next page)



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## Base Portability Flags (Continued)

502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather  
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang  
-lamdalloc-ext -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie  
-fvirtual-function-elimination -fvisibility=hidden  
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext  
-ldl

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto  
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost  
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500  
-lamdlibm -lflang -lamdalloc -ldl



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Test Sponsor: Cisco Systems

Tested by: Cisco Systems

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Hardware Availability: Oct-2024

Software Availability: Oct-2024

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes

(Continued on next page)



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## Peak Optimization Flags (Continued)

```
502.gcc_r: -m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc
```

```
505.mcf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl
```

525.x264\_r: basepeak = yes

```
557.xz_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl
```

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

```
523.xalancbmk_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang
-lamdalloc-ext -ldl
```

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## Peak Optimization Flags (Continued)

531.deepsjeng\_r: basepeak = yes

541.leela\_r: -m64 -std=c++14

-Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6

-Wl, -mllvm -Wl, -reduce-array-computations=3

-Wl, -mllvm -Wl, -do-block-reorder=advanced -Ofast

-march=znver5 -fveclib=AMDLIB -ffast-math -flto

-mllvm -unroll-threshold=100

-mllvm -reduce-array-computations=3 -zopt -fno-PIE

-no-pie -fvirtual-function-elimination -fvisibility=hidden

-mllvm -do-block-reorder=advanced -lamdlibm -lflang

-lamdalloc-ext -ldl

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc\_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v119/aocc5/1316/amd\_rate\_aocc500\_znver5\_A\_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.xml>



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