



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R6725 (AMD EPYC 9755 128-Core Processor)

SPECSpeed®2017_fp_base = 545

SPECSpeed®2017_fp_peak = 545

CPU2017 License: 6573

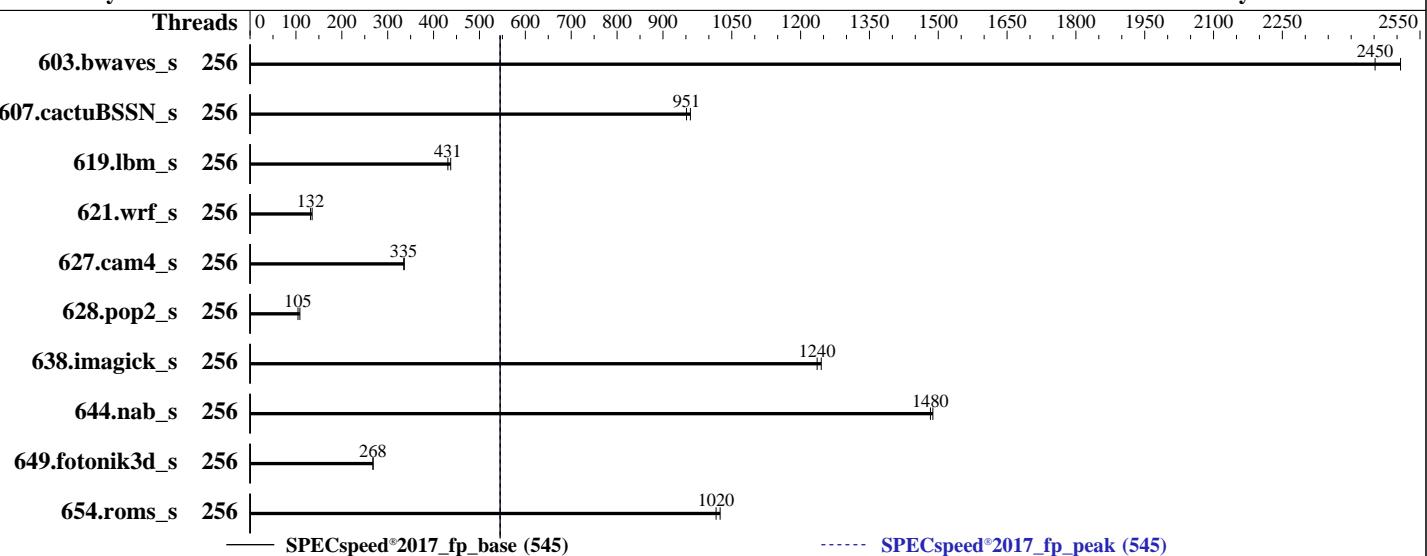
Test Date: Apr-2025

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2025

Tested by: Dell Inc.

Software Availability: Oct-2024



Hardware		Software	
CPU Name:	AMD EPYC 9755	OS:	Ubuntu 24.04 LTS
Max MHz:	4100		6.8.0-44-generic
Nominal:	2700	Compiler:	C/C++/Fortran: Version 5.0.0 of AOCC
Enabled:	256 cores, 2 chips	Parallel:	Yes
Orderable:	1,2 chips	Firmware:	Version 1.1.3 released Feb-2025
Cache L1:	32 KB I + 48 KB D on chip per core	File System:	tmpfs
L2:	1 MB I+D on chip per core	System State:	Run level 5 (graphical multi-user)
L3:	512 MB I+D on chip per chip, 32 MB shared / 8 cores	Base Pointers:	64-bit
Other:	None	Peak Pointers:	64-bit
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)	Other:	None
Storage:	130 GB on tmpfs	Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.
Other:	CPU Cooling: Air		



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds
603.bwaves_s	256	23.5	2510	24.1	2450				256	23.5	2510	24.1	2450			
607.cactuBSSN_s	256	17.5	951	17.4	960				256	17.5	951	17.4	960			
619.lbm_s	256	12.1	431	12.0	437				256	12.1	431	12.0	437			
621.wrf_s	256	100	132	97.7	135				256	100	132	97.7	135			
627.cam4_s	256	26.4	336	26.5	335				256	26.4	336	26.5	335			
628.pop2_s	256	114	105	109	109				256	114	105	109	109			
638.imagick_s	256	11.7	1240	11.6	1240				256	11.7	1240	11.6	1240			
644.nab_s	256	11.8	1480	11.7	1490				256	11.8	1480	11.7	1490			
649.fotonik3d_s	256	34.0	268	34.0	268				256	34.0	268	34.0	268			
654.roms_s	256	15.4	1020	15.5	1020				256	15.4	1020	15.5	1020			

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-255"
LD_LIBRARY_PATH =
    "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/amd_speed_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu201
    7-1.1.9-aocc500-znerv5_A1.3/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "256"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

Benchmark run from a 130 GB ramdisk created with the cmd: "mount -t tmpfs -o size=130G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

```
Logical Processor : Disabled
Virtualization Technology : Disabled

System Profile : Custom
C-States : Disabled
Memory Patrol Scrub : Disabled
PCI ASPM L1 Link Power Management : Disabled
Periodic Directory Rinse Tuning : Blended
Determinism Control : Manual
Determinism Slider : Power Determinism
Optimizer Mode : Enabled
Algorithm Performance Boost Disable : Enabled
Adaptive Allocation : Enabled
Dram Refresh Delay : Performance
DIMM Self Healing -
on Uncorrectable Memory Error : Disabled
```

```
Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on SLR6704-R6725 Tue Apr 22 21:11:56 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo

(Continued on next page)



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Platform Notes (Continued)

```
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS
```

```
-----  
1. uname -a  
Linux SLR6704-R6725 6.8.0-44-generic #44-Ubuntu SMP PREEMPT_DYNAMIC Tue Aug 13 13:35:26 UTC 2024 x86_64  
x86_64 x86_64 GNU/Linux
```

```
-----  
2. w  
21:11:56 up 2:42, 1 user, load average: 0.61, 0.90, 0.97  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root ttys1 - 18:29 2:39m 1.63s 0.58s /bin/bash ./amd_speed_aocc500_znver5_A1.sh
```

```
-----  
3. Username  
From environment variable $USER: root
```

```
-----  
4. ulimit -a  
time(seconds) unlimited  
file(blocks) unlimited  
data(kbytes) unlimited  
stack(kbytes) unlimited  
coredump(blocks) 0  
memory(kbytes) unlimited  
locked memory(kbytes) 2097152  
process 6188995  
nofiles 1024  
vmmemory(kbytes) unlimited  
locks unlimited  
rtprio 0
```

```
-----  
5. sysinfo process ancestry  
/sbin/init  
/bin/login -p --  
-bash  
/bin/bash /home/DellFiles/bin/DELL_speed.sh  
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed  
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed  
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh speed --define DL-VERS=6.2 --output_format  
html,pdf,txt  
python3 ./run_amd_speed_aocc500_znver5_A1.py  
/bin/bash ./amd_speed_aocc500_znver5_A1.sh  
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
```

(Continued on next page)



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Platform Notes (Continued)

```
DL-BIOS-NPS=1 --define DL-VERS=6.2 --output_format html,pdf,txt fpsspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=1 --define DL-VERS=6.2 --output_format html,pdf,txt --nopower --runmode speed --tune base:peak
--size test:train:refspeed fpsspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fpsspeed.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3
```

```
-----  
6. /proc/cpuinfo  
model name      : AMD EPYC 9755 128-Core Processor  
vendor_id       : AuthenticAMD  
cpu family     : 26  
model          : 2  
stepping        : 1  
microcode       : 0xb00211e  
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass  
TLB size        : 192 4K pages  
cpu cores       : 128  
siblings        : 128  
2 physical ids (chips)  
256 processors (hardware threads)  
physical id 0: core ids  
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,  
240-247  
physical id 1: core ids  
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,  
240-247  
physical id 0: apicids  
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,  
240-247  
physical id 1: apicids  
256-263,272-279,288-295,304-311,320-327,336-343,352-359,368-375,384-391,400-407,416-423,432-439,448-455,4  
64-471,480-487,496-503
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	256
On-line CPU(s) list:	0-255
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	AMD
Model name:	AMD EPYC 9755 128-Core Processor
BIOS Model name:	AMD EPYC 9755 128-Core Processor
BIOS CPU family:	107
CPU family:	26
Model:	2
Thread(s) per core:	1
Core(s) per socket:	128
Socket(s):	2
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	66%

(Continued on next page)



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Platform Notes (Continued)

CPU max MHz:	4121.1909
CPU min MHz:	1500.0000
BogoMIPS:	5392.57
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpx cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsqsbbase tsc_adjust bmil avx2 smep bmi2 invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local user_shstk avx_vnni avx512_bf16 clzero iperf xsaveeprptr rdpru wbnoinvd amd_ppin cpc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpocntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca avx512_vp2intersect flush_ll1d debug_swap
L1d cache:	12 MiB (256 instances)
L1i cache:	8 MiB (256 instances)
L2 cache:	256 MiB (256 instances)
L3 cache:	1 GiB (32 instances)
NUMA node(s):	2
NUMA node0 CPU(s):	0-127
NUMA node1 CPU(s):	128-255
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	12M	12	Data	1	64	1	64
L1i	32K	8M	8	Instruction	1	64	1	64
L2	1M	256M	16	Unified	2	1024	1	64
L3	32M	1G	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-127

node 0 size: 773275 MB

node 0 free: 767778 MB

(Continued on next page)



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Platform Notes (Continued)

```
node 1 cpus: 128-255
node 1 size: 774044 MB
node 1 free: 772312 MB
node distances:
node 0 1
 0: 10 32
 1: 32 10

-----
9. /proc/meminfo
MemTotal:      1584455464 kB

-----
10. who -r
run-level 5 Apr 22 18:29

-----
11. Systemd service manager version: systemd 255 (255.4-lubuntu8)
Default Target  Status
graphical       degraded

-----
12. Failed units, from systemctl list-units --state=failed
UNIT            LOAD  ACTIVE SUB   DESCRIPTION
* fwupd-refresh.service loaded failed Refresh fwupd metadata and update motd
Legend: LOAD  -> Reflects whether the unit definition was properly loaded.
        ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.
        SUB   -> The low-level unit activation state, values depend on unit type.
1 loaded units listed.

-----
13. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
                cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
                grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd
                networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
                sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
                systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgaauth
enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
disabled       console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                systemd-boot-check-no-failures systemd-confext systemd-network-generator
                systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                systemd-time-wait-sync upower
indirect       systemd-sysupdate systemd-sysupdate-reboot uuid
masked        cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.8.0-44-generic
root=UUID=8458ae54-58cc-4621-9289-b1d743fde503
ro

-----
15. cpupower frequency-info
analyzing CPU 104:
current policy: frequency should be within 1.50 GHz and 2.70 GHz.
The governor "performance" may decide which speed to use
```

(Continued on next page)



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Platform Notes (Continued)

within this range.

boost state support:

Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2800MHz

16. tuned-adm active

Current active profile: latency-performance

17. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	3
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

18. /sys/kernel/mm/transparent_hugepage

defrag	[always] defer defer+madvise madvise never
enabled	[always] madvise never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

19. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

20. OS release

From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

21. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3

Filesystem Type Size Used Avail Use% Mounted on

(Continued on next page)



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Platform Notes (Continued)

tmpfs tmpfs 130G 3.3G 127G 3% /mnt/ramdisk

22. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.
Product: PowerEdge R6725
Product Family: PowerEdge
Serial: SLR6704

23. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x 80AD000080AD HMC94AHBRA283N 64 GB 2 rank 6400

24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.
BIOS Version: 1.1.3
BIOS Date: 02/25/2025
BIOS Revision: 1.1

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C, Fortran | 607.cactusBSSN_s(base, peak)
=====
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

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SPEC CPU®2017 Floating Point Speed Result

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Dell Inc.

PowerEdge R6725 (AMD EPYC 9755 128-Core Processor)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECSpeed®2017_fp_base = 545

SPECSpeed®2017_fp_peak = 545

Test Date: Apr-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactusBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapi -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapi -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64



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Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt  
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100  
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```



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Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

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Hardware Availability: Mar-2025

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Peak Optimization Flags (Continued)

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>



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