



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR650 V4
(2.30 GHz, Intel Xeon 6511P)

SPECrate®2017_int_base = 193

SPECrate®2017_int_peak = Not Run

CPU2017 License: 9017

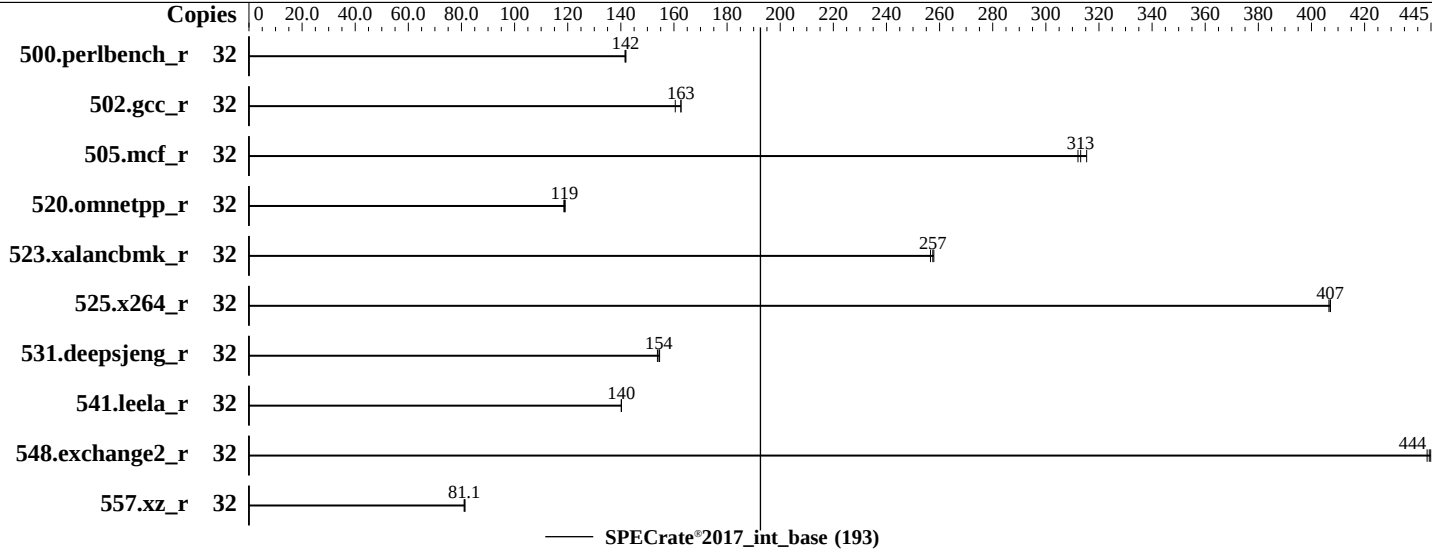
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: May-2025

Hardware Availability: May-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6511P
Max MHz: 4200
Nominal: 2300
Enabled: 16 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 72 MB I+D on chip per chip
Other: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 960 GB NVME SSD
Other: CPU Cooling: DLC

Software

OS: SUSE Linux Enterprise Server 15 SP6
Kernel 6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Lenovo BIOS Version IHE109U 1.20 released Mar-2025
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	359	142	<u>359</u>	<u>142</u>	360	142							
502.gcc_r	32	279	163	<u>279</u>	<u>163</u>	282	161							
505.mcf_r	32	166	312	<u>165</u>	<u>313</u>	164	315							
520.omnetpp_r	32	354	119	<u>353</u>	<u>119</u>	353	119							
523.xalancbmk_r	32	131	258	<u>131</u>	<u>257</u>	132	257							
525.x264_r	32	138	407	<u>138</u>	<u>407</u>	138	407							
531.deepsjeng_r	32	238	154	237	155	<u>238</u>	<u>154</u>							
541.leela_r	32	378	140	378	140	378	140							
548.exchange2_r	32	189	444	188	445	189	444							
557.xz_r	32	426	81.1	425	81.4	426	81.0							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/home/cpu2017-1.1.9-ic2024.1/lib/intel64:/home/cpu2017-1.1.9-ic2024.1/lib/ia32:/home/cpu2017-1.1.9-ic2024.1/je5.0.1-32"

MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



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Platform Notes

BIOS configuration:
SNC set to Enabled

Sysinfo program /home/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu May 22 19:00:55 2025

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
2. w
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4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
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9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
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17. /sys/kernel/mm/transparent_hugepage/khugepaged
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20. /sys/devices/virtual/dmi/id
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22. BIOS

1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
19:00:55 up 3 min, 1 user, load average: 0.14, 0.15, 0.07
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 1030042
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024

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Platform Notes (Continued)

pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 1030042
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECCpu_1.01.sh
sh Run502-compliant-ic2024.1-lin-sapphirerapids-rateint-base-smt-on-20240308.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define no-numa --tune base -o all --define drop_caches intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define no-numa --tune base --output_format all --define drop_caches --nopower --runmode rate --tune base
--size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.637/templots/preenv.intrate.637.0.log --lognum 637.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.1
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6511P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0xa0000c0
bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 16
siblings        : 32
1 physical ids (chips)
32 processors (hardware threads)
physical id 0:  core ids 0-15
physical id 0:  apicids 0-31
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                32
On-line CPU(s) list:   0-31
Vendor ID:             GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:            Intel(R) Xeon(R) 6511P
BIOS Model name:       Intel(R) Xeon(R) 6511P UNKNOWN CPU @ 2.3GHz
```

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Platform Notes (Continued)

BIOS CPU family: 179
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 1
Stepping: 1
BogoMIPS: 4600.00
Flags: fpv vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hfi vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2
gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm
md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16
amx_tile amx_int8 flush_l1d arch_capabilities
VT-x
768 KiB (16 instances)
L1d cache: 1 MiB (16 instances)
L1i cache: 32 MiB (16 instances)
L2 cache: 72 MiB (1 instance)
L3 cache: 1
NUMA node(s): 0-31
NUMA node0 CPU(s):
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
PBRBS-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	64K	1M	16	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	72M	72M	16	Unified	3	73728	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

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Platform Notes (Continued)

```
available: 1 nodes (0)
node 0 cpus: 0-31
node 0 size: 257537 MB
node 0 free: 256715 MB
node distances:
node 0
0: 10
```

```
-----
9. /proc/meminfo
MemTotal: 263717932 kB
```

```
-----
10. who -r
run-level 3 May 22 18:58
```

```
-----
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
multi-user running
```

```
-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-autoconnect postfix
purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld fsidd gpm grub2-once haveged ipmi ipmievd issue-add-ssh-keys kexec-load lunmask
man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@
smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysexit systemd-time-wait-sync systemd-timesyncd
generated ntp_sync
indirect systemd-userdbd wickedd
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=8c76b9eb-7cab-4e58-a0ba-c4f6030335e5
splash=silent
mitigations=auto
quiet
security=apparmor
```

```
-----
14. cpupower frequency-info
analyzing CPU 3:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes
```

```
-----
15. sysctl
kernel.numa_balancing 0
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
```

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Platform Notes (Continued)

```
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages      0
vm.swappiness                   60
vm.watermark_boost_factor       15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            0
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled          [always] madvice never
hpage_pmd_size  2097152
shmem_enabled    always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs    10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
-----
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2024.1
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/nvme0n1p2  xfs       893G      84G  810G  10% /
-----
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:          Lenovo
Product:         ThinkSystem SR650 V4
Product Family:  ThinkSystem
Serial:          0987654321
-----
```

```
-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
8x Samsung M321R4GA3PB2-CCPEC 32 GB 2 rank 6400
-----
```

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Platform Notes (Continued)

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo
BIOS Version: IHE109U-1.20
BIOS Date: 03/15/2025
BIOS Revision: 1.20
Firmware Revision: 1.20

Compiler Version Notes

=====
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++ | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran | 548.exchange2_r(base)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -DSPEC_LP64

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Base Portability Flags (Continued)

505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapfirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapfirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapfirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-C.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-C.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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