



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

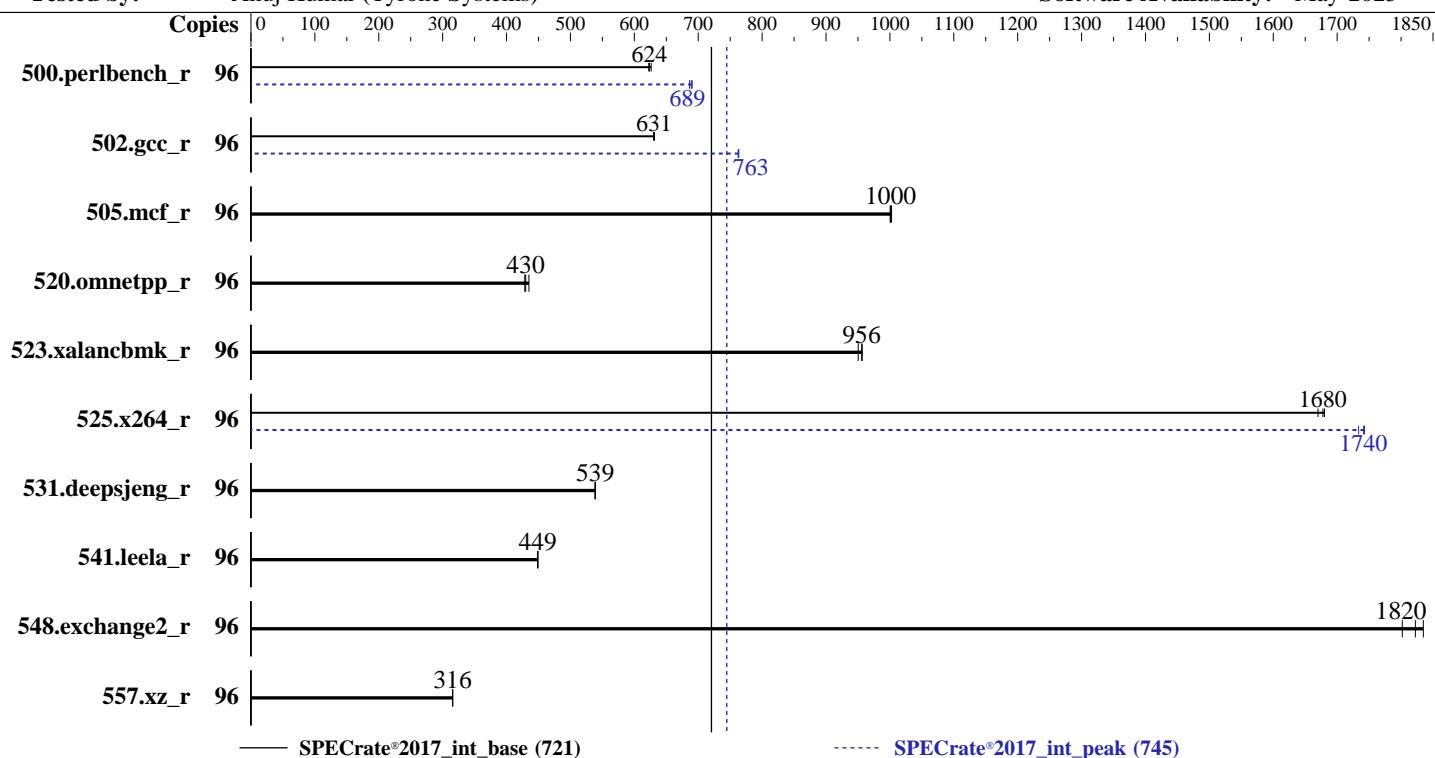
Test Date: May-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jun-2024

Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025



— SPECrate®2017_int_base (721)

----- SPECrate®2017_int_peak (745)

Hardware

CPU Name: Intel Xeon Platinum 8568Y+
Max MHz: 4000
Nominal: 2300
Enabled: 96 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 300 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 960 GB NVMe
Other: CPU Cooling: Air

Software

OS: Ubuntu 22.04.5 LTS
Compiler: 5.15.0-140-generic
C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version 2.4 released Sep-2024
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at cost of additional power.



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Date: May-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jun-2024

Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	245	624	245	623	244	626	96	223	686	221	691	222	689		
502.gcc_r	96	216	630	215	631	215	631	96	178	763	178	763	178	763		
505.mcf_r	96	155	1000	155	1000	155	1000	96	155	1000	155	1000	155	1000		
520.omnetpp_r	96	294	429	293	430	290	435	96	294	429	293	430	290	435		
523.xalancbmk_r	96	106	956	106	957	107	950	96	106	956	106	957	107	950		
525.x264_r	96	101	1670	100	1680	100	1680	96	97.0	1730	96.5	1740	96.5	1740		
531.deepsjeng_r	96	204	538	204	539	204	539	96	204	538	204	539	204	539		
541.leela_r	96	354	449	354	449	354	449	96	354	449	354	449	354	449		
548.exchange2_r	96	140	1800	137	1830	138	1820	96	140	1800	137	1830	138	1820		
557.xz_r	96	328	316	328	316	329	316	96	328	316	328	316	329	316		

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Date: May-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jun-2024

Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025

General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on benchmark Sun May 18 12:46:40 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux benchmark 5.15.0-140-generic #150-Ubuntu SMP Sat Apr 12 06:00:09 UTC 2025 x86_64 x86_64 x86_64
GNU/Linux

2. w
12:46:40 up 6 min, 2 users, load average: 0.02, 0.53, 0.39
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
intel tty1 - 12:42 4:08 0.10s 0.01s -bash
intel pts/0 - 12:42 6.00s 0.96s 0.09s sudo su

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

Platform Notes (Continued)

3. Username

```
From environment variable $USER: root
From the command 'logname': intel
```

4. ulimit -a

```
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited
locked memory(kbytes) 66002056
process 2062124
nofiles 1024
vmemory(kbytes) unlimited
locks unlimited
rtprio 0
```

5. sysinfo process ancestry

```
/sbin/init
/bin/login -p --
-bash
sudo su
sudo su
su
bash
bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 -c
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=48 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 --configfile
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=48 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) PLATINUM 8568Y+
vendor_id       : GenuineIntel
cpu family      : 6
model          : 207
stepping        : 2
microcode       : 0x21000291
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrss bhi
cpu cores       : 48
siblings        : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-47
physical id 1: core ids 0-47
physical id 0: apicids
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72
,74,76,78,80,82,84,86,88,90,92,94
physical id 1: apicids
128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Date: May-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jun-2024

Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025

Platform Notes (Continued)

80,182,184,186,188,190,192,194,196,198,200,202,204,206,208,210,212,214,216,218,220,222

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:                                x86_64
CPU op-mode(s):                            32-bit, 64-bit
Address sizes:                             46 bits physical, 57 bits virtual
Byte Order:                                Little Endian
CPU(s):                                     96
On-line CPU(s) list:                      0-95
Vendor ID:                                 GenuineIntel
Model name:                               INTEL(R) XEON(R) PLATINUM 8568Y+
CPU family:                                6
Model:                                      207
Thread(s) per core:                       1
Core(s) per socket:                      48
Socket(s):                                2
Stepping:                                  2
BogoMIPS:                                 4600.00
Flags:                                     fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat
                                         pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                         pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                         nopl xtopology nonstop_tsc cpuid aperf mperf tsc_known_freq pn
                                         pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                                         xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                                         tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                                         3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
                                         intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                                         vnmi flexpriority ept vpid_ad fsgsbase tsc_adjust bmii1 avx2 smep
                                         bmii2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                         avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                         xsaveopt xsavec xgetbv1 xsaves cqmm_llc cqmm_occup_llc cqmm_mbm_total
                                         cqmm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm
                                         ida arat pln pts avx512vbmi umip pkru ospke waitpkg avx512_vbmi2 gfni
                                         vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57
                                         rdpid bus_lock_detect cldemote movdir64b enqcmd fsrm md_clear
                                         serialize tsxldtrk pconfig arch_lbr amx_bf16 avx512_fp16 amx_tile
                                         amx_int8 flush_lld arch_capabilities
                                         VT-x
                                         4.5 MiB (96 instances)
                                         3 MiB (96 instances)
                                         192 MiB (96 instances)
                                         600 MiB (2 instances)
                                         4
                                         0-23
                                         24-47
                                         48-71
                                         72-95
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected

```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Date: May-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jun-2024

Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025

Platform Notes (Continued)

Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	2M	192M	16	Unified	2	2048	1	64
L3	300M	600M	20	Unified	3	245760	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-23
node 0 size: 128641 MB
node 0 free: 128028 MB
node 1 cpus: 24-47
node 1 size: 129017 MB
node 1 free: 128533 MB
node 2 cpus: 48-71
node 2 size: 128970 MB
node 2 free: 128357 MB
node 3 cpus: 72-95
node 3 size: 129010 MB
node 3 free: 128346 MB
node distances:
node   0   1   2   3
 0: 10 12 21 21
 1: 12 10 21 21
 2: 21 21 10 12
 3: 21 21 12 10
```

9. /proc/meminfo

```
MemTotal:      528016472 kB
```

10. who -r

```
run-level 3 May 18 12:42
```

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)

```
Default Target      Status
multi-user          degraded
```

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* systemd-networkd-wait-online.service	loaded	failed	failed	Wait for Network to be Configured

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager apparmor atop atopacct binfmt-support blk-availability cloud-config

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Date: May-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jun-2024

Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025

Platform Notes (Continued)

```
cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd
getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lvm2-monitor
lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog
secureboot-db setvtrgb snapd ssh systemd-networkd systemd-networkd-wait-online
systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage
udisks2 ufw unattended-upgrades vgaauth
enabled-runtime netplan-ovs-cleanupsystemd-fsck-rootsystemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@
systemd-boot-check-no-failures systemd-network-generatorsystemd-sysext
systemd-time-wait-sync upower
generated apport
indirect uidd
masked cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo
x11-common
```

14. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/vmlinuz-5.15.0-140-generic
root=UUID=d23f4c96-e4ba-4003-a8ac-11587c2fe77c
ro
```

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

16. /sys/kernel/mm/transparent_hugepage

```
defrag always defer defer+madvise [madvise] never
enabled always [madvise] never
huge_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

Platform Notes (Continued)

18. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.5 LTS

19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p5 ext4 701G 75G 590G 12% /home

20. /sys/devices/virtual/dmi/id
Vendor: Tyrone Systems
Product: SDI200A3N-212
Product Family: Family
Serial: A495115X4412722

21. dmidecode
Additional information from dmidecode 3.3 follows. **WARNING:** Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x NO DIMM NO DIMM
16x Samsung M321R4GA0BB0-CQKET 32 GB 1 rank 4800

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 2.4
BIOS Date: 09/23/2024
BIOS Revision: 5.32

Compiler Version Notes

=====

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

Compiler Version Notes (Continued)

```
=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

```
=====
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
      | 541.leela_r(base, peak)
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

```
=====
Fortran | 548.exchange2_r(base, peak)
```

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

557.xz_r: basepeak = yes
```

C++ benchmarks:

```
520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes
```

Fortran benchmarks:

```
548.exchange2_r: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-EMR-revA.html>



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_int_base = 721

SPECrate®2017_int_peak = 745

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-EMR-revA.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-05-18 08:46:40-0400.

Report generated on 2025-06-17 18:12:14 by CPU2017 PDF formatter v6716.

Originally published on 2025-06-17.