



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9555P)

SPECrate®2017_fp_base = 847

SPECrate®2017_fp_peak = 849

CPU2017 License: 001176

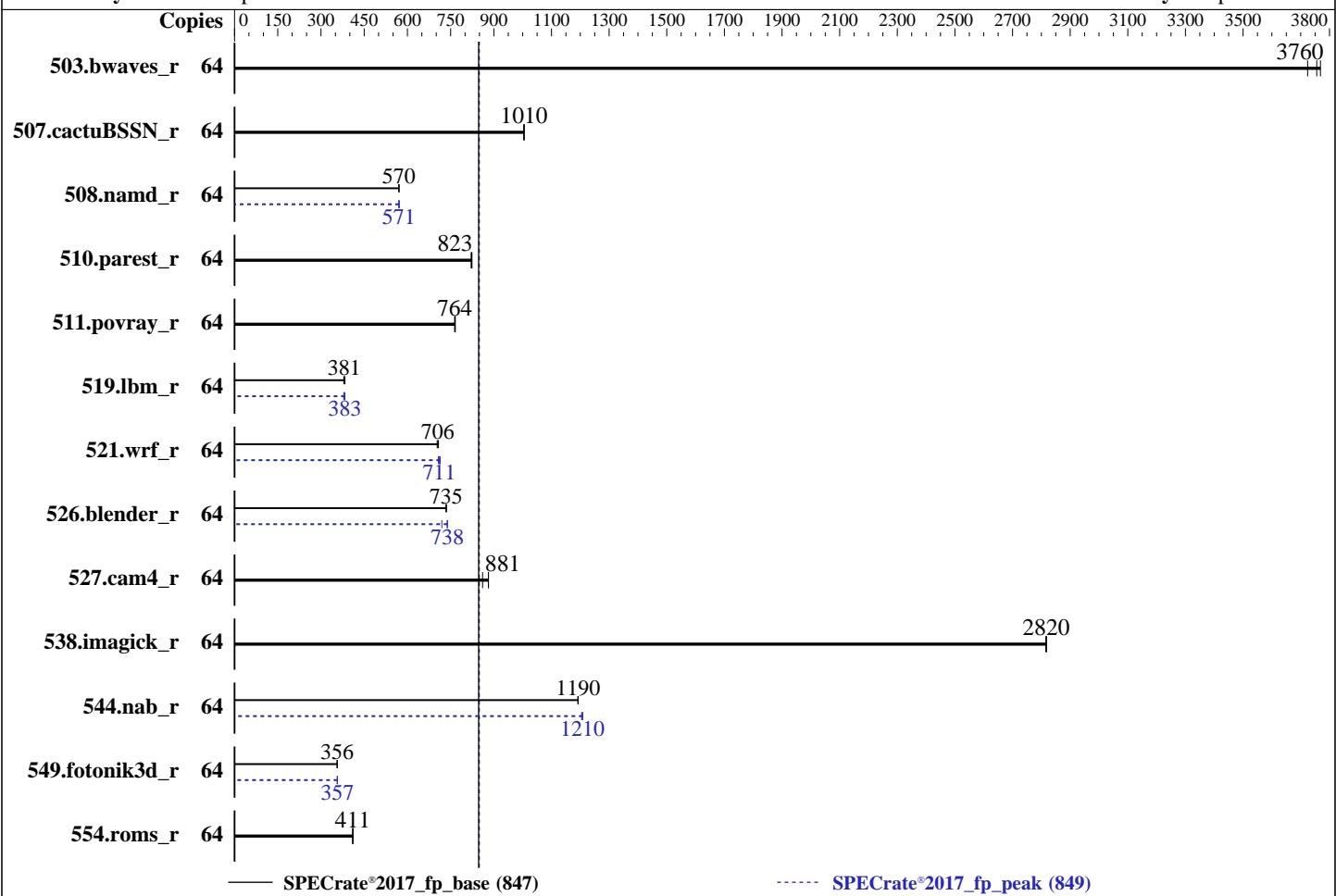
Test Date: May-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Apr-2025



— SPECrate®2017_fp_base (847)

----- SPECrate®2017_fp_peak (849)

Hardware

CPU Name: AMD EPYC 9555P
Max MHz: 4400
Nominal: 3200
Enabled: 64 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 6000)
Storage: 1 x 1.92 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04.2 LTS
Compiler: Kernel 6.8.0-59-generic
Parallel: C/C++/Fortran: Version 5.0.0 of AOCC
Firmware: No
File System: Version 3.5 released Apr-2025
System State: ext4
Base Pointers: Run level 3 (multi-user)
Peak Pointers: 64-bit
Other: 64-bit
Power Management: None
BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	170	3770	172	3720	171	3760	64	170	3770	172	3720	171	3760
507.cactusBSSN_r	64	80.6	1010	80.7	1000	80.6	1010	64	80.6	1010	80.7	1000	80.6	1010
508.namd_r	64	107	571	107	570	107	570	64	107	571	107	571	107	571
510.parest_r	64	204	821	203	823	203	824	64	204	821	203	823	203	824
511.povray_r	64	195	766	195	764	196	764	64	195	766	195	764	196	764
519.lbm_r	64	177	381	176	383	178	379	64	176	383	178	379	176	383
521.wrf_r	64	203	708	204	703	203	706	64	202	711	203	707	201	713
526.blender_r	64	133	735	133	734	133	735	64	132	739	132	738	135	719
527.cam4_r	64	127	881	127	881	130	861	64	127	881	127	881	130	861
538.imagick_r	64	56.5	2820	56.5	2820	56.5	2820	64	56.5	2820	56.5	2820	56.5	2820
544.nab_r	64	90.4	1190	90.3	1190	90.4	1190	64	89.4	1200	89.2	1210	89.2	1210
549.fotonik3d_r	64	700	356	699	357	700	356	64	699	357	699	357	699	357
554.roms_r	64	248	411	248	410	248	411	64	248	411	248	410	248	411

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
NUMA Nodes Per Socket = NPS4
Determinism Control = Manual
Determinism Enable = Power
TDP Control = Manual
TDP = 400
Package Power Limit Control = Manual
Package Power Limit = 400
SMT Control = Disabled

```
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on as-1115cs-tnr-9555p Tue May 6 02:18:08 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- ```
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.6)
```

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## Platform Notes (Continued)

12. Services, from systemctl list-unit-files

13. Linux kernel boot-time arguments, from /proc/cmdline

14. cpupower frequency-info

15. tuned-adm active

16. sysctl

17. /sys/kernel/mm/transparent\_hugepage

18. /sys/kernel/mm/transparent\_hugepage/khugepaged

19. OS release

20. Disk information

21. /sys/devices/virtual/dmi/id

22. dmidecode

23. BIOS

-----

1. uname -a

```
Linux as-1115cs-tnr-9555p 6.8.0-59-generic #61-Ubuntu SMP PREEMPT_DYNAMIC Fri Apr 11 23:16:11 UTC 2025
x86_64 x86_64 x86_64 GNU/Linux
```

2. w

```
02:18:08 up 1 min, 1 user, load average: 0.35, 0.17, 0.06
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
smc tty1 - 02:17 8.00s 0.08s 0.06s sudo su -
```

3. Username

```
From environment variable $USER: root
From the command 'logname': smc
```

4. ulimit -a

```
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited
locked memory(kbytes) 2097152
process 3094112
nofiles 1024
vmemory(kbytes) unlimited
locks unlimited
rtprio 0
```

5. sysinfo process ancestry

```
/sbin/init
/bin/login -p --
-bash
sudo su -
sudo su -
su -
-bash
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
```

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## Platform Notes (Continued)

```
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```

6. /proc/cpuinfo
 model name : AMD EPYC 9555P 64-Core Processor
 vendor_id : AuthenticAMD
 cpu family : 26
 model : 2
 stepping : 1
 microcode : 0xb002147
 bugs : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
 TLB size : 192 4K pages
 cpu cores : 64
 siblings : 64
 1 physical ids (chips)
 64 processors (hardware threads)
 physical id 0: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
 physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```

7. lscpu
```

```
From lscpu from util-linux 2.39.3:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Vendor ID: AuthenticAMD
BIOS Vendor ID: Advanced Micro Devices, Inc.
Model name: AMD EPYC 9555P 64-Core Processor
BIOS Model name: AMD EPYC 9555P 64-Core Processor
BIOS CPU family: Unknown CPU @ 3.2GHz
CPU family: 26
Model: 2
Thread(s) per core: 1
Core(s) per socket: 64
Socket(s): 1
Stepping: 1
Frequency boost: enabled
CPU(s) scaling MHz: 74%
CPU max MHz: 4409.3750
CPU min MHz: 1500.0000
BogoMIPS: 6390.37
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_nb bpext
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsqsbbase tsc_adjust bmi1 avx2
smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqmq_llc cqmq_occu_llc cqmq_mb_m_total
```

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## Platform Notes (Continued)

```
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero iperf
xsaverptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl
vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap
AMD-V
```

Virtualization:

|            |                       |
|------------|-----------------------|
| L1d cache: | 3 MiB (64 instances)  |
| L1i cache: | 2 MiB (64 instances)  |
| L2 cache:  | 64 MiB (64 instances) |

|           |                       |
|-----------|-----------------------|
| L3 cache: | 256 MiB (8 instances) |
|-----------|-----------------------|

|               |   |
|---------------|---|
| NUMA node(s): | 4 |
|---------------|---|

|                    |      |
|--------------------|------|
| NUMA node0 CPU(s): | 0-15 |
|--------------------|------|

|                    |       |
|--------------------|-------|
| NUMA node1 CPU(s): | 16-31 |
|--------------------|-------|

|                    |       |
|--------------------|-------|
| NUMA node2 CPU(s): | 32-47 |
|--------------------|-------|

|                    |       |
|--------------------|-------|
| NUMA node3 CPU(s): | 48-63 |
|--------------------|-------|

|                                     |              |
|-------------------------------------|--------------|
| Vulnerability Gather data sampling: | Not affected |
|-------------------------------------|--------------|

|                              |              |
|------------------------------|--------------|
| Vulnerability Itlb multihit: | Not affected |
|------------------------------|--------------|

|                     |              |
|---------------------|--------------|
| Vulnerability Llft: | Not affected |
|---------------------|--------------|

|                    |              |
|--------------------|--------------|
| Vulnerability Mds: | Not affected |
|--------------------|--------------|

|                         |              |
|-------------------------|--------------|
| Vulnerability Meltdown: | Not affected |
|-------------------------|--------------|

|                                |              |
|--------------------------------|--------------|
| Vulnerability Mmio stale data: | Not affected |
|--------------------------------|--------------|

|                                       |              |
|---------------------------------------|--------------|
| Vulnerability Reg file data sampling: | Not affected |
|---------------------------------------|--------------|

|                         |              |
|-------------------------|--------------|
| Vulnerability Retbleed: | Not affected |
|-------------------------|--------------|

|                                     |              |
|-------------------------------------|--------------|
| Vulnerability Spec rstack overflow: | Not affected |
|-------------------------------------|--------------|

|                                  |                                                         |
|----------------------------------|---------------------------------------------------------|
| Vulnerability Spec store bypass: | Mitigation; Speculative Store Bypass disabled via prctl |
|----------------------------------|---------------------------------------------------------|

|                           |                                                                      |
|---------------------------|----------------------------------------------------------------------|
| Vulnerability Spectre v1: | Mitigation; usercopy/swapgs barriers and __user pointer sanitization |
|---------------------------|----------------------------------------------------------------------|

|                           |                                                                |
|---------------------------|----------------------------------------------------------------|
| Vulnerability Spectre v2: | Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP |
|---------------------------|----------------------------------------------------------------|

|  |                                    |
|--|------------------------------------|
|  | disabled; RSB filling; PBRSB-eIBRS |
|--|------------------------------------|

|                      |              |
|----------------------|--------------|
| Vulnerability Srbds: | Not affected |
|----------------------|--------------|

|                                |              |
|--------------------------------|--------------|
| Vulnerability Tsx async abort: | Not affected |
|--------------------------------|--------------|

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 3M       | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 2M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 64M      | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 32M      | 256M     | 16   | Unified     | 3     | 32768 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-15

node 0 size: 193109 MB

node 0 free: 192706 MB

node 1 cpus: 16-31

node 1 size: 193487 MB

node 1 free: 193123 MB

node 2 cpus: 32-47

node 2 size: 193530 MB

node 2 free: 193139 MB

node 3 cpus: 48-63

node 3 size: 193475 MB

node 3 free: 192998 MB

node distances:

|                |
|----------------|
| node 0 1 2 3   |
| 0: 10 12 12 12 |

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## Platform Notes (Continued)

```
1: 12 10 12 12
2: 12 12 10 12
3: 12 12 12 10
```

```

9. /proc/meminfo
MemTotal: 792169048 kB
```

```

10. who -r
run-level 3 May 6 02:16
```

```

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.6)
Default Target Status
multi-user running
```

```

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
grub-common grub-initrd-fallback keyboard-setup lvm2-monitor multipathd
networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
snapd sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore
systemd-resolved systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2
ufw unattended-upgrades vauth
enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
systemd-boot-check-no-failures systemd-confext systemd-network-generator
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
systemd-time-wait-sync upower
indirect systemd-sysupdate systemd-sysupdate-reboot uuidd
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common
```

```

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.8.0-59-generic
root=UUID=01bed86d-3d45-45cd-b1d7-0ecb5e8d0cc0
ro
```

```

14. cpupower frequency-info
analyzing CPU 42:
current policy: frequency should be within 1.50 GHz and 3.20 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 3200MHz
```

```

15. tuned-adm active
Current active profile: throughput-performance
```

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## Platform Notes (Continued)

```

16. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1
```

```

17. /sys/kernel/mm/transparent_hugepage
defrag [always] defer defer+madvise madvise never
enabled [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

```

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
```

```

19. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04.2 LTS
```

```

20. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 ext4 1.8T 16G 1.7T 1% /
```

```

21. /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: H13SSW-P
Product Family: SMC H13
Serial: WM252S601370
```

```

22. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
```

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## Platform Notes (Continued)

The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x Samsung M321R8GA0EB2-CCPKC 64 GB 2 rank 6400, configured at 6000

-----  
23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.

BIOS Version: 3.5

BIOS Date: 04/30/2025

BIOS Revision: 5.35

## Compiler Version Notes

=====

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C, Fortran | 507.cactusBSSN\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

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# SPEC CPU®2017 Floating Point Rate Result

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**Supermicro**

CloudDC A+ Server AS -1115CS-TNR  
(H13SSW , AMD EPYC 9555P)

**SPECrate®2017\_fp\_base = 847**

**SPECrate®2017\_fp\_peak = 849**

**CPU2017 License:** 001176

**Test Date:** May-2025

**Test Sponsor:** Supermicro

**Hardware Availability:** Oct-2024

**Tested by:** Supermicro

**Software Availability:** Apr-2025

## Compiler Version Notes (Continued)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

CloudDC A+ Server AS -1115CS-TNR  
(H13SSW , AMD EPYC 9555P)

SPECrate®2017\_fp\_base = 847

SPECrate®2017\_fp\_peak = 849

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: May-2025

Hardware Availability: Oct-2024

Software Availability: Apr-2025

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactubSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
```

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# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

CloudDC A+ Server AS -1115CS-TNR  
(H13SSW , AMD EPYC 9555P)

SPECrate®2017\_fp\_base = 847

SPECrate®2017\_fp\_peak = 849

CPU2017 License: 001176

Test Date: May-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Apr-2025

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-lflang -ldl

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

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# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

CloudDC A+ Server AS -1115CS-TNR  
(H13SSW , AMD EPYC 9555P)

SPECrate®2017\_fp\_base = 847

SPECrate®2017\_fp\_peak = 849

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: May-2025

Hardware Availability: Oct-2024

Software Availability: Apr-2025

## Base Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags



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## Supermicro

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CPU2017 License: 001176

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Tested by: Supermicro

Test Date: May-2025

Hardware Availability: Oct-2024

Software Availability: Apr-2025

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

```
538.imagick_r: basepeak = yes
```

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

```
510.parest_r: basepeak = yes
```

Fortran benchmarks:

```
503.bwaves_r: basepeak = yes
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdaloc -ldl -lflang
```

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## Supermicro

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CPU2017 License: 001176

Test Date: May-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Apr-2025

## Peak Optimization Flags (Continued)

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fefilop-vectorization-of-inductions -lamdlibm -lamdalloc
-lld -lflang
```

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

511.povray\_r: basepeak = yes

```
526.blender_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -lld
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

CloudDC A+ Server AS -1115CS-TNR  
(H13SSW , AMD EPYC 9555P)

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SPECrate®2017\_fp\_peak = 849

CPU2017 License: 001176

Test Date: May-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Apr-2025

## Peak Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Turin-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Turin-revD.xml>

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