SPEC CPU®2017 Floating Point Speed Result

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Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9115 2.60 GHz Processor)

$SPECspeed^{®}2017\_fp\_base = 290$

$SPECspeed^{®}2017\_fp\_peak = 291$

CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Oct-2024

Software Availability: Sep-2024

Threads scale: 0 45.0 90.0 135 180 225 270 315 360 405 450 495 540 585 630 675 720 765 810 855 900 975

Benchmark	Threads	base	peak
603.bwaves_s	32	958	968
607.cactuBSSN_s	32	386	395
619.lbm_s	32	238	
621.wrf_s	32	217	
627.cam4_s	32	163	
628.pop2_s	32	94.1	94.8
638.imagick_s	32	343	344
644.nab_s	32	456	457
649.fotonik3d_s	32	206	
654.roms_s	32	439	444

SPECspeed®2017_fp_base (290)   SPECspeed®2017_fp_peak (291)

Hardware

CPU Name: AMD EPYC 9115
 Max MHz: 4100
 Nominal: 2600
Enabled: 32 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 64 MB I+D on chip per chip, 32 MB shared / 8 cores
 Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R, running at 6000)
Storage: 1 x 960 GB SATA SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6 kernel version 6.4.0-150600.21-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: Version 4.3.5f released Feb-2025
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

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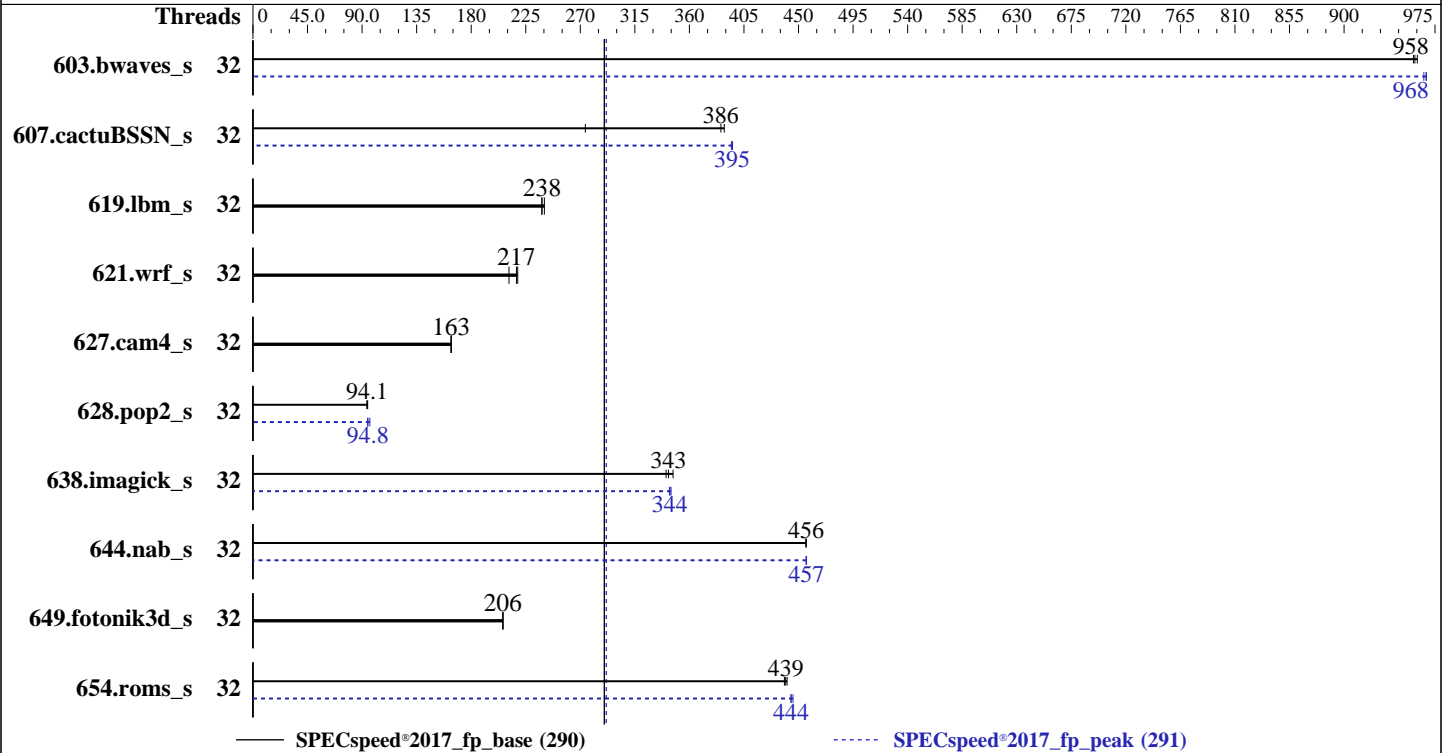
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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	61.4	960	61.6	957	**61.6**	**958**	32	61.0	968	**61.0**	**968**	61.1	966
607.cactuBSSN_s	32	60.8	274	**43.2**	**386**	42.9	389	32	42.1	396	42.2	395	**42.2**	**395**
619.lbm_s	32	21.8	240	**22.0**	**238**	22.0	238	32	21.8	240	**22.0**	**238**	22.0	238
621.wrf_s	32	60.6	218	62.6	211	**60.9**	**217**	32	60.6	218	62.6	211	**60.9**	**217**
627.cam4_s	32	**54.2**	**163**	54.2	164	54.2	163	32	**54.2**	**163**	54.2	164	54.2	163
628.pop2_s	32	**126**	**94.1**	126	94.1	125	94.6	32	123	96.4	**125**	**94.8**	125	94.7
638.imagick_s	32	42.3	341	41.6	347	**42.1**	**343**	32	42.0	343	41.8	345	**41.9**	**344**
644.nab_s	32	**38.3**	**456**	38.3	456	38.3	456	32	38.3	456	38.3	457	**38.3**	**457**
649.fotonik3d_s	32	**44.2**	**206**	44.2	206	44.2	206	32	**44.2**	**206**	44.2	206	44.2	206
654.roms_s	32	35.7	441	**35.8**	**439**	35.9	438	32	35.5	444	**35.5**	**444**	35.3	445
SPECspeed®2017_fp_base =					**290**									
SPECspeed®2017_fp_peak =					**291**									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

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Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-31"
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "32"

Environment variables set by runcpu during the 603.bwaves_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 628.pop2_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 638.imagick_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 644.nab_s peak run:
GOMP_CPU_AFFINITY = "0-31"

Environment variables set by runcpu during the 654.roms_s peak run:
GOMP_CPU_AFFINITY = "0-31"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS settings:
SMT Mode set to Disabled
NUMA nodes per socket set to NPS1
Determinism Slider set to Power
DF C-States set to Disabled
Enhanced CPU performance set to Auto

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Wed Apr 30 07:59:41 2025

 SUT (System Under Test) info as seen by some common utilities.
```

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Test Date: Jan-2025

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Platform Notes (Continued)

```
--------------------------------------------------------------
Table of contents
--------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
--------------------------------------------------------------


--------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
  x86_64 x86_64 x86_64 GNU/Linux


--------------------------------------------------------------
2. w
   07:59:41 up 14:24,  2 users,  load average: 0.00, 0.00, 0.00
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                Tue17    21.00s  0.86s  0.09s /bin/bash ./amd_speed_aocc500_znver5_A1.sh


--------------------------------------------------------------
3. Username
  From environment variable $USER:  root


--------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 6190944
  max locked memory       (kbytes, -l) 2097152
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 6190944
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited
```

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Platform Notes (Continued)

```
--------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=42
 login -- root
 -bash
 python3 ./run_amd_speed_aocc500_znver5_A1.py -b fpspeed
 /bin/bash ./amd_speed_aocc500_znver5_A1.sh
 runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
 runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
   --runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017


--------------------------------------------------------------
6. /proc/cpuinfo
   model name      : AMD EPYC 9115 16-Core Processor
   vendor_id       : AuthenticAMD
   cpu family      : 26
   model           : 2
   stepping        : 1
   microcode       : 0xb00211e
   bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
   TLB size        : 192 4K pages
   cpu cores       : 16
   siblings        : 16
   2 physical ids (chips)
   32 processors (hardware threads)
   physical id 0: core ids 0-7,16-23
   physical id 1: core ids 0-7,16-23
   physical id 0: apicids 0-7,16-23
   physical id 1: apicids 32-39,48-55
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.


--------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                 x86_64
  CPU op-mode(s):               32-bit, 64-bit
  Address sizes:                52 bits physical, 57 bits virtual
  Byte Order:                   Little Endian
  CPU(s):                       32
  On-line CPU(s) list:          0-31
  Vendor ID:                    AuthenticAMD
  BIOS Vendor ID:               Advanced Micro Devices, Inc.
  Model name:                   AMD EPYC 9115 16-Core Processor
  BIOS Model name:              AMD EPYC 9115 16-Core Processor          Unknown CPU @ 2.6GHz
  BIOS CPU family:              107
  CPU family:                   26
  Model:                        2
  Thread(s) per core:           1
  Core(s) per socket:           16
  Socket(s):                    2
  Stepping:                     1
  Frequency boost:              enabled
  CPU(s) scaling MHz:           71%
  CPU max MHz:                  4115.8198
```

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Platform Notes (Continued)

```
  CPU min MHz:                         1500.0000
  BogoMIPS:                            5192.18
  Flags:                               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                       pse36 clflush mmx fxsr sse sse2 ht syscall nx mmext fxsr_opt pdpe1gb
                                       rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                       extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                       sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                       cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                       osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                       perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                       ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                                       smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                       avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                                       xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                       cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                                       xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                                       nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                                       pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                                       avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                                       avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                                       movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
                                       flush_l1d debug_swap
  Virtualization:                      AMD-V
  L1d cache:                           1.5 MiB (32 instances)
  L1i cache:                           1 MiB (32 instances)
  L2 cache:                            32 MiB (32 instances)
  L3 cache:                            128 MiB (4 instances)
  NUMA node(s):                        2
  NUMA node0 CPU(s):                   0-15
  NUMA node1 CPU(s):                   16-31
  Vulnerability Gather data sampling:  Not affected
  Vulnerability Itlb multihit:         Not affected
  Vulnerability L1tf:                  Not affected
  Vulnerability Mds:                   Not affected
  Vulnerability Meltdown:              Not affected
  Vulnerability Mmio stale data:       Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:              Not affected
  Vulnerability Spec rstack overflow:  Not affected
  Vulnerability Spec store bypass:     Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:            Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:            Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                       disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
  Vulnerability Srbds:                 Not affected
  Vulnerability Tsx async abort:       Not affected

  From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE         LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d      48K     1.5M  12 Data             1    64       1              64
      L1i      32K       1M   8 Instruction      1    64       1              64
      L2        1M      32M  16 Unified          2  1024       1              64
      L3       32M     128M  16 Unified          3 32768       1              64

  -----------------------------------------------------------
  8. numactl --hardware
  NOTE: a numactl 'node' might or might not correspond to a physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0-15
    node 0 size: 773682 MB
    node 0 free: 772756 MB
```

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Platform Notes (Continued)

```
   node 1 cpus: 16-31
   node 1 size: 774079 MB
   node 1 free: 773309 MB
   node distances:
   node   0   1
     0:  10  32
     1:  32  10


  ------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584908756 kB


  ------------------------------------------------------------
10. who -r
   run-level 3 Apr 29 17:35


  ------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
   Default Target  Status
   multi-user      running


  ------------------------------------------------------------
12. Services, from systemctl list-unit-files
   STATE            UNIT FILES
   enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
                    issue-generator kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections
                    nvmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                    virtqemud wicked wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
   enabled-runtime  systemd-remount-fs
   disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                    chronyd console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info
                    firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd iscsi-init iscsid
                    issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests lunmask man-db-create multipathd
                    nfs nfs-blkmap nfs-server nfsserver rpcbind rpmconfigcheck rsyncd rtkit-daemon
                    serial-getty@ smartd_generate_opts snmpd snmptrapd strongswan strongswan-starter svnserve
                    systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@
                    systemd-sysext systemd-time-wait-sync systemd-timesyncd tcsd udisks2 virtinterfaced
                    virtlockd virtlogd virtnetworkd virtnodedevd virtnwfilterd virtsecretd virtstoraged
   indirect         pcscd systemd-userdbd tftp wickedd

  ------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
   BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
   root=UUID=049c38d8-cbd8-44f5-87bc-70d9d0d63c0a
   splash=silent
   mitigations=auto
   quiet
   security=apparmor


  ------------------------------------------------------------
14. cpupower frequency-info
   analyzing CPU 8:
     current policy: frequency should be within 1.50 GHz and 2.60 GHz.
                     The governor "performance" may decide which speed to use
                     within this range.
     boost state support:
       Supported: yes
       Active: yes


  ------------------------------------------------------------
```

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Platform Notes (Continued)

```
15. sysctl
  kernel.numa_balancing            1
  kernel.randomize_va_space        0
  vm.compaction_proactiveness      20
  vm.dirty_background_bytes        0
  vm.dirty_background_ratio        10
  vm.dirty_bytes                   0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                   8
  vm.dirty_writeback_centisecs     500
  vm.dirtytime_expire_seconds      43200
  vm.extfrag_threshold             500
  vm.min_unmapped_ratio            1
  vm.nr_hugepages                  0
  vm.nr_hugepages_mempolicy        0
  vm.nr_overcommit_hugepages       0
  vm.swappiness                    1
  vm.watermark_boost_factor        15000
  vm.watermark_scale_factor        10
  vm.zone_reclaim_mode             1


----------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag         [always] defer defer+madvise madvise never
  enabled        [always] madvise never
  hpage_pmd_size 2097152
  shmem_enabled   always within_size advise [never] deny force


----------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                  1
  max_ptes_none           511
  max_ptes_shared         256
  max_ptes_swap           64
  pages_to_scan           4096
  scan_sleep_millisecs    10000


----------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6


----------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017
  Filesystem     Type   Size  Used Avail Use% Mounted on
  /dev/nvme1n1p3 btrfs  929G   11G  917G   2% /home


----------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Cisco Systems Inc
    Product:        UCSX-215C-M8
    Product Family: Cisco UCS Rack Server
    Serial:         FCH282172ES


----------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
```

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Platform Notes (Continued)

```
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     10x 0xCE00 M321R8GA0PB2-CCPEC 64 GB 2 rank 6400, configured at 6000
     14x 0xCE00 M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 6000


  --------------------------------------------------------------
  22. BIOS
  (This section combines info from /sys/devices and dmidecode.)
     BIOS Vendor:        Cisco Systems, Inc.
     BIOS Version:       X215M8.4.3.5f.0.0218251523
     BIOS Date:          02/18/2025
     BIOS Revision:      5.35
```

Compiler Version Notes

```
==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------


==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------


==============================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

(Continued on next page)

SPEC CPU®2017 Floating Point Speed Result

Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9115
2.60 GHz Processor)

SPECspeed®2017_fp_base = 290

SPECspeed®2017_fp_peak = 291

Compiler Version Notes (Continued)

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
clang
```

Fortran benchmarks:
```
flang
```

Benchmarks using both Fortran and C:
```
flang clang
```

Benchmarks using Fortran, C, and C++:
```
clang++ clang flang
```

Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:
```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

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Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9115 2.60 GHz Processor)

SPECspeed®2017_fp_base = 290

SPECspeed®2017_fp_peak = 291

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Jan-2025
Hardware Availability: Oct-2024
Software Availability: Sep-2024

Base Optimization Flags (Continued)

Fortran benchmarks:
```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Benchmarks using both Fortran and C:
```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:
```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Base Other Flags

C benchmarks:
```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:
```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:
```
-Wno-return-type -Wno-unused-command-line-argument
```

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SPEC CPU®2017 Floating Point Speed Result

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Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9115 2.60 GHz Processor)

SPECspeed®2017_fp_base = 290

SPECspeed®2017_fp_peak = 291

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Jan-2025
Hardware Availability: Oct-2024
Software Availability: Sep-2024

Base Other Flags (Continued)

Benchmarks using Fortran, C, and C++:
```
-Wno-return-type -Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:
```
clang
```

Fortran benchmarks:
```
flang
```

Benchmarks using both Fortran and C:
```
flang clang
```

Benchmarks using Fortran, C, and C++:
```
clang++ clang flang
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp`
`-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining`
`-fstruct-layout=9 -mllvm -inline-threshold=1000`
`-mllvm -reduce-array-computations=3`
`-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp`
`-lamdlibm -lamdalloc -lflang`

644.nab_s: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`

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Software Availability: Sep-2024

Peak Optimization Flags (Continued)

644.nab_s (continued):
```
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

603.bwaves_s:
```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

649.fotonik3d_s: basepeak = yes

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s:
```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fscalar-transform
-fvector-transform -Mrecursive -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:
```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -DSPEC_OPENMP
-fremap-arrays -fstrip-mining -fstruct-layout=9
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
```

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SPEC CPU®2017 Floating Point Speed Result

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Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
```
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Peak Other Flags

C benchmarks:
```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:
```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:
```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:
```
-Wno-return-type -Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at
```
http://www.spec.org/cpu2017/flags/aocc500-flags.html
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.html
```

You can also download the XML flags sources by saving the following links:
```
http://www.spec.org/cpu2017/flags/aocc500-flags.xml
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-Turin-v1.1-revG.xml
```

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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