



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Esconet Technologies Ltd.

Hexadata HD-RS4000 Ver: SPR-002
(Intel Xeon Gold 6426Y, 2.50 GHz)

SPECspeed®2017_fp_base = 232

SPECspeed®2017_fp_peak = 232

CPU2017 License: 6523

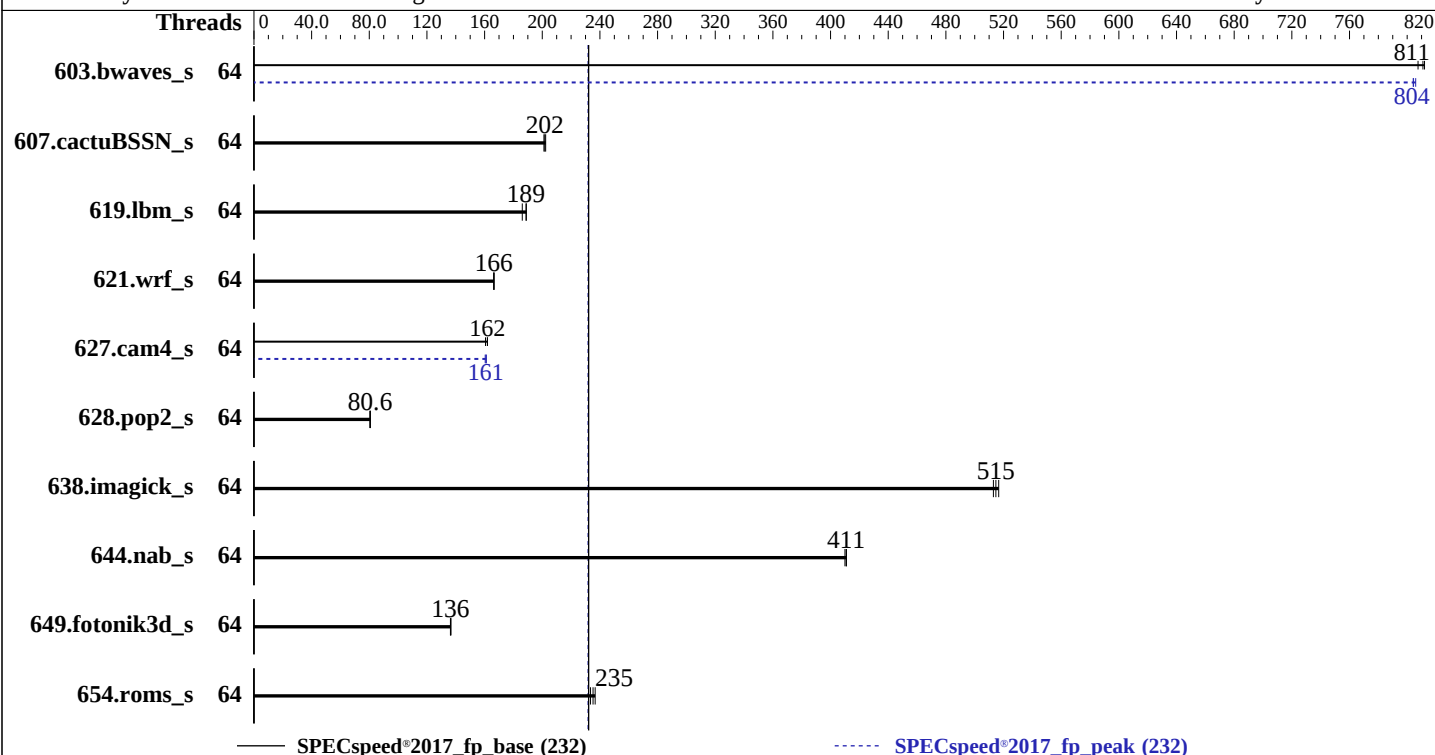
Test Sponsor: Esconet Technologies Ltd.

Tested by: Esconet Technologies Ltd.

Test Date: Apr-2025

Hardware Availability: Apr-2023

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon Gold 6426Y
Max MHz: 4100
Nominal: 2500
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 37.5 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 1Rx4 PC5-5600B-R, running at 4800)
Storage: 125 GB on tmpfs
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
6.4.0-150600.21-default
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2023.2.3 of Intel Fortran
Compiler for Linux;
Parallel: Yes
Firmware: Version 10.10 released Mar-2024
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: OS set to prefer performance
at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	72.7	812	73.1	808	<u>72.8</u>	<u>811</u>	64	73.4	804	<u>73.3</u>	<u>804</u>	73.2	806
607.cactuBSSN_s	64	<u>82.6</u>	<u>202</u>	82.4	202	82.9	201	64	<u>82.6</u>	<u>202</u>	82.4	202	82.9	201
619.lbm_s	64	27.7	189	28.1	186	<u>27.8</u>	<u>189</u>	64	27.7	189	28.1	186	<u>27.8</u>	<u>189</u>
621.wrf_s	64	79.5	166	<u>79.5</u>	<u>166</u>	79.4	167	64	79.5	166	<u>79.5</u>	<u>166</u>	79.4	167
627.cam4_s	64	<u>54.7</u>	<u>162</u>	54.7	162	55.2	161	64	55.3	160	<u>55.1</u>	<u>161</u>	55.0	161
628.pop2_s	64	147	80.8	<u>147</u>	<u>80.6</u>	148	80.5	64	147	80.8	<u>147</u>	<u>80.6</u>	148	80.5
638.imagick_s	64	<u>28.0</u>	<u>515</u>	28.1	513	27.9	517	64	<u>28.0</u>	<u>515</u>	28.1	513	27.9	517
644.nab_s	64	<u>42.5</u>	<u>411</u>	42.5	411	42.6	410	64	<u>42.5</u>	<u>411</u>	42.5	411	42.6	410
649.fotonik3d_s	64	66.7	137	<u>66.8</u>	<u>136</u>	66.9	136	64	66.7	137	<u>66.8</u>	<u>136</u>	66.9	136
654.roms_s	64	67.4	233	<u>66.9</u>	<u>235</u>	66.5	237	64	67.4	233	<u>66.9</u>	<u>235</u>	66.5	237

SPECspeed®2017_fp_base = 232

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/mnt/ramdisk/cpu17/lib/intel64:/mnt/ramdisk/cpu17/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"



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Software Availability: Jun-2024

Platform Notes

BIOS settings: Default

Sysinfo program /mnt/ramdisk/cpu17/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Apr 28 11:03:12 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
11:03:12 up 1 day, 16:29, 1 user, load average: 0.08, 0.02, 0.01
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - Sun02 2.00s 0.85s 0.00s sh
reportable-ic2023.2.3-lin-sapphirerapids-speed-smt-on-20231121.sh

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2060590
max locked memory (kbytes, -l) 8192

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Platform Notes (Continued)

max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2060590
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
login -- root
-bash
sh reportable-ic2023.2.3-lin-sapphirerapids-speed-smt-on-20231121.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=64 --tune base,peak -o all --define smt-on
--define drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=64 --tune base,peak --output_format all
--define smt-on --define drop_caches --nopower --runmode speed --tune base:peak --size refspeak fpspeed
--nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.005/temlogs/preenv.fpspeed.005.0.log --lognum 005.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu17
-----
```

```
-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6426Y
vendor_id       : GenuineIntel
cpu family      : 6
model           : 143
stepping        : 8
microcode       : 0x2b000590
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
cpu cores       : 16
siblings        : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0:  core ids 0-15
physical id 1:  core ids 0-15
physical id 0:  apicids 0-31
physical id 1:  apicids 128-159
-----
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	64
On-line CPU(s) list:	0-63
Vendor ID:	GenuineIntel
BIOS Vendor ID:	Intel(R) Corporation

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Platform Notes (Continued)

Model name: Intel(R) Xeon(R) Gold 6426Y
BIOS Model name: Intel(R) Xeon(R) Gold 6426Y CPU @ 2.5GHz
BIOS CPU family: 179
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
Stepping: 8
CPU(s) scaling MHz: 35%
CPU max MHz: 4100.0000
CPU min MHz: 800.0000
BogoMIPS: 5000.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req hfi vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d arch_capabilities
Virtualization: VT-x
L1d cache: 1.5 MiB (32 instances)
L1i cache: 1 MiB (32 instances)
L2 cache: 64 MiB (32 instances)
L3 cache: 75 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-15,32-47
NUMA node1 CPU(s): 16-31,48-63
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64

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Platform Notes (Continued)

L1i	32K	1M	8 Instruction	1	64	1	64
L2	2M	64M	16 Unified	2	2048	1	64
L3	37.5M	75M	15 Unified	3	40960	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-15,32-47
node 0 size: 257538 MB
node 0 free: 218885 MB
node 1 cpus: 16-31,48-63
node 1 size: 257635 MB
node 1 free: 233157 MB
node distances:
node  0  1
  0: 10  21
  1: 21  10
```

9. /proc/meminfo

MemTotal: 527537984 kB

10. who -r

run-level 3 Apr 27 02:29

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump kdump-early kdump-notify postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables fsidd grub2-once haveged issue-add-ssh-keys kexec-load lunmask nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysexec systemd-time-wait-sync systemd-timesyncd
indirect	systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=10b1c3ae-020b-49a2-af15-07ade4c24542
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=383M,high
crashkernel=72M,low
```

14. cpupower frequency-info

analyzing CPU 43:
current policy: frequency should be within 800 MHz and 4.10 GHz.

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Platform Notes (Continued)

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

```
-----
15. sysctl
kernel.numa_balancing      1
kernel.randomize_va_space  2
vm.compaction_proactiveness 20
vm.dirty_background_bytes   0
vm.dirty_background_ratio   10
vm.dirty_bytes              0
vm.dirty_expire_centisecs   3000
vm.dirty_ratio              20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio       1
vm.nr_hugepages              0
vm.nr_hugepages_mempolicy   0
vm.nr_overcommit_hugepages  0
vm.swappiness                60
vm.watermark_boost_factor    15000
vm.watermark_scale_factor    10
vm.zone_reclaim_mode         0
-----
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag      always defer defer+madvice [madvice] never
enabled     [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
-----
```

```
-----
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu17
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs            tmpfs 125G   57G   69G  46% /mnt/ramdisk
-----
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:    HEXADATA
Product:   HD-RS4000
-----
```

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Platform Notes (Continued)

21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Hynix TR532G56S446 32 GB 1 rank 5600, configured at 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.

BIOS Version: 10.10

BIOS Date: 03/26/2024

BIOS Revision: 5.32

Compiler Version Notes

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x

Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x

Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x

Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x

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Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

607.cactuBSSN_s: -DSPEC_LP64

619.lbm_s: -DSPEC_LP64

621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG

628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

-assume byterecl

638.imagick_s: -DSPEC_LP64

644.nab_s: -DSPEC_LP64

649.fotonik3d_s: -DSPEC_LP64

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math

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SPEC CPU®2017 Floating Point Speed Result

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Esconet Technologies Ltd.

Hexadata HD-RS4000 Ver: SPR-002
(Intel Xeon Gold 6426Y, 2.50 GHz)

SPECspeed®2017_fp_base = 232

SPECspeed®2017_fp_peak = 232

CPU2017 License: 6523

Test Sponsor: Esconet Technologies Ltd.

Tested by: Esconet Technologies Ltd.

Test Date: Apr-2025

Hardware Availability: Apr-2023

Software Availability: Jun-2024

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

(Continued on next page)



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Peak Optimization Flags (Continued)

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Hexadata-Platform-Flags-Intel-rev1.7.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Hexadata-Platform-Flags-Intel-rev1.7.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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