



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176

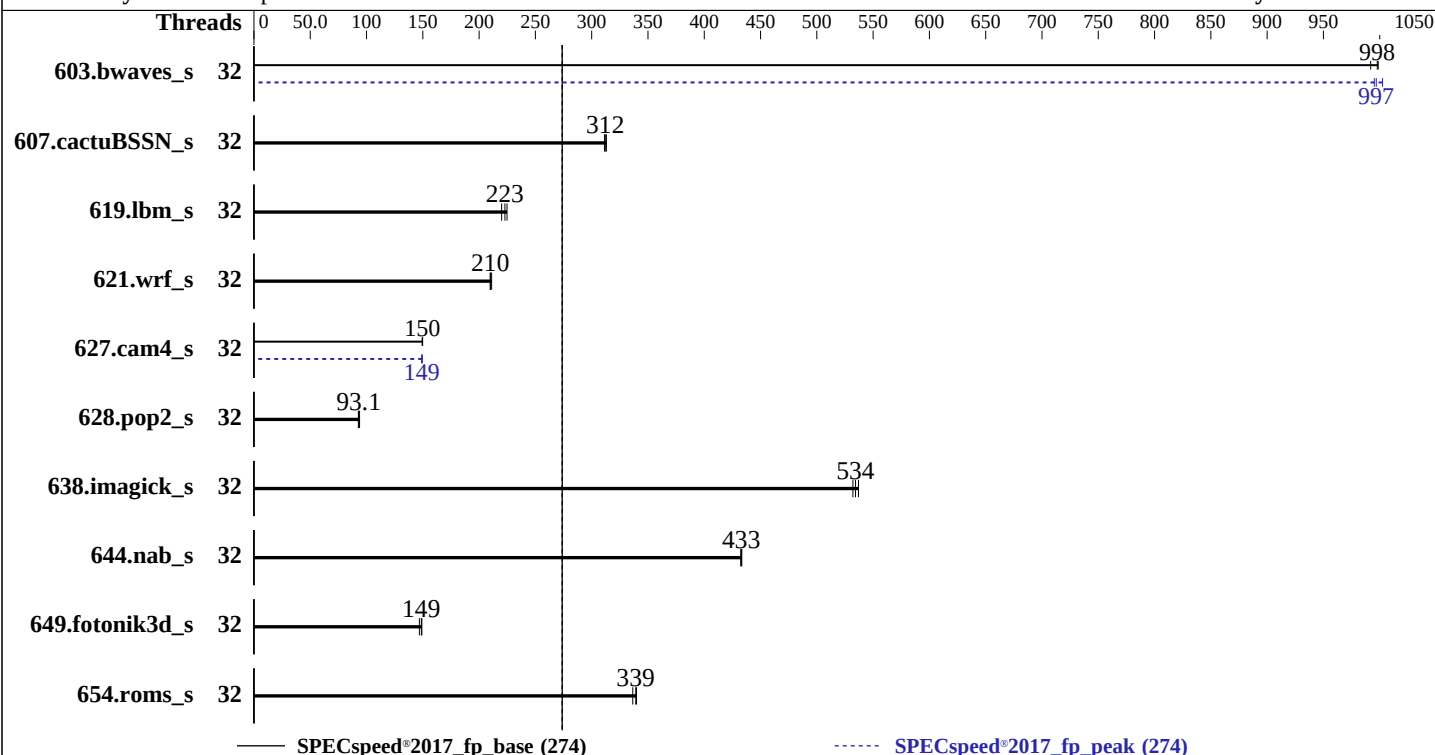
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Apr-2025

Hardware Availability: Dec-2023

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon Gold 6544Y
Max MHz: 4100
Nominal: 3600
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 45 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 5200)
Storage: 1 x 1.6 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
Kernel 6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: Version 2.5 released Feb-2025
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Apr-2025

Hardware Availability: Dec-2023

Software Availability: Jun-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	59.5	992	59.1	999	<u>59.1</u>	<u>998</u>	32	58.8	1000	59.3	995	<u>59.2</u>	<u>997</u>
607.cactuBSSN_s	32	53.5	311	53.2	313	<u>53.4</u>	<u>312</u>	32	53.5	311	53.2	313	<u>53.4</u>	<u>312</u>
619.lbm_s	32	23.8	220	23.3	225	<u>23.5</u>	<u>223</u>	32	23.8	220	23.3	225	<u>23.5</u>	<u>223</u>
621.wrf_s	32	62.6	211	<u>63.0</u>	<u>210</u>	63.0	210	32	62.6	211	<u>63.0</u>	<u>210</u>	63.0	210
627.cam4_s	32	59.1	150	59.3	149	<u>59.2</u>	<u>150</u>	32	59.3	149	59.5	149	<u>59.4</u>	<u>149</u>
628.pop2_s	32	127	93.8	128	92.7	<u>127</u>	<u>93.1</u>	32	127	93.8	128	92.7	<u>127</u>	<u>93.1</u>
638.imagick_s	32	<u>27.0</u>	<u>534</u>	27.1	532	26.9	537	32	<u>27.0</u>	<u>534</u>	27.1	532	26.9	537
644.nab_s	32	40.3	433	40.4	432	<u>40.4</u>	<u>433</u>	32	40.3	433	40.4	432	<u>40.4</u>	<u>433</u>
649.fotonik3d_s	32	62.0	147	61.1	149	<u>61.3</u>	<u>149</u>	32	62.0	147	61.1	149	<u>61.3</u>	<u>149</u>
654.roms_s	32	46.8	337	<u>46.4</u>	<u>339</u>	46.3	340	32	46.8	337	<u>46.4</u>	<u>339</u>	46.3	340

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Apr-2025
Hardware Availability: Dec-2023
Software Availability: Jun-2024

Platform Notes

BIOS Configuration:
Workload Profile = HPC
SNC = Enable SNC2 (2-clusters)
LLC Dead Line Alloc = Disable
KTI Prefetch = Enable
Stale AtoS = Disable
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed Apr 2 13:18:15 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
13:18:15 up 13:42, 1 user, load average: 6.31, 5.90, 3.51
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - Tue23 13:24m 0.88s 0.01s -bash

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Apr-2025

Hardware Availability: Dec-2023

Software Availability: Jun-2024

Platform Notes (Continued)

data seg size	(kbytes, -d)	unlimited
scheduling priority	(-e)	0
file size	(blocks, -f)	unlimited
pending signals	(-i)	4126899
max locked memory	(kbytes, -l)	8192
max memory size	(kbytes, -m)	unlimited
open files	(-n)	1024
pipe size	(512 bytes, -p)	8
POSIX message queues	(bytes, -q)	819200
real-time priority	(-r)	0
stack size	(kbytes, -s)	unlimited
cpu time	(seconds, -t)	unlimited
max user processes	(-u)	4126899
virtual memory	(kbytes, -v)	unlimited
file locks	(-x)	unlimited

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
ic2024.1-lin-core-avx512-speed-20240308.cfg --define cores=32 --tune base,peak -o all --define smt-on
--define drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2024.1-lin-core-avx512-speed-20240308.cfg --define cores=32 --tune base,peak --output_format all
--define smt-on --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed
--nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.004/tempslogs/preenv.fpspeed.004.0.log --lognum 004.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) GOLD 6544Y
vendor_id       : GenuineIntel
cpu family      : 6
model           : 207
stepping        : 2
microcode       : 0x21000291
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
cpu cores       : 16
siblings        : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0:  core ids 0-15
physical id 1:  core ids 0-15
physical id 0:  apicids 0-31
physical id 1:  apicids 128-159
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	46 bits physical, 57 bits virtual

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Apr-2025
Hardware Availability: Dec-2023
Software Availability: Jun-2024

Platform Notes (Continued)

Byte Order:	Little Endian
CPU(s):	64
On-line CPU(s) list:	0-63
Vendor ID:	GenuineIntel
BIOS Vendor ID:	Intel(R) Corporation
Model name:	INTEL(R) XEON(R) GOLD 6544Y
BIOS Model name:	INTEL(R) XEON(R) GOLD 6544Y CPU @ 3.6GHz
BIOS CPU family:	179
CPU family:	6
Model:	207
Thread(s) per core:	2
Core(s) per socket:	16
Socket(s):	2
Stepping:	2
BogoMIPS:	7200.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect clDEMote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_lid arch_capabilities
Virtualization:	VT-x
L1d cache:	1.5 MiB (32 instances)
L1i cache:	1 MiB (32 instances)
L2 cache:	64 MiB (32 instances)
L3 cache:	90 MiB (2 instances)
NUMA node(s):	4
NUMA node0 CPU(s):	0-7,32-39
NUMA node1 CPU(s):	8-15,40-47
NUMA node2 CPU(s):	16-23,48-55
NUMA node3 CPU(s):	24-31,56-63
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Apr-2025
Hardware Availability: Dec-2023
Software Availability: Jun-2024

Platform Notes (Continued)

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	32K	1M	8	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64
L3	45M	90M	15	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-7,32-39
node 0 size: 257680 MB
node 0 free: 239588 MB
node 1 cpus: 8-15,40-47
node 1 size: 258005 MB
node 1 free: 245022 MB
node 2 cpus: 16-23,48-55
node 2 size: 258043 MB
node 2 free: 248423 MB
node 3 cpus: 24-31,56-63
node 3 size: 258022 MB
node 3 free: 248439 MB
node distances:
node  0  1  2  3
 0:  10  12  21  21
 1:  12  10  21  21
 2:  21  21  10  12
 3:  21  21  12  10
```

9. /proc/meminfo

MemTotal: 1056513328 kB

10. who -r

run-level 3 Apr 1 23:36

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-f-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore tuned wickedd-wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autovast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewallld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysexit systemd-time-wait-sync systemd-timesyncd udisks2 vncserver@
indirect	systemd-userdbd wickedd

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Apr-2025

Hardware Availability: Dec-2023

Software Availability: Jun-2024

Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=b0d9e489-9c66-4e23-9436-b80e6238f84b
splash=silent
mitigations=auto
quiet
security=apparmor
nomodeset
```

14. cpupower frequency-info

```
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

15. tuned-adm active

```
Current active profile: throughput-performance
```

16. sysctl

```
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   10
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

17. /sys/kernel/mm/transparent_hugepage

```
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

18. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Apr-2025
Hardware Availability: Dec-2023
Software Availability: Jun-2024

Platform Notes (Continued)

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

20. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 xfs 1.5T 83G 1.4T 6% /

21. /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: Super Server
Product Family: Family
Serial: 0123456789

22. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
8x Micron Technology MTC40F2046S1RC56BD1 64 GB 2 rank 5600, configured at 5200
8x Micron Technology MTC40F2046S1RC56BD1 QMCC 64 GB 2 rank 5600, configured at 5200

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 2.5
BIOS Date: 02/05/2025
BIOS Revision: 5.32

Compiler Version Notes

=====

```
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

```
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Apr-2025
Hardware Availability: Dec-2023
Software Availability: Jun-2024

Compiler Version Notes (Continued)

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Apr-2025

Hardware Availability: Dec-2023

Software Availability: Jun-2024

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Apr-2025
Hardware Availability: Dec-2023
Software Availability: Jun-2024

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -w -m64 -wl, -z, muldefs -DSPEC_OPENMP -xCORE-AVX512
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -w -m64 -std=c11 -wl, -z, muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

Hyper SuperServer SYS-221H-TN24R
(X13DEM , Intel Xeon Gold 6544Y)

SPECspeed®2017_fp_base = 274

SPECspeed®2017_fp_peak = 274

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Apr-2025

Hardware Availability: Dec-2023

Software Availability: Jun-2024

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-EMR-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-EMR-revF.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-04-02 01:18:14-0400.

Report generated on 2025-05-08 10:04:21 by CPU2017 PDF formatter v6716.

Originally published on 2025-05-06.