



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720A-E13-RS8U  
(2.25 GHz, AMD EPYC 9965)

SPECspeed®2017\_fp\_base = 545

SPECspeed®2017\_fp\_peak = 562

CPU2017 License: 9016

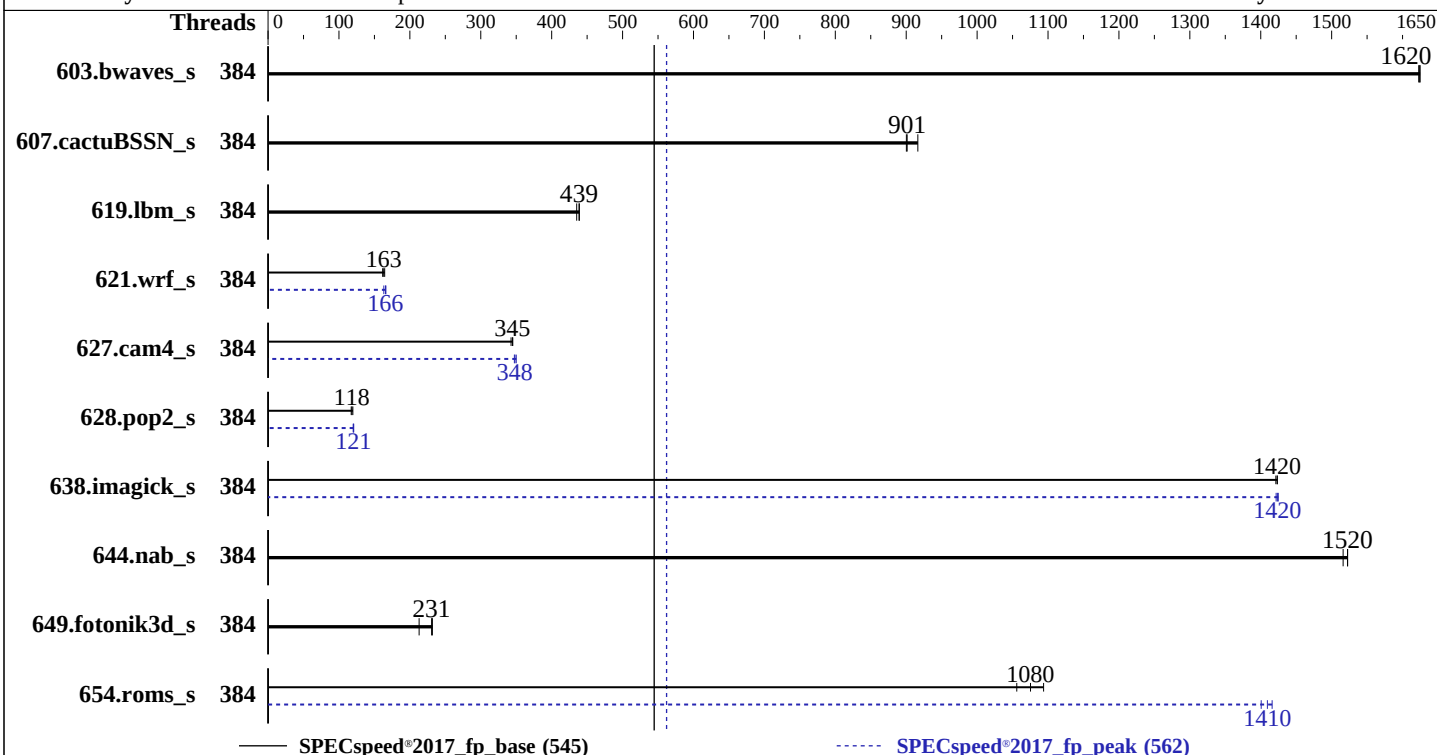
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024



## Hardware

CPU Name: AMD EPYC 9965  
Max MHz: 3700  
Nominal: 2250  
Enabled: 384 cores, 2 chips  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 384 MB I+D on chip per chip,  
32 MB shared / 16 cores  
Other: None  
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)  
Storage: 1 x 4.0 TB PCIe NVMe SSD  
Other: CPU Cooling: Air

## Software

OS: SUSE Linux Enterprise Server 15 SP6 (x86\_64)  
Kernel 6.4.0-150600.21-default  
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
Parallel: Yes  
Firmware: Version 0502 released Feb-2025  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: None  
Power Management: BIOS and OS set to prefer performance  
at the cost of additional power usage.



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## Results Table

| Benchmark       | Base    |                    |                    |                    |                    |                    |                    |         | Peak               |                    |                    |                    |                    |                   |         |       |
|-----------------|---------|--------------------|--------------------|--------------------|--------------------|--------------------|--------------------|---------|--------------------|--------------------|--------------------|--------------------|--------------------|-------------------|---------|-------|
|                 | Threads | Seconds            | Ratio              | Seconds            | Ratio              | Seconds            | Ratio              | Threads | Seconds            | Ratio              | Seconds            | Ratio              | Seconds            | Ratio             | Seconds | Ratio |
| 603.bwaves_s    | 384     | 36.4               | 1620               | <b><u>36.3</u></b> | <b><u>1620</u></b> | 36.3               | 1620               | 384     | 36.4               | 1620               | <b><u>36.3</u></b> | <b><u>1620</u></b> | 36.3               | 1620              |         |       |
| 607.cactuBSSN_s | 384     | <b><u>18.5</u></b> | <b><u>901</u></b>  | 18.2               | 916                | 18.5               | 900                | 384     | <b><u>18.5</u></b> | <b><u>901</u></b>  | 18.2               | 916                | 18.5               | 900               |         |       |
| 619.lbm_s       | 384     | 11.9               | 439                | <b><u>11.9</u></b> | <b><u>439</u></b>  | 12.0               | 435                | 384     | 11.9               | 439                | <b><u>11.9</u></b> | <b><u>439</u></b>  | 12.0               | 435               |         |       |
| 621.wrf_s       | 384     | <b><u>81.1</u></b> | <b><u>163</u></b>  | 80.4               | 164                | 81.8               | 162                | 384     | <b><u>79.9</u></b> | <b><u>166</u></b>  | 81.0               | 163                | 79.6               | 166               |         |       |
| 627.cam4_s      | 384     | <b><u>25.7</u></b> | <b><u>345</u></b>  | 25.7               | 345                | 25.9               | 343                | 384     | 25.5               | 347                | 25.3               | 350                | <b><u>25.5</u></b> | <b><u>348</u></b> |         |       |
| 628.pop2_s      | 384     | 101                | 117                | 99.3               | 120                | <b><u>100</u></b>  | <b><u>118</u></b>  | 384     | 98.9               | 120                | <b><u>98.4</u></b> | <b><u>121</u></b>  | 98.3               | 121               |         |       |
| 638.imagick_s   | 384     | <b><u>10.1</u></b> | <b><u>1420</u></b> | 10.1               | 1420               | 10.1               | 1420               | 384     | <b><u>10.1</u></b> | <b><u>1420</u></b> | 10.1               | 1420               | 10.1               | 1420              |         |       |
| 644.nab_s       | 384     | <b><u>11.5</u></b> | <b><u>1520</u></b> | 11.5               | 1520               | 11.5               | 1520               | 384     | <b><u>11.5</u></b> | <b><u>1520</u></b> | 11.5               | 1520               | 11.5               | 1520              |         |       |
| 649.fotonik3d_s | 384     | <b><u>39.5</u></b> | <b><u>231</u></b>  | 39.3               | 232                | 42.8               | 213                | 384     | <b><u>39.5</u></b> | <b><u>231</u></b>  | 39.3               | 232                | 42.8               | 213               |         |       |
| 654.roms_s      | 384     | 14.9               | 1060               | 14.4               | 1090               | <b><u>14.6</u></b> | <b><u>1080</u></b> | 384     | <b><u>11.2</u></b> | <b><u>1410</u></b> | 11.2               | 1400               | 11.1               | 1420              |         |       |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit  
OS set to performance mode via cpupower frequency-set -g performance  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-383"

LD\_LIBRARY\_PATH =

"/aocc500A1/amd\_speed\_aocc500\_znver5\_A\_lib/lib:/aocc500A1/amd\_speed\_aocc500\_znver5\_A\_lib/lib32:"

LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "384"

Environment variables set by runcpu during the 621.wrf\_s peak run:

GOMP\_CPU\_AFFINITY = "0-383"

Environment variables set by runcpu during the 627.cam4\_s peak run:

GOMP\_CPU\_AFFINITY = "0-383"

Environment variables set by runcpu during the 628.pop2\_s peak run:

GOMP\_CPU\_AFFINITY = "0-383"

Environment variables set by runcpu during the 638.imagick\_s peak run:

GOMP\_CPU\_AFFINITY = "0-383"

Environment variables set by runcpu during the 654.roms\_s peak run:

GOMP\_CPU\_AFFINITY = "0-383"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Configuration:

SR-IOV Support = Disabled

SVM Mode = Disabled

L1 Stride Prefetcher = Disabled

APBDIS = 1

SMT Control = Disable

Determinism Control = Manual

DRAM Scrub time = Disabled

Engine Boost = Aggressive

TDP Control = Manual

TDP = 500

PPT Control = Manual

PPT = 500

BMC Configuration:

Fan mode = Full speed mode

Sysinfo program /aocc500A1/bin/sysinfo

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## Platform Notes (Continued)

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Wed Feb 19 12:15:08 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a  
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT\_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
12:15:08 up 2:38, 2 users, load average: 128.90, 287.39, 330.16  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 09:38 2:36m 0.91s 0.07s /bin/bash ./amd\_speed\_aocc500\_znver5\_A1.sh  
root tty2 - 10:18 1:56m 0.01s 0.01s -bash

3. Username  
From environment variable \$USER: root

4. ulimit -a  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 6188779  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8

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## Platform Notes (Continued)

POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 6188779  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
/bin/bash ./speed.sh
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.537/templogs/preenv.fpspeed.537.0.log --lognum 537.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /aocc500A1
-----
```

```
-----
6. /proc/cpuinfo
model name      : AMD EPYC 9965 192-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 17
stepping       : 0
microcode      : 0xb101028
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages
cpu cores      : 192
siblings       : 192
2 physical ids (chips)
384 processors (hardware threads)
physical id 0: core ids 0-191
physical id 1: core ids 0-191
physical id 0: apicids 0-191
physical id 1: apicids 256-447
-----
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

## 7. lscpu

From lscpu from util-linux 2.39.3:

|                      |                                   |
|----------------------|-----------------------------------|
| Architecture:        | x86_64                            |
| CPU op-mode(s):      | 32-bit, 64-bit                    |
| Address sizes:       | 52 bits physical, 57 bits virtual |
| Byte Order:          | Little Endian                     |
| CPU(s):              | 384                               |
| On-line CPU(s) list: | 0-383                             |
| Vendor ID:           | AuthenticAMD                      |
| BIOS Vendor ID:      | Advanced Micro Devices, Inc.      |
| Model name:          | AMD EPYC 9965 192-Core Processor  |
| BIOS Model name:     | AMD EPYC 9965 192-Core Processor  |
| BIOS CPU family:     | 107                               |
| CPU family:          | 26                                |

Unknown CPU @ 2.2GHz

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### Platform Notes (Continued)

Model: 17  
Thread(s) per core: 1  
Core(s) per socket: 192  
Socket(s): 2  
Stepping: 0  
Frequency boost: enabled  
CPU(s) scaling MHz: 61%  
CPU max MHz: 3700.1951  
CPU min MHz: 1500.0000  
BogoMIPS: 4493.28  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat  
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpe1gb  
rdtscp lm constant\_tsc rep\_good amd\_lbr\_v2 nopl nonstop\_tsc cpuid  
extd\_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid  
sse4\_1 sse4\_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf\_lm  
cmp\_legacy extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch  
osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpext  
perfctr\_llc mwaitx cpb cat\_l3 cdp\_l3 hw\_pstate ssbd mba perfmon\_v2  
ibrs ibpb stibp ibrs\_enhanced vmmcall fsgsbase tsc\_adjust bmi1 avx2  
smep bmi2 erms invpcid cqm rdt\_a avx512f avx512dq rdseed adx smap  
avx512ifma clflushopt clwb avx512cd sha\_ni avx512bw avx512vl xsaveopt  
xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local user\_shstk avx\_vnni avx512\_bf16 clzero irperf  
xsaveerptr rdpru wbnoinvd amd\_ppin cppc arat npt lbrv svm\_lock  
nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v\_vmsave\_vmload vgif x2avic v\_spec\_ctrl vnmi  
avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq  
avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid bus\_lock\_detect  
movdiri movdir64b overflow\_recov succor smca fsrm avx512\_vp2intersect  
flush\_lld debug\_swap  
L1d cache: 18 MiB (384 instances)  
L1i cache: 12 MiB (384 instances)  
L2 cache: 384 MiB (384 instances)  
L3 cache: 768 MiB (24 instances)  
NUMA node(s): 2  
NUMA node0 CPU(s): 0-191  
NUMA node1 CPU(s): 192-383  
Vulnerability Gather data sampling: Not affected  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Reg file data sampling: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec rstack overflow: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP  
disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 18M      | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 12M      | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 384M     | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 32M      | 768M     | 16   | Unified     | 3     | 32768 | 1        | 64             |

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## Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-191

node 0 size: 773298 MB

node 0 free: 772134 MB

node 1 cpus: 192-383

node 1 size: 773922 MB

node 1 free: 771783 MB

node distances:

node 0 1

0: 10 32

1: 32 10

9. /proc/meminfo

MemTotal: 1584354172 kB

10. who -r

run-level 3 Feb 19 09:37

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance  
issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections  
nvme-fc-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked  
wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

enabled-runtime systemd-remount-fs

disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info  
firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievdev issue-add-ssh-keys  
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd  
serial-getty@ smartd\_generate\_opts snmpd snmptrapd svnserve systemd-boot-check-no-failures  
systemd-confext systemd-network-generator systemd-sysexit systemd-time-wait-sync  
systemd-timesyncd tuned udisks2 vncserver@

indirect systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default

root=UUID=bd4eeb48-8f2c-47c9-ae06-b7241b1d0eb7

splash=silent

mitigations=auto

quiet

security=apparmor

video=1024x768

14. cpupower frequency-info

analyzing CPU 339:

current policy: frequency should be within 1.50 GHz and 2.25 GHz.

The governor "performance" may decide which speed to use

(Continued on next page)





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## Platform Notes (Continued)

within this range.  
boost state support:  
Supported: yes  
Active: yes

### 15. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.  
Preset profile: latency-performance

### 16. sysctl

|                              |       |
|------------------------------|-------|
| kernel.numa_balancing        | 1     |
| kernel.randomize_va_space    | 0     |
| vm.compaction_proactiveness  | 20    |
| vm.dirty_background_bytes    | 0     |
| vm.dirty_background_ratio    | 10    |
| vm.dirty_bytes               | 0     |
| vm.dirty_expire_centisecs    | 3000  |
| vm.dirty_ratio               | 8     |
| vm.dirty_writeback_centisecs | 500   |
| vm.dirtytime_expire_seconds  | 43200 |
| vm.extfrag_threshold         | 500   |
| vm.min_unmapped_ratio        | 1     |
| vm.nr_hugepages              | 0     |
| vm.nr_hugepages_mempolicy    | 0     |
| vm.nr_overcommit_hugepages   | 0     |
| vm.swappiness                | 1     |
| vm.watermark_boost_factor    | 15000 |
| vm.watermark_scale_factor    | 10    |
| vm.zone_reclaim_mode         | 1     |

### 17. /sys/kernel/mm/transparent\_hugepage

|                |          |             |               |         |            |
|----------------|----------|-------------|---------------|---------|------------|
| defrag         | [always] | defer       | defer+madvise | madvise | never      |
| enabled        | [always] | madvise     | never         |         |            |
| hpage_pmd_size | 2097152  |             |               |         |            |
| shmem_enabled  | always   | within_size | advise        | [never] | deny force |

### 18. /sys/kernel/mm/transparent\_hugepage/khugepaged

|                       |       |
|-----------------------|-------|
| alloc_sleep_millisecs | 60000 |
| defrag                | 1     |
| max_ptes_none         | 511   |
| max_ptes_shared       | 256   |
| max_ptes_swap         | 64    |
| pages_to_scan         | 4096  |
| scan_sleep_millisecs  | 10000 |

### 19. OS release

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP6

### 20. Disk information

SPEC is set to: /aocc500A1

| Filesystem     | Type | Size | Used | Avail | Use% | Mounted on |
|----------------|------|------|------|-------|------|------------|
| /dev/nvme0n1p4 | xfs  | 2.0T | 268G | 1.8T  | 14%  | /          |

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# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720A-E13-RS8U  
(2.25 GHz, AMD EPYC 9965)

SPECspeed®2017\_fp\_base = 545

SPECspeed®2017\_fp\_peak = 562

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

## Platform Notes (Continued)

21. /sys/devices/virtual/dmi/id  
Vendor: ASUSTeK COMPUTER INC.  
Product: RS720A-E13-RS8U  
Product Family: Server  
Serial: 123456789012

22. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
24x Samsung M321R8GA0EB2-CCPPC 64 GB 2 rank 6400

23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: American Megatrends Inc.  
BIOS Version: 0502  
BIOS Date: 02/04/2025  
BIOS Revision: 5.2

## Compiler Version Notes

=====  
C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix

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Hardware Availability: Mar-2025

Software Availability: Oct-2024

## Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64

607.cactuBSSN\_s: -DSPEC\_LP64

619.lbm\_s: -DSPEC\_LP64

621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64

627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64

628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64

638.imagick\_s: -DSPEC\_LP64

644.nab\_s: -DSPEC\_LP64

649.fotonik3d\_s: -DSPEC\_LP64

654.roms\_s: -DSPEC\_LP64



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## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt  
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100  
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```



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**Test Date:** Feb-2025

**Hardware Availability:** Mar-2025

**Software Availability:** Oct-2024

## Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast

-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp

-flto -DSPEC\_OPENMP -fremap-arrays -fstrip-mining

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## Peak Optimization Flags (Continued)

638.imagick\_s (continued):

-fstruct-layout=9 -mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc -lflang

644.nab\_s: basepeak = yes

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

654.roms\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC\_OPENMP  
-Ofast -march=znver5 -fvecLib=AMDLIBM -ffast-math  
-fopenmp -fscalar-transform -fvector-transform  
-mllvm -reduce-array-computations=3 -Mrecursive  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Benchmarks using both Fortran and C:

621.wrf\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fvecLib=AMDLIBM -ffast-math -fopenmp  
-flt0 -DSPEC\_OPENMP -fremap-arrays -fstrip-mining  
-fstruct-layout=9 -mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang

627.cam4\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fvecLib=AMDLIBM -ffast-math -fopenmp  
-flt0 -DSPEC\_OPENMP -fremap-arrays -fstrip-mining  
-fstruct-layout=9 -mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -Mrecursive  
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang

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## Peak Optimization Flags (Continued)

```
628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fscalar-transform
-fvector-transform -mrecursive -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-K15-V1.1.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-K15-V1.1.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.00.xml>

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