



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

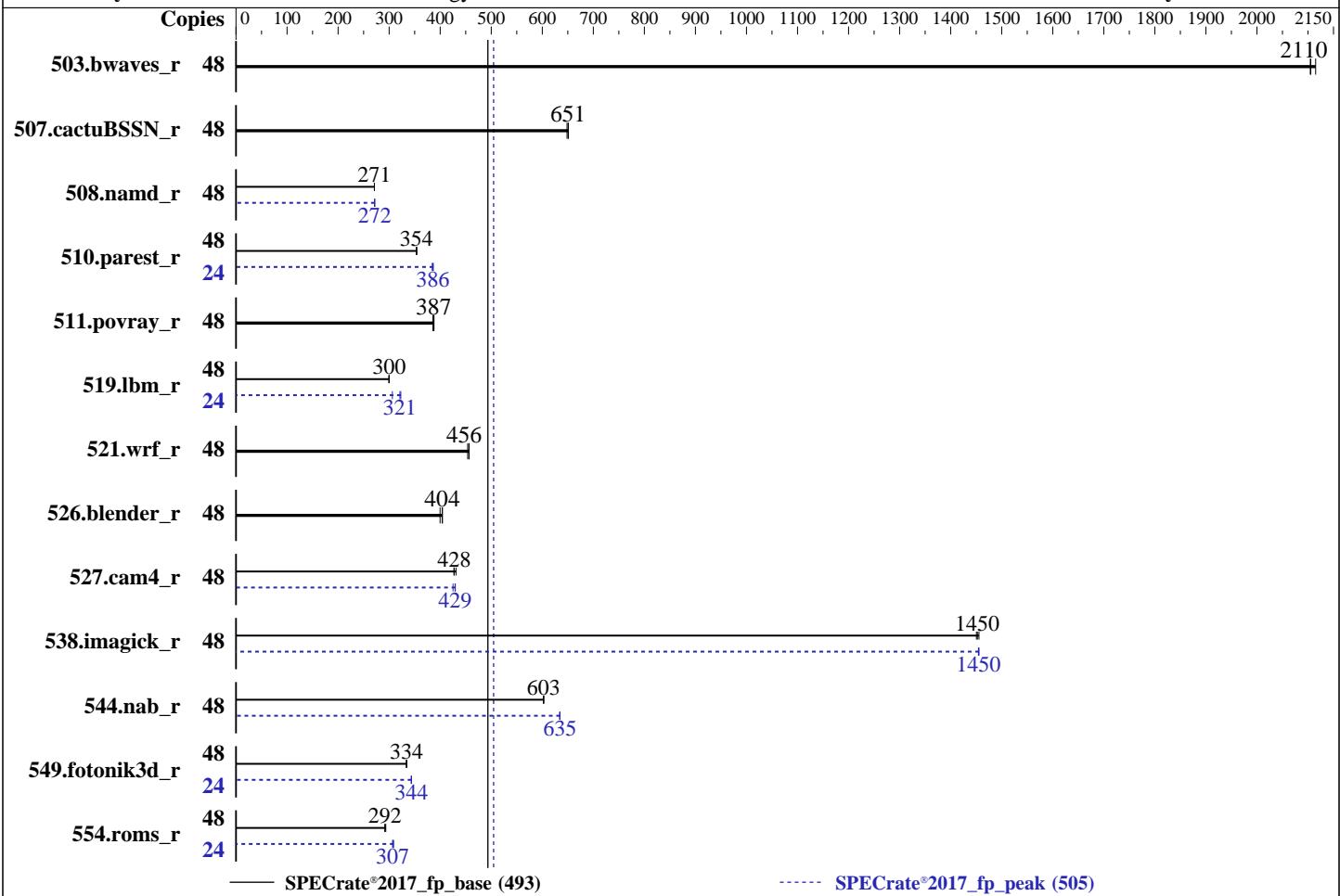
Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024



Hardware		Software	
CPU Name:	AMD EPYC 9255	OS:	Red Hat Enterprise Linux 9.4 (Plow)
Max MHz:	4300	Compiler:	Kernel 5.14.0-427.13.1.el9_4.x86_64
Nominal:	3200	Parallel:	C/C++/Fortran: Version 5.0.0 of AOCC
Enabled:	24 cores, 1 chip, 2 threads/core	Firmware:	No
Orderable:	1 chip	File System:	Lenovo BIOS Version KAE13II 5.30 released Dec-2024
Cache L1:	32 KB I + 48 KB D on chip per core	System State:	xfs
L2:	1 MB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	128 MB I+D on chip per chip, 32 MB shared / 6 cores	Peak Pointers:	64-bit
Other:	None	Other:	64-bit
Memory:	384 GB (12 x 32 GB 2Rx8 PC5-6400B-R, running at 6000)	Power Management:	None
Storage:	1 x 480 GB SATA SSD		BIOS set to prefer performance at the cost of additional power usage
Other:	CPU Cooling: Air		



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	48	229	2110	228	2120	229	2100	48	229	2110	228	2120	229	2100
507.cactusBSSN_r	48	93.7	648	93.4	651	93.3	651	48	93.7	648	93.4	651	93.3	651
508.namd_r	48	168	271	168	271	168	271	48	168	272	168	272	168	272
510.parest_r	48	355	353	355	354	355	354	24	162	387	163	386	163	384
511.povray_r	48	291	386	289	387	289	388	48	291	386	289	387	289	388
519.lbm_r	48	169	300	169	299	169	300	24	78.8	321	82.6	306	78.4	323
521.wrf_r	48	236	456	235	457	237	454	48	236	456	235	457	237	454
526.blender_r	48	181	404	183	400	181	404	48	181	404	183	400	181	404
527.cam4_r	48	197	427	196	428	195	431	48	195	430	196	429	197	425
538.imagick_r	48	82.2	1450	82.3	1450	82.0	1460	48	82.1	1450	82.0	1460	82.1	1450
544.nab_r	48	134	603	134	602	134	603	48	127	635	127	634	127	635
549.fotonik3d_r	48	560	334	560	334	560	334	24	272	344	272	344	272	343
554.roms_r	48	262	292	261	292	260	293	24	124	307	124	307	124	309

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations:

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd  
    -aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

NUMA Nodes per Socket set to NPS4

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Tue Feb 25 19:46:17 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9_4)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

18. /sys/kernel/mm/transparent_hugepage/khugepaged

19. OS release

20. Disk information

21. /sys/devices/virtual/dmi/id

22. dmidecode

23. BIOS

1. uname -a
Linux localhost.localdomain 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86_64 x86_64 x86_64 GNU/Linux

2. w
19:46:17 up 3 min, 0 users, load average: 0.04, 0.12, 0.06
USER TTY LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (i) 1546040
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 1546040
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECcpu_1.01.sh
/bin/bash ./Run026-compliant-amd-ratefp.sh
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.310/templogs/preenv.fprate.310.0.log --lognum 310.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
6. /proc/cpuinfo
model name      : AMD EPYC 9255 24-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 2
stepping        : 1
microcode       : 0xb00211a
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 24
siblings         : 48
1 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-11,16-27,32-43,48-59
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 48
On-line CPU(s) list:    0-47
Vendor ID:              AuthenticAMD
BIOS Vendor ID:         Advanced Micro Devices, Inc.
Model name:              AMD EPYC 9255 24-Core Processor
BIOS Model name:        AMD EPYC 9255 24-Core Processor
CPU family:              26
Model:                  2
Thread(s) per core:     2
Core(s) per socket:     24
Socket(s):              1
Stepping:                1
BogoMIPS:                6390.42
Flags:
fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpelgb rdtscp
lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
aperfmpfperf rapl pn1 pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
extapic cr8_legacy abm sse4a misalignsse 3dnnowprefetch osvw ibr skininit
wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
ibrs_enhanced vmmcall fsgsbase tsc_adjust bml1 avx2 smep bml2 erms
invpcid cqmq_rdt_a avx512f avx512dq rdseed adx smap avx512ifm
clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavew
xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin
cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic
v_spec_ctrl vnmi avx512vbmi unip pkru ospke avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnmi avx512_bitalg avx512_vpopcntdq la57 rdpid
bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm
avx512_vp2intersect flush_lld debug_swap
AMD-V
1.1 MiB (24 instances)

Virtualization:
L1d cache:
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

L1i cache:	768 KiB (24 instances)
L2 cache:	24 MiB (24 instances)
L3 cache:	128 MiB (4 instances)
NUMA node(s):	4
NUMA node0 CPU(s):	0-5,24-29
NUMA node1 CPU(s):	6-11,30-35
NUMA node2 CPU(s):	12-17,36-41
NUMA node3 CPU(s):	18-23,42-47
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation: Speculative Store Bypass disabled via prot1
Vulnerability Spectre v1:	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation: Enhanced / Automatic IBRS, IBPB conditional, STIBP always-on, RSB filling, PBRSB-eIBRS Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	16	Unified	2	1024	1	64
L3	32M	128M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-5,24-29

node 0 size: 96348 MB

node 0 free: 95876 MB

node 1 cpus: 6-11,30-35

node 1 size: 96764 MB

node 1 free: 96306 MB

node 2 cpus: 12-17,36-41

node 2 size: 96723 MB

node 2 free: 96232 MB

node 3 cpus: 18-23,42-47

node 3 size: 96713 MB

node 3 free: 95991 MB

node distances:

node	0	1	2	3
------	---	---	---	---

0:	10	12	12	12
----	----	----	----	----

1:	12	10	12	12
----	----	----	----	----

2:	12	12	10	12
----	----	----	----	----

3:	12	12	12	10
----	----	----	----	----

9. /proc/meminfo

MemTotal:	395826464 kB
-----------	--------------

10. who -r

run-level 3 Feb 25 19:44

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
11. Systemd service manager version: systemd 252 (252-32.el9_4)
    Default Target      Status
    multi-user          degraded

-----
12. Failed units, from systemctl list-units --state=failed
    UNIT                  LOAD ACTIVE SUB   DESCRIPTION
    * NetworkManager-wait-online.service loaded failed failed Network Manager Wait Online

-----
13. Services, from systemctl list-unit-files
    STATE            UNIT FILES
    enabled           NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited chronyd crond
                      dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor
                      mdmonitor microcode nis-domainname rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark
                      sshd sssd systemd-boot-update systemd-network-generator udisks2 upower
    enabled-runtime   systemd-remount-fs
    disabled          canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot
                      chrony-wait chronyd-restricted console-getty cpupower debug-shell dnf-system-upgrade
                      hwloc-dump-hwdata kvm_stat man-db-restart-cache-update nftables pesign rdisc rhcd rhsm
                      rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
    indirect          systemd-boot-check-no-failures systemd-pstore systemd-sysext
                      sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
                      systemd-sysupdate-reboot

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=(hd3,gpt3)/boot/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
    root=UUID=273c79f3-13a9-4ae6-9c36-e82799318ef8
    ro
    resume=UUID=19c8733f-555f-4142-8f61-7554d0dc64ae

-----
15. cpupower frequency-info
analyzing CPU 28:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 12800MHz

-----
16. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space       0
    vm.compaction_proactiveness    20
    vm.dirty_background_bytes       0
    vm.dirty_background_ratio       10
    vm.dirty_bytes                  0
    vm.dirty_expire_centisecs      3000
    vm.dirty_ratio                  8
    vm.dirty_writeback_centisecs   500
    vm.dirtytime_expire_seconds    43200
    vm.extfrag_threshold           500
    vm.min_unmapped_ratio          1
    vm.nr_hugepages                 0
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
vm.nr_hugepages_mempolicy          0
vm.nr_overcommit_hugepages        0
vm.swappiness                      1
vm.watermark_boost_factor         15000
vm.watermark_scale_factor          10
vm.zone_reclaim_mode                1

-----
17. /sys/kernel/mm/transparent_hugepage
    defrag           [always] defer defer+madvise madvise never
    enabled          [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap            64
    pages_to_scan           4096
    scan_sleep_millisecs    10000

-----
19. OS release
    From /etc/*-release /etc/*-version
    os-release      Red Hat Enterprise Linux 9.4 (Plow)
    redhat-release  Red Hat Enterprise Linux release 9.4 (Plow)
    system-release  Red Hat Enterprise Linux release 9.4 (Plow)

-----
20. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   372G  106G  266G  29%  /home

-----
21. /sys/devices/virtual/dmi/id
Vendor:          Lenovo
Product:         ThinkSystem SR635V3
Product Family:  ThinkSystem
Serial:          1234567890

-----
22. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
    12x SK Hynix HMCG88AHBRA290N 32 GB 2 rank 6400, configured at 6000

-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Lenovo
BIOS Version:     KAE131I-5.30
BIOS Date:        12/17/2024
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

Test Date: Feb-2025

Hardware Availability: Feb-2025

Software Availability: Oct-2024

Platform Notes (Continued)

BIOS Revision: 5.30

Firmware Revision: 54.6

Compiler Version Notes

=====

C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C, Fortran | 507.cactusBSSN_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-rel-5.0.0-4925-1316/bin

=====

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Base Portability Flags (Continued)

527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64

538.imagick_r: -DSPEC_LP64

544.nab_r: -DSPEC_LP64

549.fotonik3d_r: -DSPEC_LP64

554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Base Other Flags (Continued)

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto
-fstruct-layout=7 -mllvm -unroll-threshold=50
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

519.lbm_r (continued):

```
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

538.imagick_r: Same as 519.lbm_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

Fortran benchmarks:

503.bwaves_r: basepeak = yes

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdaloc -ldl -lflang
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

```
554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdaloc -ldl -lflang
```

Benchmarks using both Fortran and C:

```
521.wrf_r: basepeak = yes
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc
-ldl -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: basepeak = yes
```

```
526.blender_r: basepeak = yes
```

Benchmarks using Fortran, C, and C++:

```
507.cactusBSSN_r: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR635 V3
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017_fp_base = 493

SPECrate®2017_fp_peak = 505

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Peak Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECCpu2017-Flags-V1.2-Turin-E.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECCpu2017-Flags-V1.2-Turin-E.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-02-25 06:46:17-0500.

Report generated on 2025-03-26 10:36:38 by CPU2017 PDF formatter v6716.

Originally published on 2025-03-25.