



# SPEC CPU®2017 Floating Point Rate Result

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## New H3C Technologies Co., Ltd.

H3C UniServer R3350 G7  
AMD EPYC 9745

SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1040

CPU2017 License: 9066

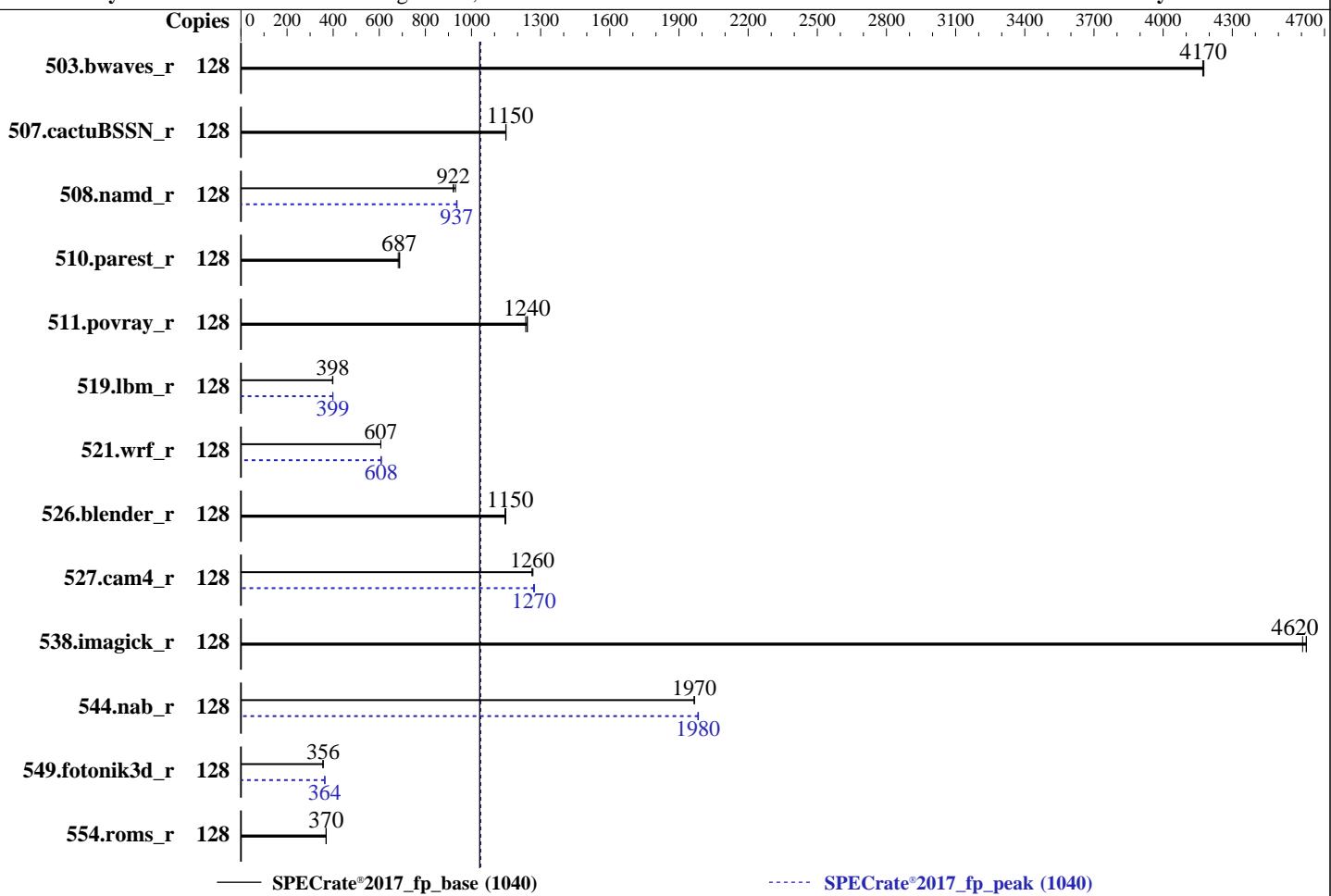
Test Date: Mar-2025

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2024

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2024



— SPECrate®2017\_fp\_base (1040)

----- SPECrate®2017\_fp\_peak (1040)

### Hardware

CPU Name: AMD EPYC 9745  
Max MHz: 3700  
Nominal: 2400  
Enabled: 128 cores, 1 chip  
Orderable: 1 chip  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 256 MB I+D on chip per chip, 32 MB shared / 16 cores  
Other: None  
Memory: 384 GB (12 x 32 GB 2Rx4 PC5-6400B-R, running at 6000)  
Storage: 1 x 3.84TB SSD  
Other: CPU Cooling: Air

### Software

OS: Ubuntu 24.04.1 LTS  
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
Parallel: No  
Firmware: Version 7.30.06 released Jan-2025  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: None  
Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	<b>307</b>	<b>4170</b>	308	4170	307	4180	128	<b>307</b>	<b>4170</b>	308	4170	<b>307</b>	4180
507.cactubSSN_r	128	141	1150	141	1150	<b>141</b>	<b>1150</b>	128	141	1150	141	1150	<b>141</b>	<b>1150</b>
508.namd_r	128	132	921	131	931	<b>132</b>	<b>922</b>	128	130	937	<b>130</b>	<b>937</b>	130	935
510.parest_r	128	<b>487</b>	<b>687</b>	486	689	491	682	128	<b>487</b>	<b>687</b>	486	689	491	682
511.povray_r	128	242	1240	<b>241</b>	<b>1240</b>	240	1240	128	242	1240	<b>241</b>	<b>1240</b>	240	1240
519.lbm_r	128	338	399	<b>339</b>	<b>398</b>	340	397	128	338	399	339	398	<b>338</b>	<b>399</b>
521.wrf_r	128	<b>472</b>	<b>607</b>	472	607	473	606	128	472	608	471	609	<b>472</b>	<b>608</b>
526.blender_r	128	<b>170</b>	<b>1150</b>	170	1150	170	1140	128	<b>170</b>	<b>1150</b>	170	1150	170	1140
527.cam4_r	128	<b>177</b>	<b>1260</b>	177	1270	177	1260	128	176	1270	176	1270	<b>176</b>	<b>1270</b>
538.imagick_r	128	<b>68.9</b>	<b>4620</b>	69.1	4600	68.9	4620	128	<b>68.9</b>	<b>4620</b>	69.1	4600	68.9	4620
544.nab_r	128	<b>110</b>	<b>1970</b>	110	1970	110	1970	128	109	1980	109	1980	<b>109</b>	<b>1980</b>
549.fotonik3d_r	128	1401	356	1400	356	<b>1401</b>	<b>356</b>	128	1372	<b>364</b>	1372	364	1372	364
554.roms_r	128	<b>550</b>	<b>370</b>	550	370	551	369	128	<b>550</b>	<b>370</b>	550	370	551	369

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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## Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_rate_aocc500_znver5_A/lib:/home/cpu2017/amd_rate_aocc500_znver5_A/lib/lib32:/us  
r/local/mpc-131/lib:/usr/local/gmp-630/lib:/usr/local/mpfr-421/lib:/usr/local/isl-027/lib:/usr/local/g  
cc-1420/lib64:/usr/local/lib:/usr/lib:/usr/local/amd/aocc-compiler-5.0.0/lib:/usr/local/amd/aocc-compi  
ler-5.0.0/lib32"  
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS settings:  
SMT Control set to Disabled  
SVM Mode set to Disabled  
Power Profile Selection set to High Performance Mode  
Determinism Slider set to Power  
cTDP set to 400  
PPT set to 400  
NUMA nodes per socket set to NPS 4

```
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on h3c Mon Mar  3 23:55:58 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r

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## Platform Notes (Continued)

```
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.4)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

---

```
1. uname -a
Linux h3c 6.8.0-51-generic #52-Ubuntu SMP PREEMPT_DYNAMIC Thu Dec 5 13:09:44 UTC 2024 x86_64 x86_64 x86_64
GNU/Linux
```

---

```
2. w
23:55:58 up 1 min, 1 user, load average: 0.62, 0.36, 0.14
USER      TTY      FROM          LOGIN@    IDLE      JCPU      PCPU      WHAT
root      tty1      -           23:55     6.00s   0.99s   0.09s   /bin/bash ./amd_rate_aocc500_znver5_A1.sh
```

---

```
3. Username
From environment variable $USER: root
```

---

```
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)        unlimited
stack(kbytes)       unlimited
coredump(blocks)    0
memory(kbytes)      unlimited
locked memory(kbytes) 2097152
process            1545555
nofiles             1024
vmemory(kbytes)     unlimited
locks               unlimited
rtprio              0
```

---

```
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.006/templogs/preenv.fprate.006.0.log --lognum 006.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

---

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## Platform Notes (Continued)

```
6. /proc/cpuinfo
model name      : AMD EPYC 9745 128-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 17
stepping        : 0
microcode       : 0xb101028
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 128
siblings        : 128
1 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-127
physical id 0: apicids 0-127
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	128
On-line CPU(s) list:	0-127
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	Advanced Micro Devices, Inc.
Model name:	AMD EPYC 9745 128-Core Processor
BIOS Model name:	AMD EPYC 9745 128-Core Processor
BIOS CPU family:	107
CPU family:	26
Model:	17
Thread(s) per core:	1
Core(s) per socket:	128
Socket(s):	1
Stepping:	0
Frequency boost:	enabled
CPU(s) scaling MHz:	44%
CPU max MHz:	3707.8120
CPU min MHz:	1500.0000
BogoMIPS:	4792.89
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch oswi ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local user_shstk avx_vnmi avx512_bf16 clzero irperf xsaverptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi

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## Platform Notes (Continued)

```

avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap

L1d cache: 6 MiB (128 instances)
L1i cache: 4 MiB (128 instances)
L2 cache: 128 MiB (128 instances)
L3 cache: 256 MiB (8 instances)

NUMA node(s): 4
NUMA node0 CPU(s): 0-31
NUMA node1 CPU(s): 32-63
NUMA node2 CPU(s): 64-95
NUMA node3 CPU(s): 96-127

Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Llft: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Vulnerable
Vulnerability Spectre v1: Vulnerable: __user pointer sanitization and usercopy barriers only;
no swapgs barriers
Vulnerability Spectre v2: Vulnerable; IBPB: disabled; STIBP: disabled; PBRSB-eIBRS: Not
affected; BHI: Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	1M	128M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

-----

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

```

node 0 cpus: 0-31
node 0 size: 96298 MB
node 0 free: 95802 MB
node 1 cpus: 32-63
node 1 size: 96713 MB
node 1 free: 96247 MB
node 2 cpus: 64-95
node 2 size: 96757 MB
node 2 free: 96310 MB
node 3 cpus: 96-127
node 3 size: 96690 MB
node 3 free: 96296 MB
node distances:
node 0 1 2 3
 0: 10 12 12 12
 1: 12 10 12 12
 2: 12 12 10 12
 3: 12 12 12 10

```

(Continued on next page)



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## Platform Notes (Continued)

9. /proc/meminfo  
MemTotal: 395734992 kB

10. who -r  
run-level 3 Mar 3 23:54

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.4)  
Default Target Status  
multi-user degraded

12. Failed units, from systemctl list-units --state=failed  
UNIT LOAD ACTIVE SUB DESCRIPTION  
\* fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd  
Legend: LOAD -> Reflects whether the unit definition was properly loaded.  
ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.  
SUB -> The low-level unit activation state, values depend on unit type.  
1 loaded units listed.

13. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online apparmor  
apport blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup  
cron dmesg e2scrub\_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback  
keyboard-setup lm-sensors lvm2-monitor multipathd networkd-dispatcher open-iscsi  
open-vm-tools pollinate rsyslog secureboot-db setvtrgb snapd sysstat systemd-networkd  
systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald  
ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades vgaauth wpa\_supplicant  
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs  
disabled console-getty debug-shell iscsid nftables rsync serial-getty@ ssh  
systemd-boot-check-no-failures systemd-confext systemd-network-generator  
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code  
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy  
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext  
systemd-time-wait-sync upower wpa\_supplicant-nl80211@ wpa\_supplicant-wired@  
wpa\_supplicant@  
indirect systemd-sysupdate systemd-sysupdate-reboot uuidd  
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/vmlinuz-6.8.0-51-generic  
root=UUID=5079c432-fd48-464d-92df-94ceb7591bc8  
ro  
iommu=pt  
mitigations=off  
security=none

15. cpupower frequency-info  
analyzing CPU 6:  
current policy: frequency should be within 1.50 GHz and 2.40 GHz.  
The governor "schedutil" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes

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## Platform Notes (Continued)

```
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2400MHz
```

```
-----  
16. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space      0  
vm.compaction_proactiveness   20  
vm.dirty_background_bytes     0  
vm.dirty_background_ratio     10  
vm.dirty_bytes                0  
vm.dirty_expire_centisecs    3000  
vm.dirty_ratio                8  
vm.dirty_writeback_centisecs  500  
vm.dirtytime_expire_seconds   43200  
vm.extfrag_threshold          500  
vm.min_unmapped_ratio         1  
vm.nr_hugepages               0  
vm.nr_hugepages_mempolicy     0  
vm.nr_overcommit_hugepages   0  
vm.swappiness                 1  
vm.watermark_boost_factor    15000  
vm.watermark_scale_factor     10  
vm.zone_reclaim_mode          1
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage  
defrag           [always] defer defer+madvise madvise never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled    always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged  
alloc_sleep_millisecs  60000  
defrag                1  
max_ptes_none        511  
max_ptes_shared      256  
max_ptes_swap        64  
pages_to_scan        4096  
scan_sleep_millisecs 10000
```

```
-----  
19. OS release  
From /etc/*-release /etc/*-version  
os-release Ubuntu 24.04.1 LTS
```

```
-----  
20. Disk information  
SPEC is set to: /home/cpu2017  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/sda4        ext4  3.4T  19G  3.2T  1%  /
```

```
-----  
21. /sys/devices/virtual/dmi/id  
Vendor:          AMD Corporation  
Product:         Quartz  
Product Family:  Rack
```

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## Platform Notes (Continued)

### 22. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

9x Samsung M321R4GA3EB2-CCPPC 32 GB 2 rank 6400, configured at 6000  
1x Samsung M321R4GA3PB1-CCPPC 32 GB 2 rank 6400, configured at 6000  
2x Samsung M321R4GA3PB2-CCPPC 32 GB 2 rank 6400, configured at 6000

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 7.30.06  
BIOS Date: 01/20/2025  
BIOS Revision: 5.35  
Firmware Revision: 2.2

## Compiler Version Notes

=====

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C, Fortran | 507.cactusBSSN\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## New H3C Technologies Co., Ltd.

H3C UniServer R3350 G7  
AMD EPYC 9745

SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = 1040

CPU2017 License: 9066

Test Date: Mar-2025

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2024

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2024

## Compiler Version Notes (Continued)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

(Continued on next page)



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CPU2017 License: 9066

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Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2024

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2024

## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactusBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

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Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2024

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2024

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdaloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdaloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdaloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdaloc -lflang -ldl
```



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AMD EPYC 9745

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SPECrate®2017\_fp\_peak = 1040

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2025

Hardware Availability: Oct-2024

Software Availability: Dec-2024

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags



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CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2025

Hardware Availability: Oct-2024

Software Availability: Dec-2024

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

```
538.imagick_r: basepeak = yes
```

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

```
510.parest_r: basepeak = yes
```

Fortran benchmarks:

```
503.bwaves_r: basepeak = yes
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdaloc -ldl -lflang
```

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CPU2017 License: 9066

Test Date: Mar-2025

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2024

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2024

## Peak Optimization Flags (Continued)

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc
-lld -lflang
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc
-lld -lflang
```

Benchmarks using both C and C++:

511.povray\_r: basepeak = yes

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

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Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2024

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2024

## Peak Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-AMD-Settings-V1.5-Turin.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-AMD-Settings-V1.5-Turin.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-AMD-Settings-V1.5-Turin.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-AMD-Settings-V1.5-Turin.xml)

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