



# SPEC CPU®2017 Integer Rate Result

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## Supermicro

SuperServer SYS-122HA-TN-LCC  
(X14DBM-APL, Intel Xeon 6972P)

**SPECrate®2017\_int\_base = 1960**

**SPECrate®2017\_int\_peak = 2020**

CPU2017 License: 001176

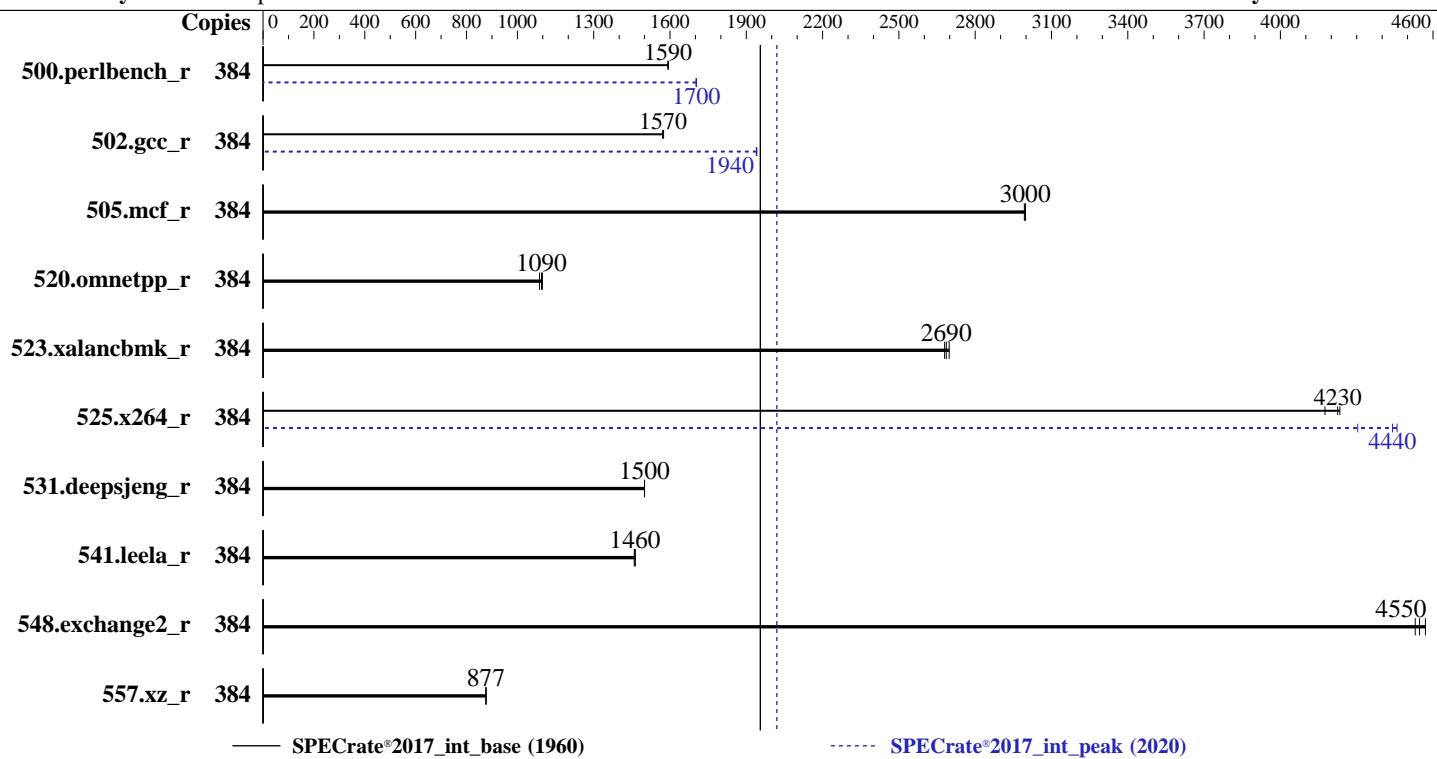
Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Feb-2025

Tested by: Supermicro

Software Availability: Jun-2024



Hardware		Software	
CPU Name:	Intel Xeon 6972P	OS:	SUSE Linux Enterprise Server 15 SP6
Max MHz:	3900	Compiler:	6.4.0-150600.21-default
Nominal:	2400	Parallel:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Enabled:	192 cores, 2 chips, 2 threads/core	Firmware:	Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Orderable:	1, 2 chips	File System:	No
Cache L1:	64 KB I + 48 KB D on chip per core	System State:	Version 1.2 released Jan-2025
L2:	2 MB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	480 MB I+D on chip per chip	Peak Pointers:	64-bit
Other:	None	Other:	32/64-bit
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-8800B-R)	Power Management:	jemalloc memory allocator V5.0.1
Storage:	1 x 1.6TB NVMe SSD		BIOS set to prefer performance at the cost of additional power usage.
Other:	CPU Cooling: DLC		



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	384	384	1590	384	1590	<b>384</b>	<b>1590</b>	384	<b>359</b>	<b>1700</b>	359	1700	359	1700	359	1700
502.gcc_r	384	345	1570	<b>346</b>	<b>1570</b>	346	1570	384	<b>280</b>	<b>1940</b>	280	1940	280	1940	280	1940
505.mcf_r	384	207	2990	207	3000	<b>207</b>	<b>3000</b>	384	<b>207</b>	<b>2990</b>	207	3000	<b>207</b>	<b>3000</b>	<b>207</b>	<b>3000</b>
520.omnetpp_r	384	464	1090	459	1100	<b>460</b>	<b>1090</b>	384	464	1090	459	1100	<b>460</b>	<b>1090</b>	<b>460</b>	<b>1090</b>
523.xalancbmk_r	384	150	2700	<b>151</b>	<b>2690</b>	151	2680	384	150	2700	<b>151</b>	<b>2690</b>	<b>151</b>	<b>2680</b>	<b>151</b>	<b>2680</b>
525.x264_r	384	161	4180	159	4230	<b>159</b>	<b>4230</b>	384	156	4300	151	4460	<b>151</b>	<b>4440</b>	<b>151</b>	<b>4440</b>
531.deepsjeng_r	384	293	1500	293	1500	<b>293</b>	<b>1500</b>	384	293	1500	293	1500	<b>293</b>	<b>1500</b>	<b>293</b>	<b>1500</b>
541.leela_r	384	<b>435</b>	<b>1460</b>	435	1460	436	1460	384	<b>435</b>	<b>1460</b>	435	1460	436	1460	436	1460
548.exchange2_r	384	220	4570	<b>221</b>	<b>4550</b>	222	4530	384	220	<b>4570</b>	<b>221</b>	<b>4550</b>	222	4530	222	4530
557.xz_r	384	473	876	<b>473</b>	<b>877</b>	473	877	384	473	876	<b>473</b>	<b>877</b>	473	877	473	877

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop\_caches

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



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## General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

### BIOS Settings:

Power Performance Tuning = BIOS Controls EPPB  
ENERGY\_PERF\_BIAS\_CFG mode = Extreme Performance  
KTI Prefetch = Enable  
DCU Streamer Prefetcher = Disable  
LLC Dead Line Alloc = Disable  
Stale AtoS = Disable

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on 181-170 Mon Mar 10 16:36:17 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
- 12. Failed units, from systemctl list-units --state=failed
- 13. Services, from systemctl list-unit-files
- 14. Linux kernel boot-time arguments, from /proc/cmdline
- 15. cpupower frequency-info
- 16. sysctl
- 17. /sys/kernel/mm/transparent\_hugepage
- 18. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

1. uname -a  
Linux 181-170 6.4.0-150600.21-default #1 SMP PREEMPT\_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09) x86\_64  
x86\_64 x86\_64 GNU/Linux

2. w  
16:36:17 up 11 min, 1 user, load average: 0.41, 0.95, 1.10  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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## Platform Notes (Continued)

```
root      tty2      -          16:32   41.00s  1.09s  0.01s -bash
```

```
-----  
3. Username  
From environment variable $USER: root
```

```
-----  
4. ulimit -a  
core file size          (blocks, -c) unlimited  
data seg size           (kbytes, -d) unlimited  
scheduling priority     (-e) 0  
file size               (blocks, -f) unlimited  
pending signals          (-i) 6188426  
max locked memory       (kbytes, -l) 8192  
max memory size         (kbytes, -m) unlimited  
open files              (-n) 1024  
pipe size               (512 bytes, -p) 8  
POSIX message queues    (bytes, -q) 819200  
real-time priority       (-r) 0  
stack size               (kbytes, -s) unlimited  
cpu time                (seconds, -t) unlimited  
max user processes       (-u) 6188426  
virtual memory           (kbytes, -v) unlimited  
file locks               (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize=42  
login -- root  
-bash  
-bash  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=384 -c  
  ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=192 --define physicalfirst  
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=384 --configfile  
  ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=192 --define physicalfirst  
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower  
  --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile  
  $SPEC/tmp/CPU2017.013/templogs/preenv.intrate.013.0.log --lognum 013.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/cpu2017
```

```
-----  
6. /proc/cpuinfo  
model name      : Intel(R) Xeon(R) 6972P  
vendor_id       : GenuineIntel  
cpu family     : 6  
model          : 173  
stepping        : 1  
microcode       : 0x1000380  
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi  
cpu cores       : 96  
siblings        : 192  
2 physical ids (chips)  
384 processors (hardware threads)  
physical id 0: core ids 0-31,64-95,128-159  
physical id 1: core ids 0-31,64-95,128-159  
physical id 0: apicids 0-63,128-191,256-319  
physical id 1: apicids 512-575,640-703,768-831
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for

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## Platform Notes (Continued)

virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 384
On-line CPU(s) list: 0-383
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) 6972P
BIOS Model name: Intel(R) Xeon(R) 6972P CPU @ 2.4GHz
BIOS CPU family: 179
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 96
Socket(s): 2
Stepping: 1
BogoMIPS: 4800.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperf mperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 intel_ppin cdp_12
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hfi vnmi avx512vbmi umip pkru ospke waitpkg avx512_vbm12
gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq
la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm
md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16
amx_tile amx_int8 flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 9 MiB (192 instances)
L1i cache: 12 MiB (192 instances)
L2 cache: 384 MiB (192 instances)
L3 cache: 960 MiB (2 instances)
NUMA node(s): 6
NUMA node0 CPU(s): 0-31,192-223
NUMA node1 CPU(s): 32-63,224-255
NUMA node2 CPU(s): 64-95,256-287
NUMA node3 CPU(s): 96-127,288-319
NUMA node4 CPU(s): 128-159,320-351
NUMA node5 CPU(s): 160-191,352-383
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
```

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## Platform Notes (Continued)

Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	9M	12	Data	1	64	1	64
L1i	64K	12M	16	Instruction	1	64	1	64
L2	2M	384M	16	Unified	2	2048	1	64
L3	480M	960M	16	Unified	3	491520	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 6 nodes (0-5)

node 0 cpus: 0-31,192-223

node 0 size: 257465 MB

node 0 free: 256184 MB

node 1 cpus: 32-63,224-255

node 1 size: 258031 MB

node 1 free: 257241 MB

node 2 cpus: 64-95,256-287

node 2 size: 257992 MB

node 2 free: 256920 MB

node 3 cpus: 96-127,288-319

node 3 size: 258031 MB

node 3 free: 257360 MB

node 4 cpus: 128-159,320-351

node 4 size: 258031 MB

node 4 free: 257297 MB

node 5 cpus: 160-191,352-383

node 5 size: 257583 MB

node 5 free: 256871 MB

node distances:

node	0	1	2	3	4	5
0:	10	12	12	21	21	21
1:	12	10	12	21	21	21
2:	12	12	10	21	21	21
3:	21	21	21	10	12	12
4:	21	21	21	12	10	12
5:	21	21	21	12	12	10

-----  
9. /proc/meminfo

MemTotal: 1584267004 kB

-----  
10. who -r

run-level 3 Mar 10 16:32 last=5

-----  
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status

(Continued on next page)



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## Platform Notes (Continued)

graphical degraded

-----  
12. Failed units, from systemctl list-units --state=failed  
UNIT LOAD ACTIVE SUB DESCRIPTION  
\* udisks2.service loaded failed failed Disk Manager

-----  
13. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron  
display-manager firewalld getty@ irqbalance issue-generator kbdsettings kdump kdump-early  
kdump-notify klog lvm2-monitor nsqd nvmefc-boot-connections nvmf-autoconnect postfix  
purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4  
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl  
ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables  
exchange-bmc-os-info fsidd gpm grub2-once haveged ipmi ipmievfd issue-add-ssh-keys  
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb ostree-remount rpcbind  
rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd\_generate\_opts smb snmpd snmptrapd  
speech-dispatcherd systemd-boot-check-no-failures systemd-confext  
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2  
update-system-flatpaks upower vncserver@  
indirect systemd-userdbd wickedd

-----  
14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=d1a687ce-0a78-499c-9181-4e82ae083016  
splash=silent  
mitigations=auto  
quiet  
security=apparmor  
crashkernel=365M,high  
crashkernel=72M,low

-----  
15. cpupower frequency-info  
analyzing CPU 199:  
    Unable to determine current policy  
    boost state support:  
        Supported: yes  
        Active: yes

-----  
16. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 20  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0

(Continued on next page)



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## Platform Notes (Continued)

```
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages    0
vm.swappiness                  60
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage
    defrag           always defer defer+madvise [madvise] never
    enabled          [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan           4096
    scan_sleep_millisecs   10000
```

```
-----  
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----  
20. Disk information
  SPEC is set to: /home/cpu2017
  Filesystem  Type  Size  Used  Avail Use% Mounted on
  /dev/nvme0n1p3  btrfs  1.5T  156G  1.3T  11% /home
```

```
-----  
21. /sys/devices/virtual/dmi/id
  Vendor:        Supermicro
  Product:       Super Server
  Product Family: Family
  Serial:        0123456789
```

```
-----  
22. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    24x Micron Technology MTC40F2046S1HC88XD1 WCCCC 64 GB 2 rank 8800
```

```
-----  
23. BIOS
  (This section combines info from /sys/devices and dmidecode.)
  BIOS Vendor:      American Megatrends International, LLC.
  BIOS Version:     1.2
  BIOS Date:        01/24/2025
  BIOS Revision:    5.35
```



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## Compiler Version Notes

=====

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
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=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

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=====

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

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=====

Fortran | 548.exchange2\_r(base, peak)

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=====

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



# SPEC CPU®2017 Integer Rate Result

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## Supermicro

SuperServer SYS-122HA-TN-LCC  
(X14DBM-APL, Intel Xeon 6972P)

SPECrate®2017\_int\_base = 1960

SPECrate®2017\_int\_peak = 2020

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Feb-2025

Hardware Availability: Feb-2025

Software Availability: Jun-2024

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



# SPEC CPU®2017 Integer Rate Result

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## Supermicro

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SPECrate®2017\_int\_peak = 2020

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Feb-2025

Hardware Availability: Feb-2025

Software Availability: Jun-2024

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-fno-strict-overflow  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc  
  
502.gcc\_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89  
-Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf\_r: basepeak = yes

525.x264\_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-alias  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

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# SPEC CPU®2017 Integer Rate Result

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**Supermicro**

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**SPECrate®2017\_int\_peak = 2020**

**CPU2017 License:** 001176

**Test Sponsor:** Supermicro

**Tested by:** Supermicro

**Test Date:** Feb-2025

**Hardware Availability:** Feb-2025

**Software Availability:** Jun-2024

## Peak Optimization Flags (Continued)

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revG.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revG.xml>  
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2025-03-10 19:36:17-0400.

Report generated on 2025-02-25 19:05:52 by CPU2017 PDF formatter v6716.

Originally published on 2025-02-25.