SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS C225 M8 (AMD EPYC 9254 24-Core Processor)

$SPECrate®2017\_int\_base = 313$

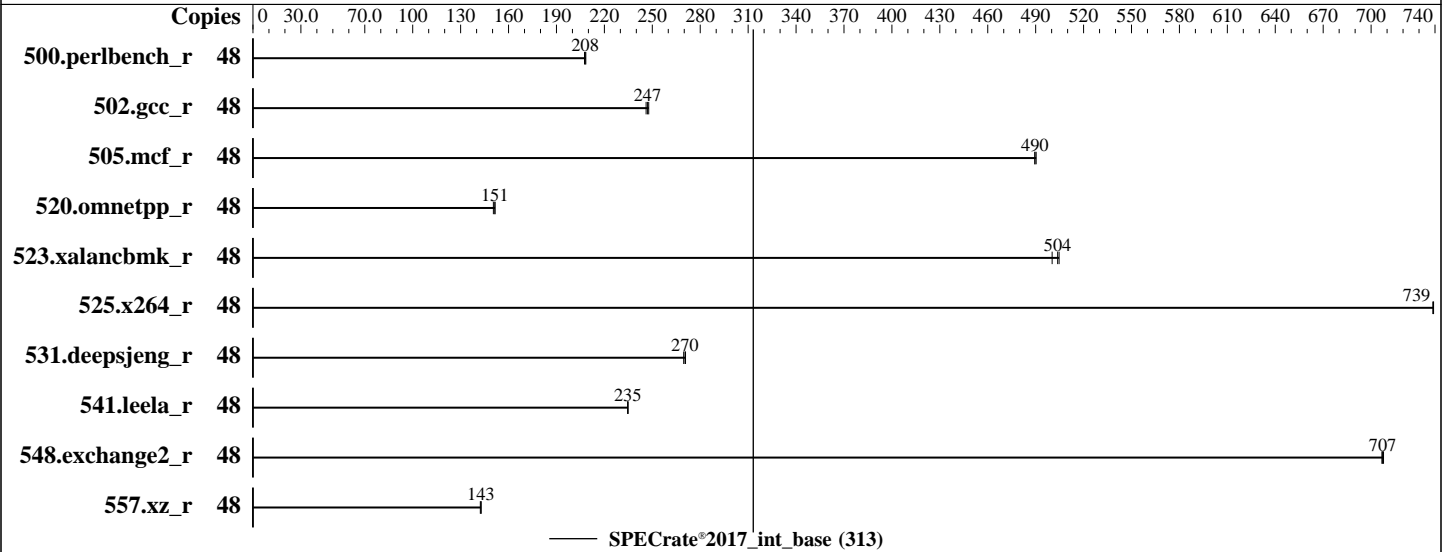$SPECrate®2017\_int\_peak = Not Run$

CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Benchmark	Copies	Result
500.perlbench_r	48	208
502.gcc_r	48	247
505.mcf_r	48	490
520.omnetpp_r	48	151
523.xalancbmk_r	48	504
525.x264_r	48	739
531.deepsjeng_r	48	270
541.leela_r	48	235
548.exchange2_r	48	707
557.xz_r	48	143

SPECrate®2017_int_base (313)

Hardware

CPU Name: AMD EPYC 9254
 Max MHz: 4150
 Nominal: 2900
Enabled: 24 cores, 1 chip, 2 threads/core
Orderable: 1 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 128 MB I+D on chip per chip, 32 MB shared / 6 cores
 Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)
Storage: 1 x 240 GB M.2 SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6 kernel version 6.4.0-150600.21-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: Version 4.3.5 released Aug-2024
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

Standard Performance Evaluation Corporation (info@spec.org)
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	**367**	**208**	367	208	368	208							
502.gcc_r	48	**275**	**247**	274	248	276	246							
505.mcf_r	48	**158**	**490**	158	489	158	490							
520.omnetpp_r	48	416	152	418	151	**416**	**151**							
523.xalancbmk_r	48	**101**	**504**	100	505	101	500							
525.x264_r	48	114	739	114	739	**114**	**739**							
531.deepsjeng_r	48	**203**	**270**	204	270	203	271							
541.leela_r	48	**339**	**235**	339	235	339	235							
548.exchange2_r	48	**178**	**707**	178	708	178	707							
557.xz_r	48	363	143	**363**	**143**	364	142							
SPECrate®2017_int_base =		**313**												
SPECrate®2017_int_peak =		**Not Run**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

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Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS settings:
NUMA nodes per socket set to NPS4
Determinism Slider set to Power
DF C-States set to Disabled


 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Thu Jan 23 03:53:10 2025

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
```

(Continued on next page)

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Platform Notes (Continued)

```
-------------------------------------------------------------

-------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
  x86_64 x86_64 x86_64 GNU/Linux

-------------------------------------------------------------
2. w
  03:53:10 up  5:57,  3 users,  load average: 0.01, 0.11, 0.07
  USER     TTY       FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1      -                22Apr24 18.00s  1.25s  0.23s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

-------------------------------------------------------------
3. Username
  From environment variable $USER:  root

-------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 3094860
  max locked memory       (kbytes, -l) 2097152
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 3094860
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

-------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=42
 login -- root
 -bash
 python3 ./run_amd_rate_aocc500_znver5_A1.py -b intrate
 /bin/bash ./amd_rate_aocc500_znver5_A1.sh
 runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 intrate
 runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 --nopower
   --runmode rate --tune base --size test:train:refrate intrate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
 $SPEC = /home/cpu2017

-------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9254 24-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 25
    model           : 17
    stepping        : 1
    microcode       : 0xa101148
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
    TLB size        : 3584 4K pages
```

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Platform Notes (Continued)

```
     cpu cores       : 24
     siblings        : 48
     1 physical ids (chips)
     48 processors (hardware threads)
     physical id 0: core ids 0-5,8-13,16-21,24-29
     physical id 0: apicids 0-11,16-27,32-43,48-59
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.


  ----------------------------------------------------------
  7. lscpu

  From lscpu from util-linux 2.39.3:
    Architecture:              x86_64
    CPU op-mode(s):            32-bit, 64-bit
    Address sizes:             52 bits physical, 57 bits virtual
    Byte Order:                Little Endian
    CPU(s):                    48
    On-line CPU(s) list:       0-47
    Vendor ID:                 AuthenticAMD
    BIOS Vendor ID:            Advanced Micro Devices, Inc.
    Model name:                AMD EPYC 9254 24-Core Processor
    BIOS Model name:           AMD EPYC 9254 24-Core Processor            Unknown CPU @ 2.9GHz
    BIOS CPU family:           107
    CPU family:                25
    Model:                     17
    Thread(s) per core:        2
    Core(s) per socket:        24
    Socket(s):                 1
    Stepping:                  1
    Frequency boost:           enabled
    CPU(s) scaling MHz:        80%
    CPU max MHz:               4151.7568
    CPU min MHz:               1500.0000
    BogoMIPS:                  5791.80
    Flags:                     fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                               pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                               rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                               extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                               sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                               cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                               osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                               perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                               ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2
                               erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                               clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                               xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                               user_shstk avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
                               amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean
                               flushbyasid decodeassists pausefilter pfthreshold avic
                               v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku
                               ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                               avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
                               debug_swap
    Virtualization:            AMD-V
    L1d cache:                 768 KiB (24 instances)
    L1i cache:                 768 KiB (24 instances)
    L2 cache:                  24 MiB (24 instances)
    L3 cache:                  128 MiB (4 instances)
    NUMA node(s):              4
```

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Platform Notes (Continued)

```
  NUMA node0 CPU(s):                0-5,24-29
  NUMA node1 CPU(s):                6-11,30-35
  NUMA node2 CPU(s):                12-17,36-41
  NUMA node3 CPU(s):                18-23,42-47
  Vulnerability Gather data sampling:  Not affected
  Vulnerability Itlb multihit:      Not affected
  Vulnerability L1tf:               Not affected
  Vulnerability Mds:                Not affected
  Vulnerability Meltdown:           Not affected
  Vulnerability Mmio stale data:    Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:           Not affected
  Vulnerability Spec rstack overflow:  Mitigation; Safe RET
  Vulnerability Spec store bypass:  Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                    always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
  Vulnerability Srbds:              Not affected
  Vulnerability Tsx async abort:    Not affected

  From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE       LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       32K     768K   8 Data            1    64        1             64
      L1i       32K     768K   8 Instruction     1    64        1             64
      L2         1M      24M   8 Unified         2  2048        1             64
      L3        32M     128M  16 Unified         3 32768        1             64


  ------------------------------------------------------------
  8. numactl --hardware
  NOTE: a numactl 'node' might or might not correspond to a physical chip.
    available: 4 nodes (0-3)
    node 0 cpus: 0-5,24-29
    node 0 size: 193231 MB
    node 0 free: 192695 MB
    node 1 cpus: 6-11,30-35
    node 1 size: 193532 MB
    node 1 free: 193036 MB
    node 2 cpus: 12-17,36-41
    node 2 size: 193494 MB
    node 2 free: 192947 MB
    node 3 cpus: 18-23,42-47
    node 3 size: 193484 MB
    node 3 free: 192807 MB
    node distances:
    node   0   1   2   3
      0:  10  12  12  12
      1:  12  10  12  12
      2:  12  12  10  12
      3:  12  12  12  10


  ------------------------------------------------------------
  9. /proc/meminfo
    MemTotal:      792312492 kB


  ------------------------------------------------------------
  10. who -r
    run-level 3 Apr 22 08:00


  ------------------------------------------------------------
  11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
```

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Platform Notes (Continued)

```
  Default Target  Status
  multi-user      running


     -----------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE           UNIT FILES
  enabled         YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
                  issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
                  smartd sshd systemd-pstore virtqemud wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
                  wickedd-nanny
  enabled-runtime systemd-remount-fs
  disabled        autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                  chronyd console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info
                  firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd iscsi-init iscsid
                  issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests lunmask man-db-create multipathd
                  munge nfs nfs-blkmap nfs-server nfsserver rpcbind rpmconfigcheck rsyncd rtkit-daemon
                  salt-minion serial-getty@ slurmd smartd_generate_opts snmpd snmptrapd strongswan
                  strongswan-starter svnserve systemd-boot-check-no-failures systemd-confext
                  systemd-network-generator systemd-nspawn@ systemd-sysext systemd-time-wait-sync
                  systemd-timesyncd tcsd udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodedevd
                  virtnwfilterd virtsecretd virtstoraged ypbind
  indirect        pcscd systemd-userdbd tftp wickedd

     -----------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=076ee295-de6f-47c7-95ad-f8ccc04c1b41
  splash=silent
  mitigations=auto
  quiet
  security=apparmor


     -----------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 14:
    current policy: frequency should be within 1.50 GHz and 2.90 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes


     -----------------------------------------------------------
15. sysctl
  kernel.numa_balancing            1
  kernel.randomize_va_space        0
  vm.compaction_proactiveness      20
  vm.dirty_background_bytes        0
  vm.dirty_background_ratio        10
  vm.dirty_bytes                   0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                   8
  vm.dirty_writeback_centisecs     500
  vm.dirtytime_expire_seconds      43200
  vm.extfrag_threshold             500
  vm.min_unmapped_ratio            1
  vm.nr_hugepages                  0
  vm.nr_hugepages_mempolicy        0
  vm.nr_overcommit_hugepages       0
  vm.swappiness                    1
```

(Continued on next page)

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Platform Notes (Continued)

```
  vm.watermark_boost_factor      15000
  vm.watermark_scale_factor         10
  vm.zone_reclaim_mode               1


  ----------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag         [always] defer defer+madvise madvise never
  enabled        [always] madvise never
  hpage_pmd_size 2097152
  shmem_enabled   always within_size advise [never] deny force


  ----------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000


  ----------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6


  ----------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017
  Filesystem      Type   Size  Used Avail Use% Mounted on
  /dev/sdb2       btrfs  224G  8.1G  212G   4% /home


  ----------------------------------------------------------
20. /sys/devices/virtual/dmi/id
   Vendor:        Cisco Systems Inc
   Product:       UCSC-C225-M8S
   Serial:        WZP28199HWP


  ----------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    9x 0xCE00 M321R8GA0PB0-CWMCH 64 GB 2 rank 5600, configured at 4800
    3x 0xCE00 M321R8GA0PB0-CWMKJ 64 GB 2 rank 5600, configured at 4800


  ----------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Cisco Systems, Inc.
   BIOS Version:      C225M8.4.3.5.166.0819241228
   BIOS Date:         08/19/2024
   BIOS Revision:     5.27
```

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Compiler Version Notes

```
==============================================================================
C        | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------


==============================================================================
C++      | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------


==============================================================================
Fortran | 548.exchange2_r(base)
------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

Base Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

Cisco Systems

Cisco UCS C225 M8 (AMD EPYC 9254 24-Core Processor)

SPECrate®2017_int_base =   313

SPECrate®2017_int_peak = Not Run

CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Nov-2024

Software Availability: Sep-2024

Base Portability Flags (Continued)

548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc-ext -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext
-ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamdlibm -lflang -lamdalloc -ldl
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

Cisco Systems

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CPU2017 License:	9019	**Test Date:**	Jan-2025
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Tested by:	Cisco Systems	**Software Availability:**	Sep-2024

Base Other Flags (Continued)

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/aocc500-flags.html
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/aocc500-flags.xml
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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