



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 2360

PowerEdge R7725 (AMD EPYC 9965 192-Core Processor)

SPECrate®2017\_fp\_peak = 2490

CPU2017 License: 6573

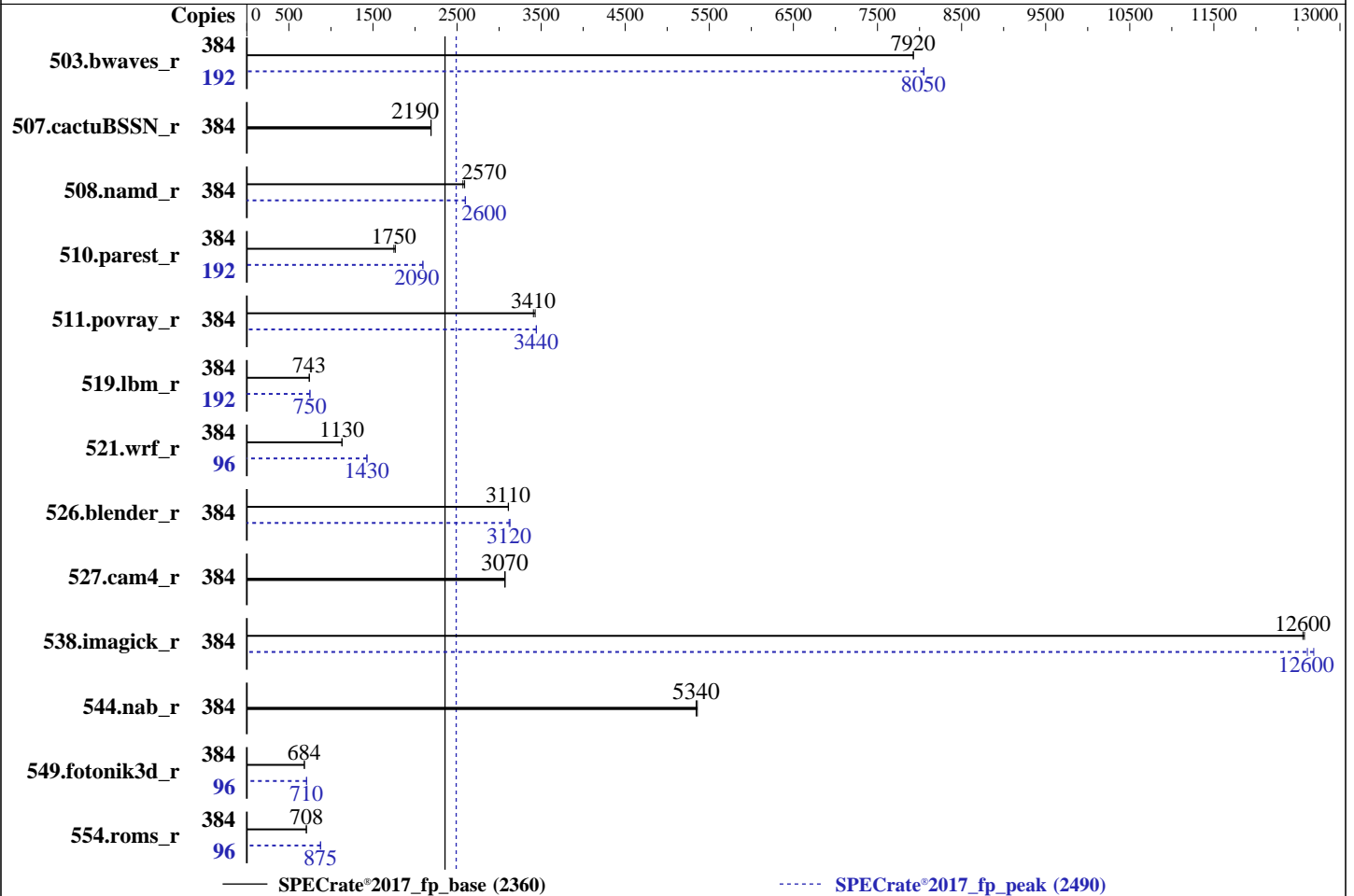
Test Date: Nov-2024

Test Sponsor: Dell Inc.

Hardware Availability: Nov-2024

Tested by: Dell Inc.

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9965  
 Max MHz: 3700  
 Nominal: 2250  
 Enabled: 384 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 384 MB I+D on chip per chip, 32 MB shared / 16 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R, running at 6000)  
 Storage: 1 x 480 GB M.2 NVMe SSD  
 Other: CPU Cooling: Air

### Software

OS: Ubuntu 24.04 LTS  
 6.8.0-44-generic  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 1.0.2 released Oct-2024  
 File System: ext4  
 System State: Run level 5 (graphical multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	384	486	7930	<b>486</b>	<b>7920</b>			192	<b>239</b>	<b>8050</b>	239	8050		
507.cactuBSSN_r	384	<b>222</b>	<b>2190</b>	222	2190			384	<b>222</b>	<b>2190</b>	222	2190		
508.namd_r	384	141	2590	<b>142</b>	<b>2570</b>			384	<b>140</b>	<b>2600</b>	140	2600		
510.parest_r	384	<b>574</b>	<b>1750</b>	569	1770			192	240	2090	<b>240</b>	<b>2090</b>		
511.povray_r	384	262	3430	<b>263</b>	<b>3410</b>			384	260	3440	<b>261</b>	<b>3440</b>		
519.lbm_r	384	545	743	<b>545</b>	<b>743</b>			192	<b>270</b>	<b>750</b>	270	751		
521.wrf_r	384	<b>761</b>	<b>1130</b>	760	1130			96	151	1430	<b>151</b>	<b>1430</b>		
526.blender_r	384	<b>188</b>	<b>3110</b>	188	3110			384	<b>187</b>	<b>3120</b>	187	3130		
527.cam4_r	384	<b>219</b>	<b>3070</b>	219	3070			384	<b>219</b>	<b>3070</b>	219	3070		
538.imagick_r	384	75.9	12600	<b>76.0</b>	<b>12600</b>			384	75.3	12700	<b>75.7</b>	<b>12600</b>		
544.nab_r	384	121	5360	<b>121</b>	<b>5340</b>			384	121	5360	<b>121</b>	<b>5340</b>		
549.fotonik3d_r	384	<b>2187</b>	<b>684</b>	2187	684			96	527	710	<b>527</b>	<b>710</b>		
554.roms_r	384	<b>862</b>	<b>708</b>	862	708			96	<b>174</b>	<b>875</b>	174	878		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and

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## Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
  "/home/cpu2017-1.1.9-aocc500-znerv5_A1/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-aocc500-z
  nerv5_A1/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Settings:

Virtualization Technology : Disabled  
NUMA Nodes Per Socket : 4

System Profile : Custom  
C-States : Disabled

Memory Patrol Scrub : Disabled  
PCI ASPM L1 Link Power Management : Disabled

Periodic Directory Rinse Tuning : Blended  
Determinism Control : Manual  
Determinism Slider : Power Determinism

Optimizer Mode : Enabled  
Adaptive Allocation : Enabled  
Dram Refresh Delay : Performance

DIMM Self Healing -  
on Uncorrectable Memory Error : Disabled  
Logical Processor : Disabled

Sysinfo program /home/cpu2017-1.1.9-aocc500-znerv5\_A1/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on CSN2601-R7725 Mon Nov 4 00:53:33 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w

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## Platform Notes (Continued)

- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. tuned-adm active
- 16. systemctl
- 17. /sys/kernel/mm/transparent\_hugepage
- 18. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

```
-----
1. uname -a
Linux CSN2601-R7725 6.8.0-44-generic #44-Ubuntu SMP PREEMPT_DYNAMIC Tue Aug 13 13:35:26 UTC 2024 x86_64
x86_64 x86_64 GNU/Linux
-----
```

```
-----
2. w
00:53:33 up 5:13, 2 users, load average: 1.12, 0.72, 45.31
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 19:40 1:17 1.54s 0.38s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
root tty2 - 19:46 3:38m 0.08s 0.04s -bash
-----
```

```
-----
3. Username
From environment variable $USER: root
-----
```

```
-----
4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited
locked memory(kbytes) 2097152
process 6187514
nofiles 1024
vmemory(kbytes) unlimited
locks unlimited
rtprio 0
-----
```

```
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
-----
```

(Continued on next page)



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### Platform Notes (Continued)

```
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-specpcpu.sh rate --define DL-VERS=6.0.5 --output_format
html,pdf,txt
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define DL-BIOS-NPS=4
--define DL-VERS=6.0.5 --output_format html,pdf,txt fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=4 --define DL-VERS=6.0.5 --output_format html,pdf,txt --nopower --runmode rate --tune
base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-aocc500-znerv5_A1
```

```
-----
6. /proc/cpuinfo
model name      : AMD EPYC 9965 192-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 17
stepping       : 0
microcode      : 0xb101025
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages
cpu cores      : 192
siblings       : 192
2 physical ids (chips)
384 processors (hardware threads)
physical id 0: core ids 0-191
physical id 1: core ids 0-191
physical id 0: apicids
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72
,74,76,78,80,82,84,86,88,90,92,94,96,98,100,102,104,106,108,110,112,114,116,118,120,122,124,126,128,130,1
32,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,180,182,18
4,186,188,190,192,194,196,198,200,202,204,206,208,210,212,214,216,218,220,222,224,226,228,230,232,234,236
,238,240,242,244,246,248,250,252,254,256,258,260,262,264,266,268,270,272,274,276,278,280,282,284,286,288,
290,292,294,296,298,300,302,304,306,308,310,312,314,316,318,320,322,324,326,328,330,332,334,336,338,340,3
42,344,346,348,350,352,354,356,358,360,362,364,366,368,370,372,374,376,378,380,382
physical id 1: apicids
512,514,516,518,520,522,524,526,528,530,532,534,536,538,540,542,544,546,548,550,552,554,556,558,560,562,5
64,566,568,570,572,574,576,578,580,582,584,586,588,590,592,594,596,598,600,602,604,606,608,610,612,614,61
6,618,620,622,624,626,628,630,632,634,636,638,640,642,644,646,648,650,652,654,656,658,660,662,664,666,668
,670,672,674,676,678,680,682,684,686,688,690,692,694,696,698,700,702,704,706,708,710,712,714,716,718,720,
722,724,726,728,730,732,734,736,738,740,742,744,746,748,750,752,754,756,758,760,762,764,766,768,770,772,7
74,776,778,780,782,784,786,788,790,792,794,796,798,800,802,804,806,808,810,812,814,816,818,820,822,824,82
6,828,830,832,834,836,838,840,842,844,846,848,850,852,854,856,858,860,862,864,866,868,870,872,874,876,878
,880,882,884,886,888,890,892,894
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
-----
7. lscpu

From lscpu from util-linux 2.39.3:
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             768
```

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### Platform Notes (Continued)

```

On-line CPU(s) list:          0-383
Off-line CPU(s) list:        384-767
Vendor ID:                    AuthenticAMD
BIOS Vendor ID:              AMD
Model name:                   AMD EPYC 9965 192-Core Processor
BIOS Model name:             AMD EPYC 9965 192-Core Processor          CPU @ 2.2GHz
BIOS CPU family:             107
CPU family:                   26
Model:                         17
Thread(s) per core:          1
Core(s) per socket:          192
Socket(s):                    2
Stepping:                     0
Frequency boost:              enabled
CPU(s) scaling MHz:          61%
CPU max MHz:                  3700.1951
CPU min MHz:                  0.0000
BogoMIPS:                     4493.99
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                                smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                                xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                                xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                                nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                                pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnni
                                avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                                avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                                movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
                                flush_lld debug_swap
L1d cache:                    18 MiB (384 instances)
L1i cache:                    12 MiB (384 instances)
L2 cache:                     384 MiB (384 instances)
L3 cache:                     768 MiB (24 instances)
NUMA node(s):                 8
NUMA node0 CPU(s):           0-47
NUMA node1 CPU(s):           48-95
NUMA node2 CPU(s):           96-143
NUMA node3 CPU(s):           144-191
NUMA node4 CPU(s):           192-239
NUMA node5 CPU(s):           240-287
NUMA node6 CPU(s):           288-335
NUMA node7 CPU(s):           336-383
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:               Not affected
Vulnerability Mds:                Not affected
Vulnerability Meltdown:           Not affected
Vulnerability Mmio stale data:     Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:           Not affected
Vulnerability Spec rstack overflow: Not affected

```

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### Platform Notes (Continued)

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
 Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected  
 Vulnerability Srbds: Not affected  
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	18M	12	Data	1	64	1	64
L1i	32K	12M	8	Instruction	1	64	1	64
L2	1M	384M	16	Unified	2	1024	1	64
L3	32M	768M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0-47
node 0 size: 192539 MB
node 0 free: 190616 MB
node 1 cpus: 48-95
node 1 size: 193497 MB
node 1 free: 192788 MB
node 2 cpus: 96-143
node 2 size: 193497 MB
node 2 free: 192706 MB
node 3 cpus: 144-191
node 3 size: 193481 MB
node 3 free: 192827 MB
node 4 cpus: 192-239
node 4 size: 193497 MB
node 4 free: 192802 MB
node 5 cpus: 240-287
node 5 size: 193497 MB
node 5 free: 192787 MB
node 6 cpus: 288-335
node 6 size: 193497 MB
node 6 free: 192805 MB
node 7 cpus: 336-383
node 7 size: 193441 MB
node 7 free: 192754 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
1:  12 10 12 12 32 32 32 32
2:  12 12 10 12 32 32 32 32
3:  12 12 12 10 32 32 32 32
4:  32 32 32 32 10 12 12 12
5:  32 32 32 32 12 10 12 12
6:  32 32 32 32 12 12 10 12
7:  32 32 32 32 12 12 12 10
```

9. /proc/meminfo

MemTotal: 1584076388 kB

10. who -r

run-level 5 Nov 3 19:40

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## Platform Notes (Continued)

```
-----
11. Systemd service manager version: systemd 255 (255.4-lubuntu8)
    Default Target   Status
    graphical        running
```

```
-----
12. Services, from systemctl list-unit-files
    STATE           UNIT FILES
    enabled          ModemManager apparmor appport blk-availability cloud-config cloud-final cloud-init
                    cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
                    grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd
                    networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
                    sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
                    systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
    enabled-runtime  netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
    disabled         console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                    systemd-boot-check-no-failures systemd-confext systemd-network-generator
                    systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                    systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                    systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                    systemd-time-wait-sync upower
    indirect         systemd-sysupdate systemd-sysupdate-reboot uidd
    masked           cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-6.8.0-44-generic
    root=UUID=8458ae54-58cc-4621-9289-b1d743fde503
    ro
```

```
-----
14. cpupower frequency-info
    analyzing CPU 2:
        current policy: frequency should be within 1.50 GHz and 2.25 GHz.
                        The governor "performance" may decide which speed to use
                        within this range.
    boost state support:
        Supported: yes
        Active: yes
        Boost States: 0
        Total States: 3
        Pstate-P0: 3880MHz
```

```
-----
15. tuned-adm active
    Current active profile: latency-performance
```

```
-----
16. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space      0
    vm.compaction_proactiveness    20
    vm.dirty_background_bytes      0
    vm.dirty_background_ratio     3
    vm.dirty_bytes                 0
    vm.dirty_expire_centisecs     3000
    vm.dirty_ratio                 8
    vm.dirty_writeback_centisecs   500
    vm.dirtytime_expire_seconds   43200
```

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# SPEC CPU®2017 Floating Point Rate Result

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## Dell Inc.

SPECrate®2017\_fp\_base = 2360

PowerEdge R7725 (AMD EPYC 9965 192-Core Processor)

SPECrate®2017\_fp\_peak = 2490

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Nov-2024  
**Hardware Availability:** Nov-2024  
**Software Availability:** Oct-2024

### Platform Notes (Continued)

```

vm.extfrag_threshold          500
vm.min_unmapped_ratio        1
vm.nr_hugepages               0
vm.nr_hugepages_mempolicy    0
vm.nr_overcommit_hugepages   0
vm.swappiness                 1
vm.watermark_boost_factor    15000
vm.watermark_scale_factor    10
vm.zone_reclaim_mode         1

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                 1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

```

```

-----
20. Disk information
SPEC is set to: /home/cpu2017-1.1.9-aocc500-znerv5_A1
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 ext4 438G 16G 400G 4% /

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:      Dell Inc.
Product:     PowerEdge R7725
Product Family: PowerEdge
Serial:      CSN2601

```

```

-----
22. dmidecode
Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
24x 80AD000080AD HMC94AHBRA277N 64 GB 2 rank 6400, configured at 6000

```

```

-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Dell Inc.
BIOS Version:     1.0.2

```

(Continued on next page)



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Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Nov-2024  
Hardware Availability: Nov-2024  
Software Availability: Oct-2024

## Platform Notes (Continued)

BIOS Date: 10/23/2024  
BIOS Revision: 1.0

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix

(Continued on next page)



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Software Availability: Oct-2024

## Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64

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# SPEC CPU®2017 Floating Point Rate Result

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Hardware Availability: Nov-2024

Tested by: Dell Inc.

Software Availability: Oct-2024

## Base Portability Flags (Continued)

527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

### C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl

### C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl

### Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc  
-lflang -ldl

### Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5

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# SPEC CPU®2017 Floating Point Rate Result

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PowerEdge R7725 (AMD EPYC 9965 192-Core Processor)

SPECrate®2017\_fp\_peak = 2490

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Nov-2024

Hardware Availability: Nov-2024

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

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# SPEC CPU®2017 Floating Point Rate Result

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**Dell Inc.**

SPECrate®2017\_fp\_base = 2360

PowerEdge R7725 (AMD EPYC 9965 192-Core Processor)

SPECrate®2017\_fp\_peak = 2490

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Nov-2024

Hardware Availability: Nov-2024

Software Availability: Oct-2024

## Base Other Flags (Continued)

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50

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# SPEC CPU®2017 Floating Point Rate Result

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Hardware Availability: Nov-2024

Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

519.lbm\_r (continued):

```
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

538.imagick\_r: Same as 519.lbm\_r

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

510.parest\_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas

```
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

Fortran benchmarks:

503.bwaves\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang
```

549.fotonik3d\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

554.roms\_r: Same as 503.bwaves\_r

(Continued on next page)





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Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc -ldl
```

```
526.blender_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes



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Tested by: Dell Inc.

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## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.4.xml>

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