



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

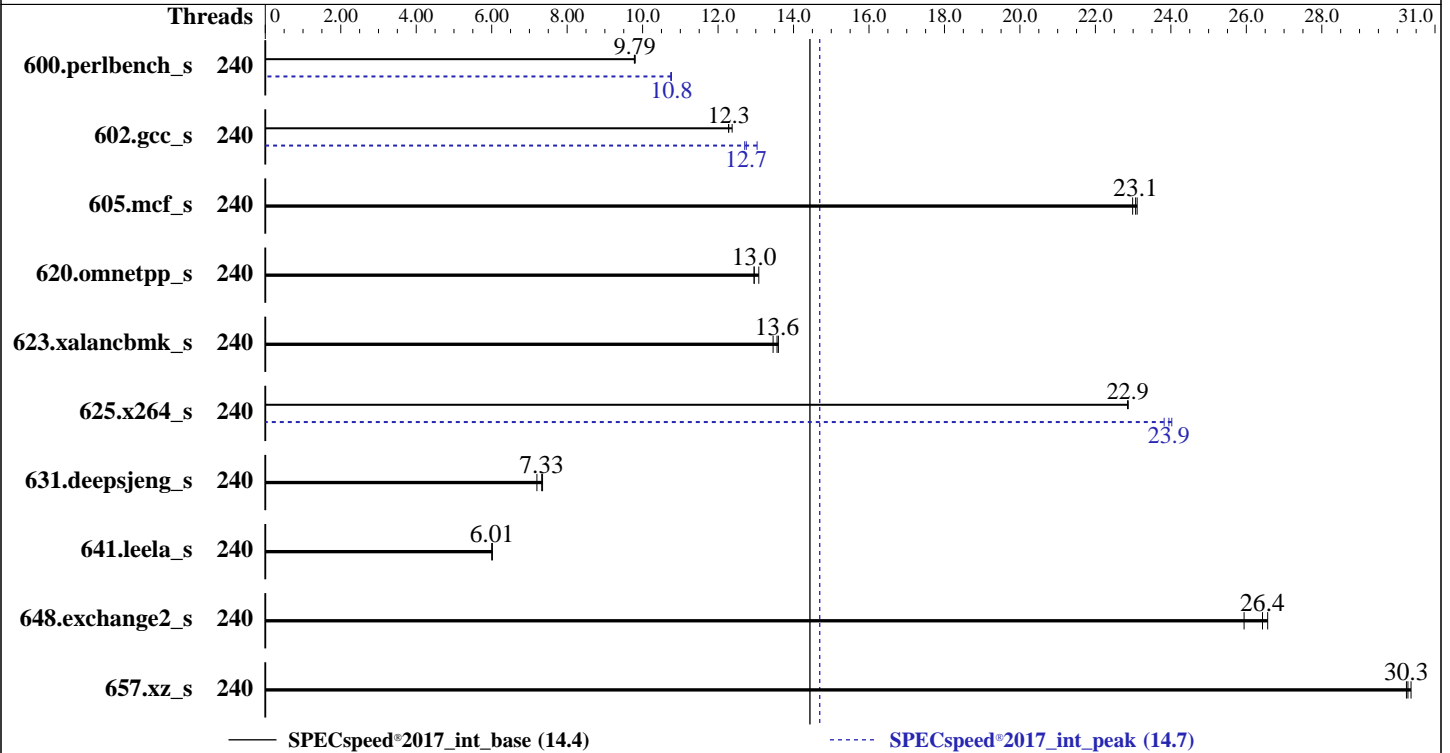
**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero SDI200A3N-212  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems

**Test Date:** Jul-2024  
**Hardware Availability:** Jan-2023  
**Software Availability:** Dec-2023



## Hardware

CPU Name: Intel Xeon Platinum 8580  
Max MHz: 4000  
Nominal: 2000  
Enabled: 120 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 300 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)  
Storage: 1 x 960 GB NVMe  
Other: CPU Cooling: Air

## Software

OS: Red Hat Enterprise Linux 9.3 (Plow)  
5.14.0-362.13.1.el9\_3.x86\_64  
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: Version 2.3 released May-2024  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at cost of additional power.



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero SDI200A3N-212  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

CPU2017 License: 006042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

Test Date: Jul-2024

Hardware Availability: Jan-2023

Software Availability: Dec-2023

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	240	181	9.78	<b>181</b>	<b>9.79</b>	181	9.80	240	165	10.8	165	10.7	<b>165</b>	<b>10.8</b>
602.gcc_s	240	324	12.3	<b>324</b>	<b>12.3</b>	322	12.4	240	306	13.0	<b>312</b>	<b>12.7</b>	313	12.7
605.mcf_s	240	205	23.0	204	23.1	<b>205</b>	<b>23.1</b>	240	205	23.0	204	23.1	<b>205</b>	<b>23.1</b>
620.omnetpp_s	240	<b>126</b>	<b>13.0</b>	126	13.0	125	13.1	240	<b>126</b>	<b>13.0</b>	126	13.0	125	13.1
623.xalancbmk_s	240	<b>104</b>	<b>13.6</b>	105	13.5	104	13.6	240	<b>104</b>	<b>13.6</b>	105	13.5	104	13.6
625.x264_s	240	<b>77.2</b>	<b>22.9</b>	77.1	22.9	77.2	22.9	240	73.4	24.0	74.0	23.8	<b>73.7</b>	<b>23.9</b>
631.deepsjeng_s	240	195	7.35	199	7.20	<b>196</b>	<b>7.33</b>	240	195	7.35	199	7.20	<b>196</b>	<b>7.33</b>
641.leela_s	240	284	6.02	<b>284</b>	<b>6.01</b>	284	6.00	240	284	6.02	<b>284</b>	<b>6.01</b>	284	6.00
648.exchange2_s	240	111	26.6	113	25.9	<b>111</b>	<b>26.4</b>	240	111	26.6	113	25.9	<b>111</b>	<b>26.4</b>
657.xz_s	240	<b>204</b>	<b>30.3</b>	204	30.2	204	30.4	240	<b>204</b>	<b>30.3</b>	204	30.2	204	30.4

SPECspeed®2017\_int\_base = **14.4**

SPECspeed®2017\_int\_peak = **14.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
**Tyrone Camarero SDI200A3N-212**  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems

**Test Date:** Jul-2024  
**Hardware Availability:** Jan-2023  
**Software Availability:** Dec-2023

## General Notes (Continued)

system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:  
Power Technology = Custom  
ENERGY\_PERF\_BIAS\_CFG mode = Maximum Performance  
KTI Prefetch = Enable  
LLC Dead Line Alloc = Disable

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Fri Jul 19 15:50:24 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-18.el9)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----  
1. uname -a  
Linux localhost.localdomain 5.14.0-362.13.1.el9\_3.x86\_64 #1 SMP PREEMPT\_DYNAMIC Fri Nov 24 01:57:57 EST  
2023 x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

-----  
2. w  
15:50:24 up 1 day, 0 min, 1 user, load average: 0.00, 0.00, 0.00  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 Thu16 8.00s 0.91s 0.00s -bash  
-----

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Tyrone Systems**

(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero SDI200A3N-212**  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

**Test Date:** Jul-2024

**Hardware Availability:** Jan-2023

**Software Availability:** Dec-2023

## Platform Notes (Continued)

### 3. Username

From environment variable \$USER: root

### 4. ulimit -a

```

real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 4126531
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 4126531
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```

### 5. sysinfo process ancestry

```

/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=120 --tune base,peak -o all --define
intspeedaffinity --define smt-on --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=120 --tune base,peak --output_format all
--define intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base:peak
--size refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.003/templogs/preenv.intspeed.003.0.log --lognum 003.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

### 6. /proc/cpuinfo

```

model name      : INTEL(R) XEON(R) PLATINUM 8580
vendor_id      : GenuineIntel
cpu family     : 6
model          : 207
stepping       : 2
microcode      : 0x21000240
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 60
siblings       : 120
2 physical ids (chips)
240 processors (hardware threads)
physical id 0: core ids 0-59
physical id 1: core ids 0-59
physical id 0: apicids 0-119
physical id 1: apicids 128-247
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero SDI200A3N-212  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

CPU2017 License: 006042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

Test Date: Jul-2024

Hardware Availability: Jan-2023

Software Availability: Dec-2023

## Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:                46 bits physical, 57 bits virtual
Byte Order:                   Little Endian
CPU(s):                       240
On-line CPU(s) list:         0-239
Vendor ID:                    GenuineIntel
BIOS Vendor ID:              Intel(R) Corporation
Model name:                   INTEL(R) XEON(R) PLATINUM 8580
BIOS Model name:             INTEL(R) XEON(R) PLATINUM 8580
CPU family:                   6
Model:                        207
Thread(s) per core:          2
Core(s) per socket:          60
Socket(s):                    2
Stepping:                     2
Frequency boost:              enabled
CPU max MHz:                  2001.0000
CPU min MHz:                  800.0000
BogoMIPS:                     4000.00
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                                rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                                xtology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq
                                dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                                pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
                                avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2
                                cdp_l3 invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
                                ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase
                                tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
                                rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
                                avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                                cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni avx512_bf16
                                wbnoinvd dtherm ida arat pln pts vnmi avx512vbmi umip pku ospke waitpkg
                                avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                                avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                                enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16
                                avx512_fp16 amx_tile amx_int8 flush_lld arch_capabilities
Virtualization:              VT-x
L1d cache:                   5.6 MiB (120 instances)
L1i cache:                   3.8 MiB (120 instances)
L2 cache:                    240 MiB (120 instances)
L3 cache:                    600 MiB (2 instances)
NUMA node(s):                4
NUMA node0 CPU(s):           0-29,120-149
NUMA node1 CPU(s):           30-59,150-179
NUMA node2 CPU(s):           60-89,180-209
NUMA node3 CPU(s):           90-119,210-239
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:  Not affected
Vulnerability Lltf:          Not affected
Vulnerability Mds:           Not affected
Vulnerability Meltdown:      Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:      Not affected
Vulnerability Spec rstack overflow: Not affected

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero SDI200A3N-212  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

**Test Date:** Jul-2024

**Hardware Availability:** Jan-2023

**Software Availability:** Dec-2023

## Platform Notes (Continued)

Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	5.6M	12	Data	1	64	1	64
L1i	32K	3.8M	8	Instruction	1	64	1	64
L2	2M	240M	16	Unified	2	2048	1	64
L3	300M	600M	20	Unified	3	245760	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-29,120-149
node 0 size: 257614 MB
node 0 free: 223281 MB
node 1 cpus: 30-59,150-179
node 1 size: 258033 MB
node 1 free: 226230 MB
node 2 cpus: 60-89,180-209
node 2 size: 258033 MB
node 2 free: 226379 MB
node 3 cpus: 90-119,210-239
node 3 size: 258017 MB
node 3 free: 224970 MB
node distances:
node  0  1  2  3
 0:  10  12  21  21
 1:  12  10  21  21
 2:  21  21  10  12
 3:  21  21  12  10

```

9. /proc/meminfo

MemTotal: 1056459424 kB

10. who -r

run-level 3 Jul 18 15:50

11. Systemd service manager version: systemd 252 (252-18.e19)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewallld gdm getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt lm_sensors low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname nvme-fc-boot-connections ostree-remount pmcd pmie pmlogger power-profiles-daemon qemu-guest-agent rshmcertd rpcbind rsyslog rtkit-daemon selinux-autorelabel-mark smartd sshd sssd switcheroo-control sysstat systemd-boot-update

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero SDI200A3N-212  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

CPU2017 License: 006042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

Test Date: Jul-2024

Hardware Availability: Jan-2023

Software Availability: Dec-2023

## Platform Notes (Continued)

```

enabled-runtime systemd-network-generator tuned udisks2 upower vgauthd virtqemud vmtoolsd
disabled      systemd-remount-fs
              arp-ethers autofs blk-availability brltty canberra-system-bootup canberra-system-shutdown
              canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed
              dbus-daemon debug-shell dnf-system-upgrade dnsmasq dovecot fancontrol fcoe grafana-server
              gssproxy httpd httpd@ ibacm iprdump iprinit iprupdate ipsec iscsid iscsiuiio kpatch
              kvm_stat ledmon libvirt-guests libvirtdd lldpad man-db-restart-cache-update named
              named-chroot netavark-dhcp-proxy nfs-blkmap nfs-server nftables nmb numad nvmmf-autoconnect
              ostree-readonly-sysroot-migration pesign pmfind pmie_farm pmlogger_farm pmproxy podman
              podman-auto-update podman-clean-transient podman-kube@ podman-restart postfix powertop
              psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmbd-rebuild rrdcached saslauthd
              selinux-check-proper-disable serial-getty@ smb snmpd snmptrapd spamassassin
              speech-dispatcherd srp_daemon srp_daemon_port@ sshd-keygen@ systemd-boot-check-no-failures
              systemd-nspawn@ systemd-pstore systemd-sysext target targetclid tog-pegasus trace-cmd
              virtinterfaced virtnetworkd virtnodeudev virtnwfilterd virtproxyd virtsecret d virtstoraged
              vsftpd wpa_supplicant
indirect      pcsd spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
              systemd-sysupdate systemd-sysupdate-reboot virtlockd virtlogd vsftpd@

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-362.13.1.el9_3.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 2.00 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. tuned-adm active
  Current active profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 40
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Tyrone Systems**

(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero SDI200A3N-212**

(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

**Test Date:** Jul-2024

**Hardware Availability:** Jan-2023

**Software Availability:** Dec-2023

## Platform Notes (Continued)

```

vm.nr_overcommit_hugepages      0
vm.swappiness                    10
vm.watermark_boost_factor       15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release           Red Hat Enterprise Linux 9.3 (Plow)
redhat-release       Red Hat Enterprise Linux release 9.3 (Plow)
system-release       Red Hat Enterprise Linux release 9.3 (Plow)

```

```

-----
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem           Type      Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs       856G  589G  267G  69% /home

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:              Tyrone Systems
Product:              Tyrone Camarero SDI200A3N-212
Product Family:      Family
Serial:               0123456789

```

```

-----
22. dmidecode
Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800

```

```

-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:         American Megatrends International, LLC.
BIOS Version:        2.3
BIOS Date:           05/14/2024
BIOS Revision:       5.32

```





# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
**Tyrone Camarero SDI200A3N-212**  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems

**Test Date:** Jul-2024  
**Hardware Availability:** Jan-2023  
**Software Availability:** Dec-2023

## Compiler Version Notes

-----  
C | 600.perlbench\_s(base, peak) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak) 625.x264\_s(base, peak)  
657.xz\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

-----  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak) 631.deepsjeng\_s(base, peak)  
641.leela\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

-----  
Fortran | 648.exchange2\_s(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Tyrone Systems**

(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero SDI200A3N-212**  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

**Test Date:** Jul-2024

**Hardware Availability:** Jan-2023

**Software Availability:** Dec-2023

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Tyrone Systems**

(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero SDI200A3N-212**  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

**Test Date:** Jul-2024

**Hardware Availability:** Jan-2023

**Software Availability:** Dec-2023

## Peak Optimization Flags (Continued)

600.perlbench\_s (continued):

```
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

602.gcc\_s: -w -m64 -std=c11 -Wl,-z,muldefs

```
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

605.mcf\_s: basepeak = yes

625.x264\_s: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3

```
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

657.xz\_s: basepeak = yes

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.xml>



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
**Tyrone Camarero SDI200A3N-212**  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECspeed®2017\_int\_base = 14.4

SPECspeed®2017\_int\_peak = 14.7

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems

**Test Date:** Jul-2024  
**Hardware Availability:** Jan-2023  
**Software Availability:** Dec-2023

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-07-19 06:20:23-0400.  
Report generated on 2024-08-14 13:59:58 by CPU2017 PDF formatter v6716.  
Originally published on 2024-08-13.