



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_fp_base = 638

PowerEdge C6620 (Intel Xeon Gold 6438Y+)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6573

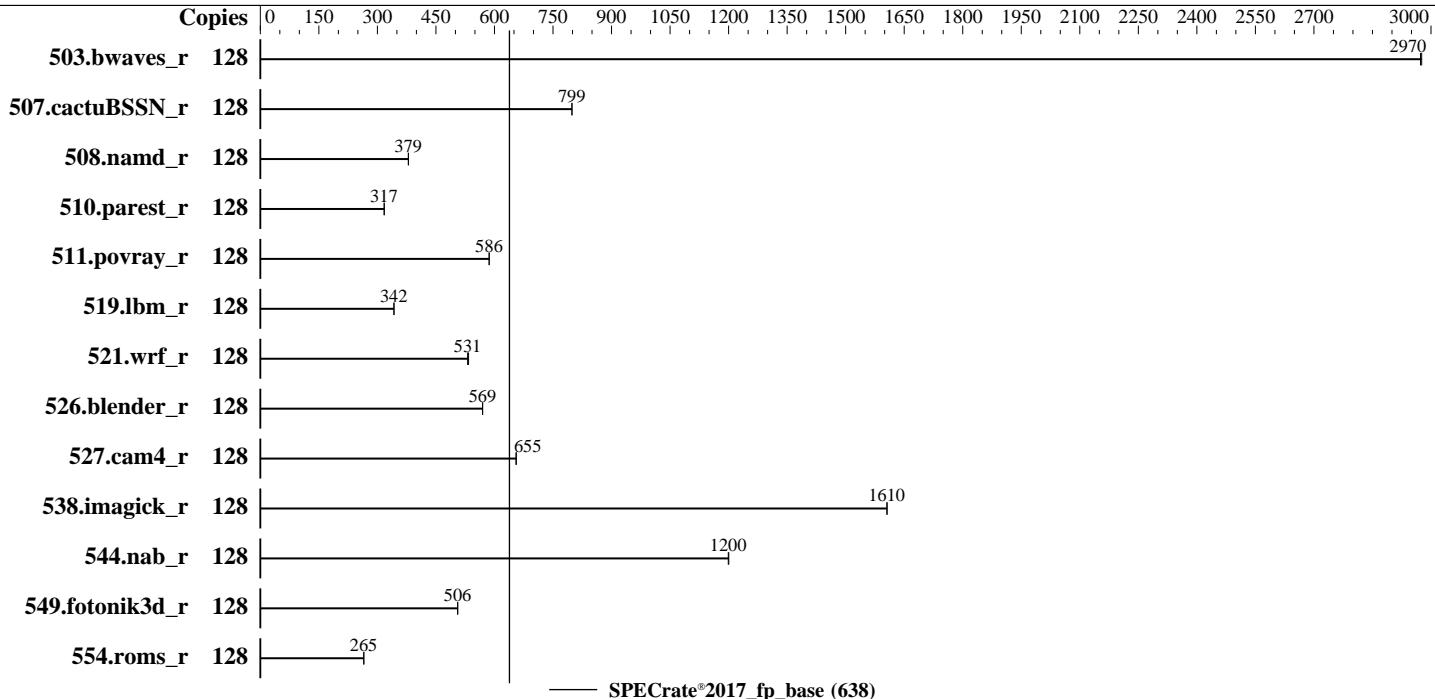
Test Date: Apr-2024

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2023

Tested by: Dell Inc.

Software Availability: Dec-2023



SPECrate®2017_fp_base (638)

Hardware

CPU Name: Intel Xeon Gold 6438Y+
 Max MHz: 4000
 Nominal: 2000
 Enabled: 64 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 60 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 80 GB on tmpfs
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5
 5.14.21-150500.53-default
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: Version 2.0.0 released Nov-2023
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|------------------|--------|------------|------------|-------------|-------------|---------|-------|--------|---------|-------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 128 | 431 | 2980 | 432 | 2970 | | | | | | | | | | | |
| 507.cactusBSSN_r | 128 | 203 | 799 | 203 | 799 | | | | | | | | | | | |
| 508.namd_r | 128 | 321 | 379 | 320 | 379 | | | | | | | | | | | |
| 510.parest_r | 128 | 1054 | 318 | 1056 | 317 | | | | | | | | | | | |
| 511.povray_r | 128 | 510 | 587 | 510 | 586 | | | | | | | | | | | |
| 519.lbm_r | 128 | 394 | 342 | 394 | 343 | | | | | | | | | | | |
| 521.wrf_r | 128 | 537 | 534 | 540 | 531 | | | | | | | | | | | |
| 526.blender_r | 128 | 342 | 569 | 342 | 570 | | | | | | | | | | | |
| 527.cam4_r | 128 | 341 | 656 | 342 | 655 | | | | | | | | | | | |
| 538.imagick_r | 128 | 198 | 1610 | 198 | 1610 | | | | | | | | | | | |
| 544.nab_r | 128 | 179 | 1200 | 180 | 1200 | | | | | | | | | | | |
| 549.fotonik3d_r | 128 | 986 | 506 | 986 | 506 | | | | | | | | | | | |
| 554.roms_r | 128 | 768 | 265 | 766 | 265 | | | | | | | | | | | |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
  "/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
`sync; echo 3 > /proc/sys/vm/drop_caches`
 runcpu command invoked through numactl i.e.:
`numactl --interleave=all runcpu <etc>`
`jemalloc`, a general purpose malloc implementation

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General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 80 GB ramdisk created with the cmd: "mount -t tmpfs -o size=80G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

 ADDDC Setting : Disabled
 DIMM Self Healing on
 Uncorrectable Memory Error : Disabled

 Virtualization Technology : Disabled
 DCU Streamer Prefetcher : Disabled
 LLC Prefetch : Disabled
 Dead Line LLC Alloc : Disabled

 System Profile : Custom
 CPU Power Management : Maximum Performance
 C1E : Disabled
 C States : Autonomous
 Memory Patrol Scrub : Disabled
 Energy Efficiency Policy : Performance
 PCI ASPM L1 Link
 Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed Apr 24 21:15:05 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage

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Platform Notes (Continued)

17. /sys/kernel/mm/transparent_hugepage/khugepaged

18. OS release

19. Disk information

20. /sys/devices/virtual/dmi/id

21. dmidecode

22. BIOS

1. uname -a

```
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

2. w

```
21:15:05 up 2:31, 1 user, load average: 100.01, 120.77, 124.35
USER      TTY      FROM             LOGIN@     IDLE    JCPU      PCPU WHAT
root      ttys1          -           18:58     2:16m  1.10s  0.00s /bin/bash
/home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define
DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt --tune base
```

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 4126059
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4126059
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
/bin/bash ./DELL_rate.sh --tune base
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate --tune base
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate --tune base
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc
--define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt
--tune base
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc
--define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt
--tune base
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
```

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Platform Notes (Continued)

```
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define
DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2
--output_format html,pdf,txt --tune base fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
--define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define
DL-VERS=5.2 --output_format html,pdf,txt --tune base --nopower --runmode rate --tune base --size refrate
fprate --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum
002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
```

```
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6438Y+
vendor_id       : GenuineIntel
cpu family     : 6
model          : 143
stepping        : 8
microcode       : 0x2b000571
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_pbrsb
cpu cores       : 32
siblings        : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                128
On-line CPU(s) list:   0-127
Vendor ID:              GenuineIntel
Model name:             Intel(R) Xeon(R) Gold 6438Y+
CPU family:             6
Model:                 143
Thread(s) per core:    2
Core(s) per socket:    32
Socket(s):              2
Stepping:               8
CPU max MHz:            4000.0000
CPU min MHz:            800.0000
BogoMIPS:               4000.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
```

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Platform Notes (Continued)

```
abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
avx2 smep bmi2 erms invpcid rtm cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512fma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
cqmq_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hfi avx512vbmi umip pkru ospke waitpkg avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid
bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lll arch_capabilities
```

L1d cache: 3 MiB (64 instances)

L1i cache: 2 MiB (64 instances)

L2 cache: 128 MiB (64 instances)

L3 cache: 120 MiB (2 instances)

NUMA node(s): 2

NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,5
2,54,56,58,60,62,64,66,68,70,72,74,76,78,80,82,84,86,88,90,92,94,96,98,100
,102,104,106,108,110,112,114,116,118,120,122,124,126

NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,5
3,55,57,59,61,63,65,67,69,71,73,75,77,79,81,83,85,87,89,91,93,95,97,99,101
,103,105,107,109,111,113,115,117,119,121,123,125,127

Vulnerability Itlb multihit: Not affected

Vulnerability L1tf: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE | LEVEL | SETS | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d | 48K | 3M | 12 | Data | 1 | 64 | | 64 |
| L1i | 32K | 2M | 8 | Instruction | 1 | 64 | | 64 |
| L2 | 2M | 128M | 16 | Unified | 2 | 2048 | | 64 |
| L3 | 60M | 120M | 15 | Unified | 3 | 65536 | | 64 |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus:
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72,7
4,76,78,80,82,84,86,88,90,92,94,96,98,100,102,104,106,108,110,112,114,116,118,120,122,124,126
node 0 size: 515531 MB
node 0 free: 514290 MB
node 1 cpus:
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63,65,67,69,71,73,7
5,77,79,81,83,85,87,89,91,93,95,97,99,101,103,105,107,109,111,113,115,117,119,121,123,125,127
node 1 size: 516006 MB
node 1 free: 506405 MB
node distances:
node 0 1
0: 10 21
1: 21 10

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Platform Notes (Continued)

```
-----  
9. /proc/meminfo  
MemTotal:      1056295136 kB  
  
-----  
10. who -r  
run-level 3 Apr 24 18:44  
  
-----  
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)  
Default Target  Status  
multi-user      running  
  
-----  
12. Services, from systemctl list-unit-files  
STATE          UNIT FILES  
enabled        apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings  
               nvmefc-boot-connections postfix purge-kernels rollback sshd systemd-pstore wicked  
               wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime   systemd-remount-fs  
disabled       autos boot-sysctl ca-certificates chrony-wait chronynd console-getty debug-shell ebttables  
               grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load lunmask nfs  
               nfs-blkmap nfs-server nfsserver nvmf-autoconnect rpcbind rpmconfigcheck rsyncd  
               serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext  
               systemd-time-wait-sync systemd-timesyncd  
generated      ipmievd  
indirect       wickedd  
  
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=aalffe9d-5851-4d06-be29-02be8b9d35b8  
splash=silent  
mitigations=auto  
quiet  
security=apparmor  
  
-----  
14. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 800 MHz and 4.00 GHz.  
              The governor "powersave" may decide which speed to use  
              within this range.  
boost state support:  
  Supported: yes  
  Active: yes  
  
-----  
15. sysctl  
kernel.numa_balancing      1  
kernel.randomize_va_space   2  
vm.compaction_proactiveness 20  
vm.dirty_background_bytes   0  
vm.dirty_background_ratio   10  
vm.dirty_bytes              0  
vm.dirty_expire_centisecs  3000  
vm.dirty_ratio              20  
vm.dirty_writeback_centisecs 500  
vm.dirtytime_expire_seconds 43200  
vm.extfrag_threshold        500
```

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```
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode           0

-----
16. /sys/kernel/mm/transparent_hugepage
    defrag           always defer defer+madvise [madvise] never
    enabled          [always] madvise never
    hpage_pmd_size   2097152
    shmem_enabled    always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                  1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan           4096
    scan_sleep_millisecs   10000

-----
18. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP5

-----
19. Disk information
    SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
    Filesystem      Type  Size  Used Avail Use% Mounted on
    tmpfs          tmpfs  80G   4.1G  76G   6% /mnt/ramdisk

-----
20. /sys/devices/virtual/dmi/id
    Vendor:        Dell Inc.
    Product:       PowerEdge C6620
    Product Family: PowerEdge
    Serial:        SL6C201

-----
21. dmidecode
    Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
    Memory:
    3x 002C0632002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
    4x 00AD00B300AD HMCG94AEBRA123N 64 GB 2 rank 4800
    7x 00AD063200AD HMCG94AEBRA109N 64 GB 2 rank 4800
    2x 00AD063200AD HMCG94MEBRA109N 64 GB 2 rank 4800

-----
22. BIOS
    (This section combines info from /sys/devices and dmidecode.)
```

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Platform Notes (Continued)

BIOS Vendor: Dell Inc.
BIOS Version: 2.0.0
BIOS Date: 11/23/2023
BIOS Revision: 2.0

Compiler Version Notes

=====
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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=====

=====
C++ | 508.namd_r(base) 510.parest_r(base)
=====

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=====

=====
C++, C | 511.povray_r(base) 526.blender_r(base)
=====

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=====

=====
C++, C, Fortran | 507.cactuBSSN_r(base)
=====

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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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=====

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====

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=====

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
=====

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SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECCrate®2017_fp_base = 638

PowerEdge C6620 (Intel Xeon Gold 6438Y+)

SPECCrate®2017_fp_peak = Not Run

CPU2017 License: 6573

Test Date: Apr-2024

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2023

Tested by: Dell Inc.

Software Availability: Dec-2023

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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Test Date: Apr-2024

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2023

Tested by: Dell Inc.

Software Availability: Dec-2023

Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml>

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