



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

(Tyrone Camarero SDA200A2N-212)
(2.4 GHz, AMD EPYC 9654)

SPECrate®2017_fp_base = 1000

SPECrate®2017_fp_peak = 1170

CPU2017 License: 6042

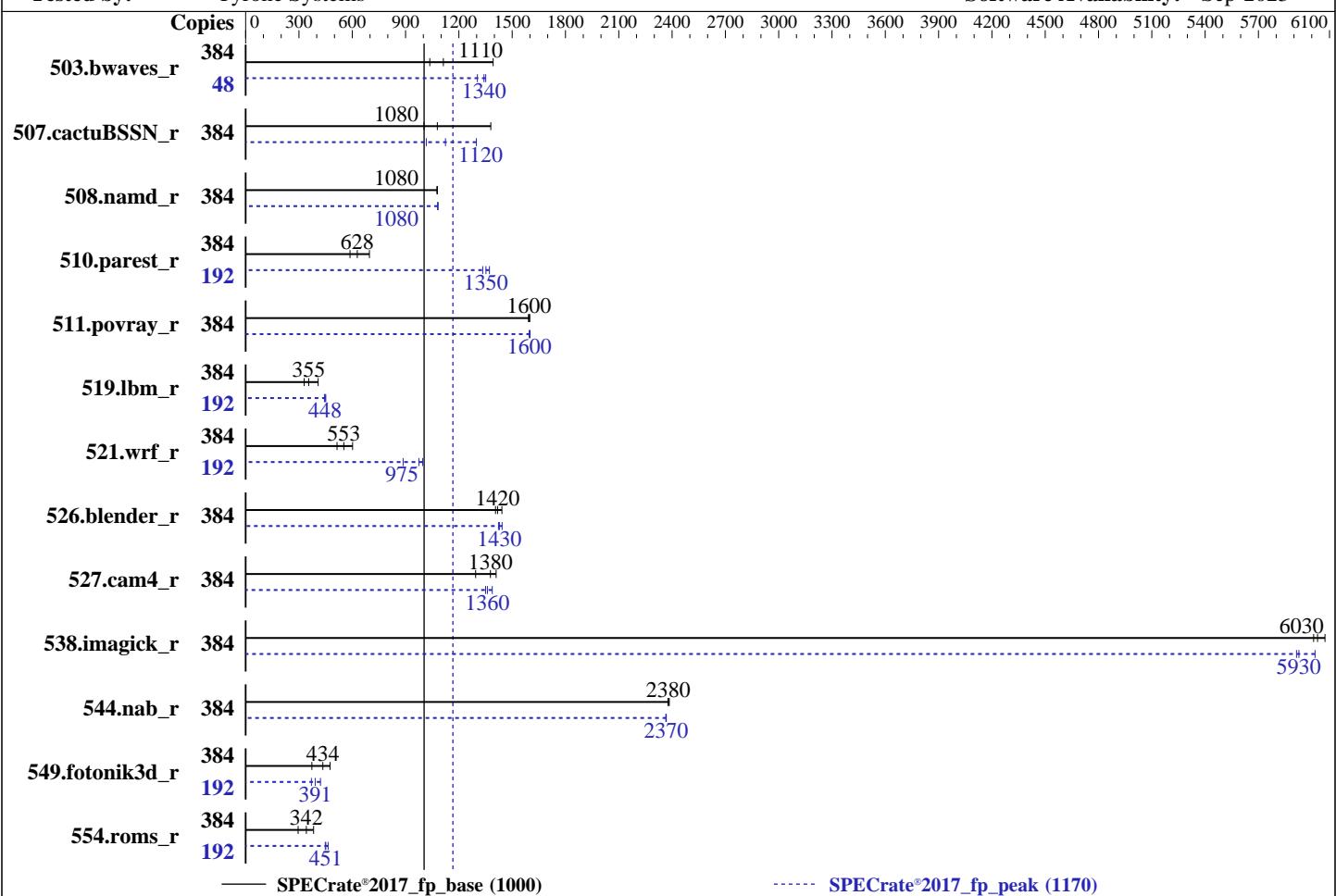
Test Date: Sep-2023

Test Sponsor: Netweb Pte Ltd

Hardware Availability: Jun-2023

Tested by: Tyrone Systems

Software Availability: Sep-2023



Hardware		Software	
CPU Name:	AMD EPYC 9654	OS:	Ubuntu 20.04.4 LTS
Max MHz:	3700		kernel version
Nominal:	2400		5.15.0-84-generic
Enabled:	192 cores, 2 chips, 2 threads/core	Compiler:	C/C++/Fortran: Version 4.0.0 of AOCC
Orderable:	1,2 chips	Parallel:	No
Cache L1:	32 KB I + 32 KB D on chip per core	Firmware:	Version 1.4 released Apr-2023
L2:	1 MB I+D on chip per core	File System:	ext4
L3:	384 MB I+D on chip per chip, 32 MB shared / 8 cores	System State:	Run level 5 (multi-user)
Other:	None	Base Pointers:	64-bit
Memory:	1472 GB (23 x 64 GB 2Rx4 PC5-4800B-R)	Peak Pointers:	64-bit
Storage:	1 x 960 GB NVMe	Other:	none
Other:	None	Power Management:	OS is set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	384	3716	1040	3461	1110	2765	1390	48	356	1350	359	1340	369	1300
507.cactubSSN_r	384	484	1000	451	1080	352	1380	384	374	1300	432	1120	478	1020
508.namd_r	384	338	1080	338	1080	339	1080	384	337	1080	337	1080	337	1080
510.parest_r	384	1710	587	1599	628	1442	697	192	376	1330	366	1370	371	1350
511.povray_r	384	561	1600	562	1600	563	1590	384	561	1600	560	1600	562	1600
519.lbm_r	384	1229	329	1141	355	992	408	192	456	443	449	451	452	448
521.wrf_r	384	1672	514	1557	553	1429	602	192	485	886	432	996	441	975
526.blender_r	384	416	1410	412	1420	406	1440	384	405	1440	409	1430	411	1420
527.cam4_r	384	519	1290	488	1380	476	1410	384	484	1390	494	1360	497	1350
538.imagick_r	384	159	6010	158	6030	157	6070	384	159	6020	161	5930	161	5910
544.nab_r	384	271	2380	272	2380	272	2380	384	273	2370	273	2370	273	2370
549.fotonik3d_r	384	4016	373	3149	475	3445	434	192	1777	421	1911	391	2021	370
554.roms_r	384	2065	295	1786	342	1595	382	192	657	464	681	448	676	451

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,

(Continued on next page)



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Operating System Notes (Continued)

```
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_rate_aocc400_znver4_A_lib/lib:/home/cpu2017/amd_rate_aocc400_znver4_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
cTDP: 360
Determinism Slider set to Power
Package Power: 360
EDC: 400
ACPI SRAT L3 Cache as NUMA Domain: enabled
Memory interleaving: Disabled
4-link xGMI max speed: 16Gbps
Fan Speed: Maximum

```
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on amd2-Super-Server Thu Sep 28 00:53:27 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo

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Platform Notes (Continued)

```
10. who -r
11. Systemd service manager version: systemd 245 (245.4-4ubuntu3.20)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
-----
-----
1. uname -a
Linux amd2-Super-Server 5.15.0-84-generic #93~20.04.1-Ubuntu SMP Wed Sep 6 16:15:40 UTC 2023 x86_64 x86_64
x86_64 GNU/Linux
-----
2. w
00:53:27 up 8:01, 1 user, load average: 158.59, 318.06, 354.78
USER      TTY      FROM          LOGIN@      IDLE      JCPU      PCPU WHAT
amd2      tty2          -           16:53     7:57m   2.23s   0.05s -bash
-----
3. Username
From environment variable $USER: root
From the command 'logname': amd2
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            5932677
nofiles            1024
vmemory(kbytes)    unlimited
locks               unlimited
rtprio              0
-----
5. sysinfo process ancestry
/sbin/init splash
/bin/login -p --
-bash
sudo su
su
bash
python3 ./run_amd_rate_aocc400_znver4_A1.py
/bin/bash ./amd_rate_aocc400_znver4_A1.sh
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
```

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Platform Notes (Continued)

```
specperl $SPEC/bin/sysinfo  
$SPEC = /home/cpu2017
```

```
-----  
6. /proc/cpuinfo  
    model name      : AMD EPYC 9654 96-Core Processor  
    vendor_id       : AuthenticAMD  
    cpu family     : 25  
    model          : 17  
    stepping        : 1  
    microcode       : 0xa10113e  
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass  
    TLB size        : 3584 4K pages  
    cpu cores       : 96  
    siblings        : 192  
    2 physical ids (chips)  
    384 processors (hardware threads)  
    physical id 0: core ids 0-95  
    physical id 1: core ids 0-95  
    physical id 0: apicids 0-191  
    physical id 1: apicids 256-447
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

From lscpu from util-linux 2.34:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Byte Order:	Little Endian
Address sizes:	52 bits physical, 57 bits virtual
CPU(s):	384
On-line CPU(s) list:	0-383
Thread(s) per core:	2
Core(s) per socket:	96
Socket(s):	2
NUMA node(s):	2
Vendor ID:	AuthenticAMD
CPU family:	25
Model:	17
Model name:	AMD EPYC 9654 96-Core Processor
Stepping:	1
Frequency boost:	enabled
CPU MHz:	1500.000
CPU max MHz:	3707.8120
CPU min MHz:	1500.0000
BogoMIPS:	4799.74
Virtualization:	AMD-V
L1d cache:	6 MiB
L1i cache:	6 MiB
L2 cache:	192 MiB
L3 cache:	768 MiB
NUMA node0 CPU(s):	0-95,192-287
NUMA node1 CPU(s):	96-191,288-383
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Lltf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected

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Platform Notes (Continued)

Vulnerability Mmio stale data:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling, PBRSB-eIBRS Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pn1 pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skininit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bm1 avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmlload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpocntdq la57 rdpid overflow_recov succor smca fsrm flush_lld

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL
L1d	32K	6M	8	Data	1
L1i	32K	6M	8	Instruction	1
L2	1M	192M	8	Unified	2
L3	32M	768M	16	Unified	3

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-95,192-287
node 0 size: 709209 MB
node 0 free: 705336 MB
node 1 cpus: 96-191,288-383
node 1 size: 774035 MB
node 1 free: 768577 MB
node distances:
node 0 1
 0: 10 32
 1: 32 10
```

9. /proc/meminfo

```
MemTotal: 1518842524 kB
```

10. who -r
run-level 5 Sep 26 16:41

11. Systemd service manager version: systemd 245 (245.4-4ubuntu3.20)
Default Target Status
graphical degraded

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Platform Notes (Continued)

- ```
12. Failed units, from systemctl list-units --state=failed
 UNIT LOAD ACTIVE SUB DESCRIPTION
 * fwupd-refresh.service loaded failed Refresh fwupd metadata and update motd
```
- 
- ```
13. Services, from systemctl list-unit-files
    STATE          UNIT   FILES
    enabled        ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
                  accounts-daemon anacron apparmor autovt@ avahi-daemon bluetooth console-setup cron cups
                  cups-browsed dmesg e2scrub_reap getty@ gpu-manager grub-common grub-initrd-fallback
                  irqbalance kerneloops keyboard-setup network-manager networkd-dispatcher ondemand openvpn
                  pppd-dns rsync rsyslog secureboot-db setvtrgb snapd ssh sshd switcheroo-control syslog
                  systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmcs udisks2 ufw
                  unattended-upgrades whoopsie wpa_supplicant
    enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
    disabled       acpid brltty console-getty debug-shell openvpn-client@ openvpn-server@ openvpn@
                  rtkit-daemon serial-getty@ speech-dispatcher speech-dispatcher@ 
    generated      apport
    indirect      display-manager lightdm saned@ spice-vdagent spice-vdagentd uidd
    masked        alsavt cryptdisks cryptdisks-early hwclock pulseaudio-enable-autospawn rc rcS saned
                  sudo x11-common
```
-
- ```
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.15.0-84-generic
root=UUID=1ae71a13-cac0-48f6-b6e6-e15e5e687f57
ro
quiet
splash
vt.handoff=7
```
- 
- ```
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            1
```
-
- ```
16. /sys/kernel/mm/transparent_hugepage
```

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## Platform Notes (Continued)

```
defrag [always] defer defer+madvice madvice never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

---

17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
```

---

18. OS release

```
From /etc/*-release /etc/*-version
os-release Ubuntu 20.04.4 LTS
```

---

19. Disk information

SPEC is set to: /home/cpu2017

| Filesystem     | Type | Size | Used | Avail | Use% | Mounted on |
|----------------|------|------|------|-------|------|------------|
| /dev/nvme1n1p2 | ext4 | 938G | 19G  | 872G  | 3%   | /          |

---

20. /sys/devices/virtual/dmi/id

```
Vendor: Tyrone Systems
Product: Tyrone Camarero SDA200A2N-212
Product Family: SMC H13
Serial: A509935X3906531
```

---

21. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
1x NO DIMM NO DIMM
23x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800
```

---

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.4
BIOS Date: 04/19/2023
BIOS Revision: 5.27
```

## Compiler Version Notes

---

```
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
```

---

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
```

(Continued on next page)



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## Compiler Version Notes (Continued)

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++, C, Fortran | 507.cactusBSSN\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

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Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

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# SPEC CPU®2017 Floating Point Rate Result

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Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

(Tyrone Camarero SDA200A2N-212)  
(2.4 GHz, AMD EPYC 9654)

SPECrate®2017\_fp\_base = 1000

SPECrate®2017\_fp\_peak = 1170

CPU2017 License: 6042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

Test Date: Sep-2023

Hardware Availability: Jun-2023

Software Availability: Sep-2023

## Compiler Version Notes (Continued)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64



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## Tyrone Systems

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**SPECrate®2017\_fp\_peak = 1170**

**CPU2017 License:** 6042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

**Test Date:** Sep-2023

**Hardware Availability:** Jun-2023

**Software Availability:** Sep-2023

## Base Optimization Flags

C benchmarks:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -lamdlibm -lamdalloc -lflang
```

C++ benchmarks:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang
```

Fortran benchmarks:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -Kieee -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -fsto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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|                                                                                                                        |                                                                                                                        |
|------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------|
| <b>Tyrone Systems</b><br>(Test Sponsor: Netweb Pte Ltd)<br>(Tyrone Camarero SDA200A2N-212)<br>(2.4 GHz, AMD EPYC 9654) | <b>SPECrate®2017_fp_base = 1000</b><br><b>SPECrate®2017_fp_peak = 1170</b>                                             |
| <b>CPU2017 License:</b> 6042<br><br><b>Test Sponsor:</b> Netweb Pte Ltd<br><br><b>Tested by:</b> Tyrone Systems        | <b>Test Date:</b> Sep-2023<br><br><b>Hardware Availability:</b> Jun-2023<br><br><b>Software Availability:</b> Sep-2023 |

## Base Optimization Flags (Continued)

## Benchmarks using Fortran, C, and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIB -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -Kieee -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang
```

## Base Other Flags

### C benchmarks:

`-Wno-unused-command-line-argument`

## C++ benchmarks:

-Wno-unused-command-line-argument

## Fortran benchmarks:

-Wno-unused-command-line-argument

## Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

### Benchmarks using both C and C++:

-Wno-unused-command-line-argument

## Benchmarks using Fortran, C, and C++:

`-Wno-unused-command-line-argument`

---

## Peak Compiler Invocation

### C benchmarks:

clang

## C++ benchmarks:

clang++

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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Tyrone Systems

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(Tyrone Camarero SDA200A2N-212)  
(2.4 GHz, AMD EPYC 9654)

SPECrate®2017\_fp\_base = 1000

SPECrate®2017\_fp\_peak = 1170

CPU2017 License: 6042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

Test Date: Sep-2023

Hardware Availability: Jun-2023

Software Availability: Sep-2023

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

C++ benchmarks:

(Continued on next page)



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**SPECrate®2017\_fp\_base = 1000**

**SPECrate®2017\_fp\_peak = 1170**

CPU2017 License: 6042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

**Test Date:** Sep-2023

**Hardware Availability:** Jun-2023

**Software Availability:** Sep-2023

## Peak Optimization Flags (Continued)

508.namd\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math  
-finline-aggressive -mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc

510.parest\_r: -m64 -flto -Wl,-mllvm -Wl,-suppress-fmas  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math  
-finline-aggressive -mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc

Fortran benchmarks:

503.bwaves\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -Mrecursive  
-mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm  
-lamdalloc -lflang

549.fotonik3d\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -Kieee  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -fvector-transform  
-fscalar-transform -lamdlibm -lamdalloc -lflang

554.roms\_r: Same as 503.bwaves\_r

Benchmarks using both Fortran and C:

521.wrf\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc

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**SPECrate®2017\_fp\_base = 1000**

**SPECrate®2017\_fp\_peak = 1170**

CPU2017 License: 6042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

**Test Date:** Sep-2023

**Hardware Availability:** Jun-2023

**Software Availability:** Sep-2023

## Peak Optimization Flags (Continued)

521.wrf\_r (continued):

-lflang

527.cam4\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt  
-Kieee -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc  
-lflang

Benchmarks using both C and C++:

511.povray\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm  
-lamdaloc

526.blender\_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt  
-finline-aggressive -mllvm -unroll-threshold=100 -lamdlibm  
-lamdaloc

Benchmarks using Fortran, C, and C++:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver4  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000  
-finline-aggressive -faggressive-loop-transform -fvector-transform

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Tested by: Tyrone Systems

Test Date: Sep-2023

Hardware Availability: Jun-2023

Software Availability: Sep-2023

## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-fscalar-transform -Mrecursive -fepilog-vectorization-of-inductions  
-lamdlibm -lamdalloc -lflang

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Genoa-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Genoa-revC.xml>

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