SPEC CPU®2017 Integer Rate Result

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Supermicro
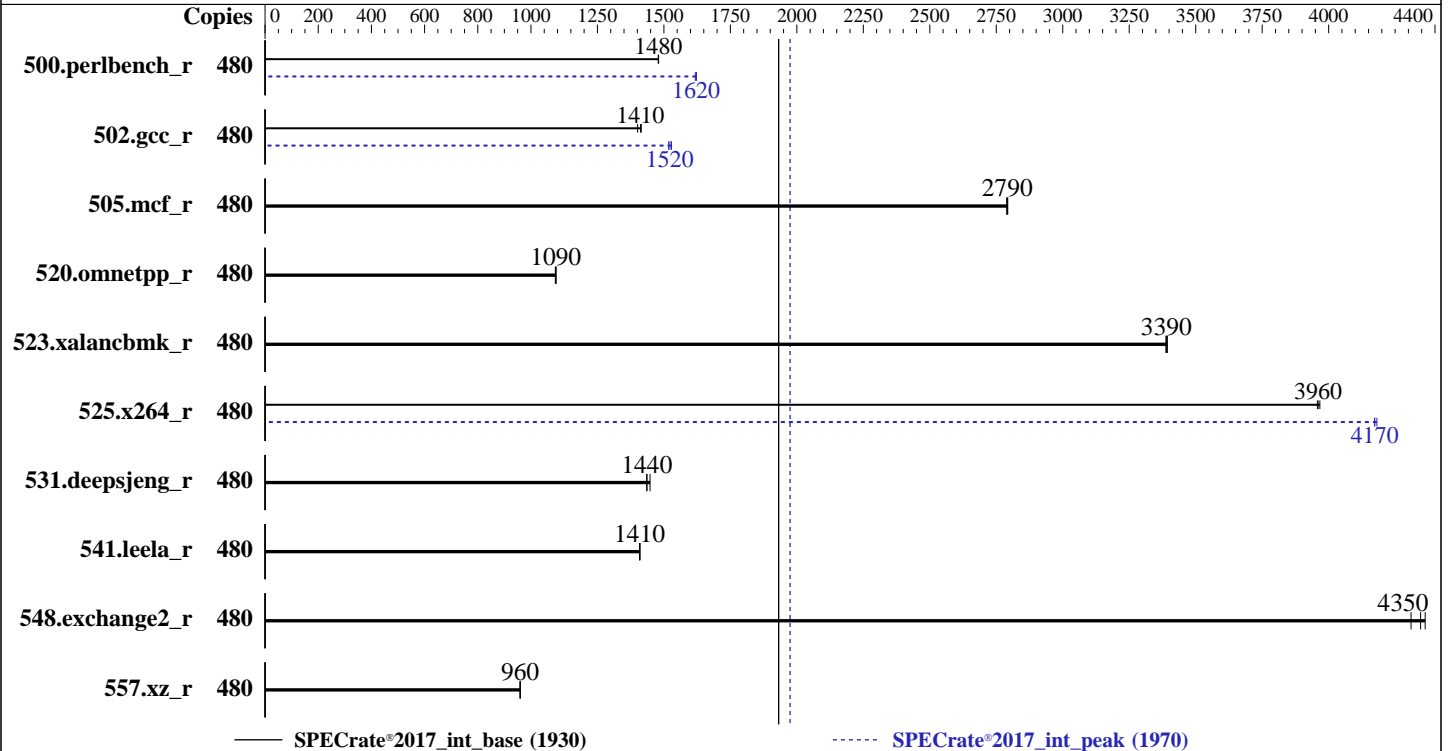
SuperServer SYS-241H-TNRTTP
(X13QEH+ , Intel Xeon Platinum 8490H)

$SPECrate®2017\_int\_base = 1930$

$SPECrate®2017\_int\_peak = 1970$

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Jun-2022

Copies	
500.perlbench_r	480
1480	
1620	
502.gcc_r	480
1410	
1520	
505.mcf_r	480
2790	
520.omnetpp_r	480
1090	
523.xalancbmk_r	480
3390	
525.x264_r	480
3960	
4170	
531.deepsjeng_r	480
1440	
541.leela_r	480
1410	
548.exchange2_r	480
4350	
557.xz_r	480
960	

—— **SPECrate®2017_int_base (1930)**   ------ **SPECrate®2017_int_peak (1970)**

Hardware

CPU Name: Intel Xeon Platinum 8490H
 Max MHz: 3500
 Nominal: 1900
Enabled: 240 cores, 4 chips, 2 threads/core
Orderable: 4 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 112.5 MB I+D on chip per chip
 Other: None
Memory: 1 TB (32 x 32 GB 2Rx8 PC5-4800B-R)
Storage: 1 x 1.90 TB NVMe SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
5.14.21-150400.22-default
Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2023.0 of Intel Fortran Compiler
for Linux;
Parallel: No
Firmware: Version 1.2 released Mar-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of
additional power usage.

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	480	**516**	**1480**	516	1480	517	1480	480	472	1620	**471**	**1620**	471	1620
502.gcc_r	480	485	1400	**481**	**1410**	480	1410	480	**446**	**1520**	448	1520	445	1530
505.mcf_r	480	278	2790	278	2790	**278**	**2790**	480	278	2790	278	2790	**278**	**2790**
520.omnetpp_r	480	576	1090	**576**	**1090**	577	1090	480	576	1090	**576**	**1090**	577	1090
523.xalancbmk_r	480	149	3390	**150**	**3390**	150	3390	480	149	3390	**150**	**3390**	150	3390
525.x264_r	480	212	3960	212	3970	**212**	**3960**	480	201	4180	**201**	**4170**	201	4170
531.deepsjeng_r	480	383	1440	380	1450	**383**	**1440**	480	383	1440	380	1450	**383**	**1440**
541.leela_r	480	564	1410	**564**	**1410**	564	1410	480	564	1410	**564**	**1410**	564	1410
548.exchange2_r	480	**289**	**4350**	288	4360	292	4310	480	**289**	**4350**	288	4360	292	4310
557.xz_r	480	**540**	**960**	540	960	541	959	480	**540**	**960**	540	960	541	959
SPECrate®2017_int_base =		**1930**												
SPECrate®2017_int_peak =		**1970**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
SPEC has ruled that the compiler used for this result was performing a compilation
that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s
benchmarks using a priori knowledge of the SPEC code and dataset to perform a
transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4
https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer
publish results using this optimization.

This result is left in the SPEC results database for historical reference.
```

Submit Notes

```
  The numactl mechanism was used to bind copies to processors. The config file option 'submit'
  was used to generate numactl commands to bind each copy to a specific processor.
  For details, please see the config file.
```

Operating System Notes

```
  Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

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General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>        /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS Settings:
 Hyper-Threading [ALL] = Enable
 Power Technology = Custom
 Power Performance Tuning = BIOS Controls EPB
 ENERGY_PERF_BIAS_CFG mode = Extreme Performance
 DCU Streamer Prefetcher = Disable
 KTI Prefetch = Enable
 SNC = Enable SNC4 (4-clusters)
 LLC Dead Line Alloc = Disable

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on 156-144 Tue Mar 21 15:19:30 2023

 SUT (System Under Test) info as seen by some common utilities.

 -------------------------------------------------------
 Table of contents
 -------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
```

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Platform Notes (Continued)

```
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
   -----------------------------------------------------------

   -----------------------------------------------------------
 1. uname -a
   Linux 156-144 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
   x86_64 x86_64 x86_64 GNU/Linux

   -----------------------------------------------------------
 2. w
    15:19:30 up 8 min,  1 user,  load average: 0.16, 0.79, 0.74
   USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
   root     tty1     -                15:11    3.00s  1.47s  0.06s -bash

   -----------------------------------------------------------
 3. Username
   From environment variable $USER:  root

   -----------------------------------------------------------
 4. ulimit -a
   core file size          (blocks, -c) unlimited
   data seg size           (kbytes, -d) unlimited
   scheduling priority             (-e) 0
   file size               (blocks, -f) unlimited
   pending signals                 (-i) 4124690
   max locked memory       (kbytes, -l) 64
   max memory size         (kbytes, -m) unlimited
   open files                      (-n) 1024
   pipe size            (512 bytes, -p) 8
   POSIX message queues     (bytes, -q) 819200
   real-time priority              (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time               (seconds, -t) unlimited
   max user processes              (-u) 4124690
   virtual memory          (kbytes, -v) unlimited
   file locks                      (-x) unlimited

   -----------------------------------------------------------
 5. sysinfo process ancestry
  /usr/lib/systemd/systemd --switched-root --system --deserialize 30
  login -- root
  -bash
  -bash
  runcpu --nobuild --action validate --define default-platform-flags --define numcopies=480 -c
    ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=240 --define physicalfirst
    --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
  runcpu --nobuild --action validate --define default-platform-flags --define numcopies=480 --configfile
    ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=240 --define physicalfirst
    --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
    --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
  specperl $SPEC/bin/sysinfo
 $SPEC = /home/cpu2017

   -----------------------------------------------------------
 6. /proc/cpuinfo
     model name      : Intel(R) Xeon(R) Platinum 8490H
     vendor_id       : GenuineIntel
```

(Continued on next page)

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Platform Notes (Continued)

```
      cpu family     : 6
      model          : 143
      stepping       : 8
      microcode      : 0x2b0001b0
      bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
      cpu cores      : 60
      siblings       : 120
      4 physical ids (chips)
      480 processors (hardware threads)
      physical id 0: core ids 0-59
      physical id 1: core ids 0-59
      physical id 2: core ids 0-59
      physical id 3: core ids 0-59
      physical id 0: apicids 0-119
      physical id 1: apicids 128-247
      physical id 2: apicids 256-375
      physical id 3: apicids 384-503
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.


    ------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.2:
  Architecture:                 x86_64
  CPU op-mode(s):               32-bit, 64-bit
  Address sizes:                46 bits physical, 57 bits virtual
  Byte Order:                   Little Endian
  CPU(s):                       480
  On-line CPU(s) list:          0-479
  Vendor ID:                    GenuineIntel
  Model name:                   Intel(R) Xeon(R) Platinum 8490H
  CPU family:                   6
  Model:                        143
  Thread(s) per core:           2
  Core(s) per socket:           60
  Socket(s):                    4
  Stepping:                     8
  Frequency boost:              enabled
  CPU max MHz:                  1901.0000
  CPU min MHz:                  800.0000
  BogoMIPS:                     3800.00
  Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                                sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                                lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                                invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                                vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                                bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                                avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                                vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                                bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                                tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
  Virtualization:               VT-x
```

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Platform Notes (Continued)

```
  L1d cache:                     11.3 MiB (240 instances)
  L1i cache:                     7.5 MiB (240 instances)
  L2 cache:                      480 MiB (240 instances)
  L3 cache:                      450 MiB (4 instances)
  NUMA node(s):                  16
  NUMA node0 CPU(s):             0-14,240-254
  NUMA node1 CPU(s):             15-29,255-269
  NUMA node2 CPU(s):             30-44,270-284
  NUMA node3 CPU(s):             45-59,285-299
  NUMA node4 CPU(s):             60-74,300-314
  NUMA node5 CPU(s):             75-89,315-329
  NUMA node6 CPU(s):             90-104,330-344
  NUMA node7 CPU(s):             105-119,345-359
  NUMA node8 CPU(s):             120-134,360-374
  NUMA node9 CPU(s):             135-149,375-389
  NUMA node10 CPU(s):            150-164,390-404
  NUMA node11 CPU(s):            165-179,405-419
  NUMA node12 CPU(s):            180-194,420-434
  NUMA node13 CPU(s):            195-209,435-449
  NUMA node14 CPU(s):            210-224,450-464
  NUMA node15 CPU(s):            225-239,465-479
  Vulnerability Itlb multihit:   Not affected
  Vulnerability L1tf:            Not affected
  Vulnerability Mds:             Not affected
  Vulnerability Meltdown:        Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:      Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:      Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
  Vulnerability Srbds:           Not affected
  Vulnerability Tsx async abort: Not affected

  From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL    SETS PHY-LINE COHERENCY-SIZE
      L1d      48K    11.3M   12 Data              1      64       1              64
      L1i      32K     7.5M    8 Instruction       1      64       1              64
      L2        2M     480M   16 Unified           2    2048       1              64
      L3    112.5M     450M   15 Unified           3  122880       1              64

  ------------------------------------------------------------
  8. numactl --hardware
  NOTE: a numactl 'node' might or might not correspond to a physical chip.
    available: 16 nodes (0-15)
    node 0 cpus: 0-14,240-254
    node 0 size: 64069 MB
    node 0 free: 61050 MB
    node 1 cpus: 15-29,255-269
    node 1 size: 64504 MB
    node 1 free: 62721 MB
    node 2 cpus: 30-44,270-284
    node 2 size: 64504 MB
    node 2 free: 63789 MB
    node 3 cpus: 45-59,285-299
    node 3 size: 64504 MB
    node 3 free: 64046 MB
    node 4 cpus: 60-74,300-314
    node 4 size: 64504 MB
    node 4 free: 64227 MB
    node 5 cpus: 75-89,315-329
    node 5 size: 64504 MB
    node 5 free: 64301 MB
```

(Continued on next page)

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Platform Notes (Continued)

```
   node 6 cpus: 90-104,330-344
   node 6 size: 64504 MB
   node 6 free: 64276 MB
   node 7 cpus: 105-119,345-359
   node 7 size: 64504 MB
   node 7 free: 64296 MB
   node 8 cpus: 120-134,360-374
   node 8 size: 64504 MB
   node 8 free: 64253 MB
   node 9 cpus: 135-149,375-389
   node 9 size: 64504 MB
   node 9 free: 64292 MB
   node 10 cpus: 150-164,390-404
   node 10 size: 64504 MB
   node 10 free: 64267 MB
   node 11 cpus: 165-179,405-419
   node 11 size: 64504 MB
   node 11 free: 64274 MB
   node 12 cpus: 180-194,420-434
   node 12 size: 64504 MB
   node 12 free: 64267 MB
   node 13 cpus: 195-209,435-449
   node 13 size: 64504 MB
   node 13 free: 64293 MB
   node 14 cpus: 210-224,450-464
   node 14 size: 64504 MB
   node 14 free: 64244 MB
   node 15 cpus: 225-239,465-479
   node 15 size: 64065 MB
   node 15 free: 63868 MB
   node distances:
   node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
     0:  10  12  12  12  21  21  21  21  21  21  21  21  21  21  21  21
     1:  12  10  12  12  21  21  21  21  21  21  21  21  21  21  21  21
     2:  12  12  10  12  21  21  21  21  21  21  21  21  21  21  21  21
     3:  12  12  12  10  21  21  21  21  21  21  21  21  21  21  21  21
     4:  21  21  21  21  10  12  12  12  21  21  21  21  21  21  21  21
     5:  21  21  21  21  12  10  12  12  21  21  21  21  21  21  21  21
     6:  21  21  21  21  12  12  10  12  21  21  21  21  21  21  21  21
     7:  21  21  21  21  12  12  12  10  21  21  21  21  21  21  21  21
     8:  21  21  21  21  21  21  21  10  12  12  12  21  21  21  21  21
     9:  21  21  21  21  21  21  21  12  10  12  12  21  21  21  21  21
    10:  21  21  21  21  21  21  21  12  12  10  12  21  21  21  21  21
    11:  21  21  21  21  21  21  21  12  12  12  10  21  21  21  21  21
    12:  21  21  21  21  21  21  21  21  21  21  21  10  12  12  12  21
    13:  21  21  21  21  21  21  21  21  21  21  21  12  10  12  12  21
    14:  21  21  21  21  21  21  21  21  21  21  21  12  12  10  12  21
    15:  21  21  21  21  21  21  21  21  21  21  21  12  12  12  10  21
```

```
   node distances:
   node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
     0:  10  12  12  12  21  21  21  21  21  21  21  21  21  21  21  21
     1:  12  10  12  12  21  21  21  21  21  21  21  21  21  21  21  21
     2:  12  12  10  12  21  21  21  21  21  21  21  21  21  21  21  21
     3:  12  12  12  10  21  21  21  21  21  21  21  21  21  21  21  21
     4:  21  21  21  21  10  12  12  12  21  21  21  21  21  21  21  21
     5:  21  21  21  21  12  10  12  12  21  21  21  21  21  21  21  21
     6:  21  21  21  21  12  12  10  12  21  21  21  21  21  21  21  21
     7:  21  21  21  21  12  12  12  10  21  21  21  21  21  21  21  21
     8:  21  21  21  21  21  21  21  10  12  12  12  21  21  21  21  21
     9:  21  21  21  21  21  21  21  12  10  12  12  21  21  21  21  21
    10:  21  21  21  21  21  21  21  12  12  10  12  21  21  21  21  21
    11:  21  21  21  21  21  21  21  12  12  12  10  21  21  21  21  21
    12:  21  21  21  21  21  21  21  21  21  21  21  10  12  12  12
    13:  21  21  21  21  21  21  21  21  21  21  21  12  10  12  12
    14:  21  21  21  21  21  21  21  21  21  21  21  12  12  10  12
    15:  21  21  21  21  21  21  21  21  21  21  21  12  12  12  10

   ----------------------------------------------------------
9. /proc/meminfo
   MemTotal:      1055945504 kB


   ----------------------------------------------------------
10. who -r
   run-level 3 Mar 21 15:11


   ----------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
   Default Target  Status
```

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Platform Notes (Continued)

```
  multi-user      running


    -------------------------------------------------------------
12. Services, from systemctl list-unit-files
    STATE            UNIT FILES
    enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd bluetooth cron display-manager
                     firewalld getty@ haveged irqbalance iscsi issue-generator kbdsettings kdump kdump-early
                     klog lvm2-monitor nscd nvmefc-boot-connections postfix purge-kernels rollback rsyslog
                     smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime  systemd-remount-fs
    disabled         accounts-daemon appstream-sync-cache autofs autoyast-initscripts blk-availability
                     bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups
                     cups-browsed debug-shell ebtables exchange-bmc-os-info gpm grub2-once haveged-switch-root
                     ipmi ipmievd iscsi-init iscsid iscsiuio issue-add-ssh-keys kexec-load lunmask
                     man-db-create multipathd nfs nfs-blkmap nmb nvmf-autoconnect ostree-remount rdisc rpcbind
                     rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd
                     speech-dispatcherd systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                     systemd-time-wait-sync systemd-timesyncd udisks2 upower
    indirect         wickedd


    -------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
    root=UUID=2b95edbf-a8ba-4a27-b211-c1f03f86601f
    splash=silent
    mitigations=auto
    quiet
    security=apparmor
    crashkernel=321M,high
    crashkernel=72M,low


    -------------------------------------------------------------
14. cpupower frequency-info
    analyzing CPU 0:
      current policy: frequency should be within 800 MHz and 1.90 GHz.
                      The governor "ondemand" may decide which speed to use
                      within this range.
      boost state support:
        Supported: yes
        Active: yes


    -------------------------------------------------------------
15. sysctl
    kernel.numa_balancing           1
    kernel.randomize_va_space       2
    vm.compaction_proactiveness     20
    vm.dirty_background_bytes        0
    vm.dirty_background_ratio       10
    vm.dirty_bytes                   0
    vm.dirty_expire_centisecs     3000
    vm.dirty_ratio                  20
    vm.dirty_writeback_centisecs   500
    vm.dirtytime_expire_seconds  43200
    vm.extfrag_threshold           500
    vm.min_unmapped_ratio            1
    vm.nr_hugepages                  0
    vm.nr_hugepages_mempolicy        0
    vm.nr_overcommit_hugepages       0
    vm.swappiness                   60
    vm.watermark_boost_factor    15000
```

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Supermicro

SuperServer SYS-241H-TNRTTP
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SPECrate®2017_int_base = 1930

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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Mar-2023
Hardware Availability: Mar-2023
Software Availability: Jun-2022

Platform Notes (Continued)

```
   vm.watermark_scale_factor          10
   vm.zone_reclaim_mode                0


   ----------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
   defrag         always defer defer+madvise [madvise] never
   enabled        [always] madvise never
   hpage_pmd_size 2097152
   shmem_enabled   always within_size advise [never] deny force


   ----------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs  60000
   defrag                     1
   max_ptes_none            511
   max_ptes_shared          256
   max_ptes_swap             64
   pages_to_scan           4096
   scan_sleep_millisecs   10000


   ----------------------------------------------------------
18. OS release
   From /etc/*-release /etc/*-version
   os-release SUSE Linux Enterprise Server 15 SP4

   ----------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/nvme0n1p3 xfs   1.1T  5.8G  1.1T   1% /home

   ----------------------------------------------------------
20. /sys/devices/virtual/dmi/id
     Vendor:        Supermicro
     Product:       Super Server
     Product Family: Family


   ----------------------------------------------------------
21. dmidecode
   Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     32x Samsung M321R4GA3BB6-CQKMG 32 GB 2 rank 4800



   ----------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:      American Megatrends International, LLC.
   BIOS Version:     1.2
   BIOS Date:        03/17/2023
   BIOS Revision:    5.29
```

Compiler Version Notes

```
==========================================================================================
C      | 502.gcc_r(peak)
```

(Continued on next page)

Supermicro

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Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C        | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
         | 557.xz_r(base, peak)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C        | 502.gcc_r(peak)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201
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--------------------------------------------------------------------------


==========================================================================
C        | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
         | 557.xz_r(base, peak)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
         | 541.leela_r(base, peak)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------


==========================================================================
Fortran | 548.exchange2_r(base, peak)
--------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

SPEC CPU®2017 Integer Rate Result

Supermicro

SuperServer SYS-241H-TNRTTP
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Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Mar-2023
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Software Availability: Jun-2022

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:
```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Peak Compiler Invocation

C benchmarks:
```
icx
```

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifx
```

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

Supermicro

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Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Mar-2023
Hardware Availability: Mar-2023
Software Availability: Jun-2022

Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-w -std=c11 -m64 -Wl,-z,muldefs`
`-fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX2`(pass 1)
`-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse`
`-funroll-loops -qopt-mem-layout-trans=4`
`-fno-strict-overflow`
`-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin`
`-lqkmalloc`

502.gcc_r: `-m32`
`-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin`
`-std=gnu89 -Wl,-z,muldefs -fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX2`(pass 1)
`-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse`
`-funroll-loops -qopt-mem-layout-trans=4`
`-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc`

505.mcf_r: basepeak = yes

525.x264_r: `-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast`
`-ffast-math -flto -mfpmath=sse -funroll-loops`
`-qopt-mem-layout-trans=4 -fno-alias`
`-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin`
`-lqkmalloc`

557.xz_r: basepeak = yes

C++ benchmarks:

(Continued on next page)

Supermicro

SuperServer SYS-241H-TNRTTP
(X13QEH+ , Intel Xeon Platinum 8490H)

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Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Jun-2022

Peak Optimization Flags (Continued)

520.omnetpp_r: `basepeak = yes`

523.xalancbmk_r: `basepeak = yes`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revC.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revC.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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