



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(3.60 GHz, Intel Xeon Gold 6334)

SPECspeed®2017_int_base = 12.5

SPECspeed®2017_int_peak = 12.7

CPU2017 License: 9016

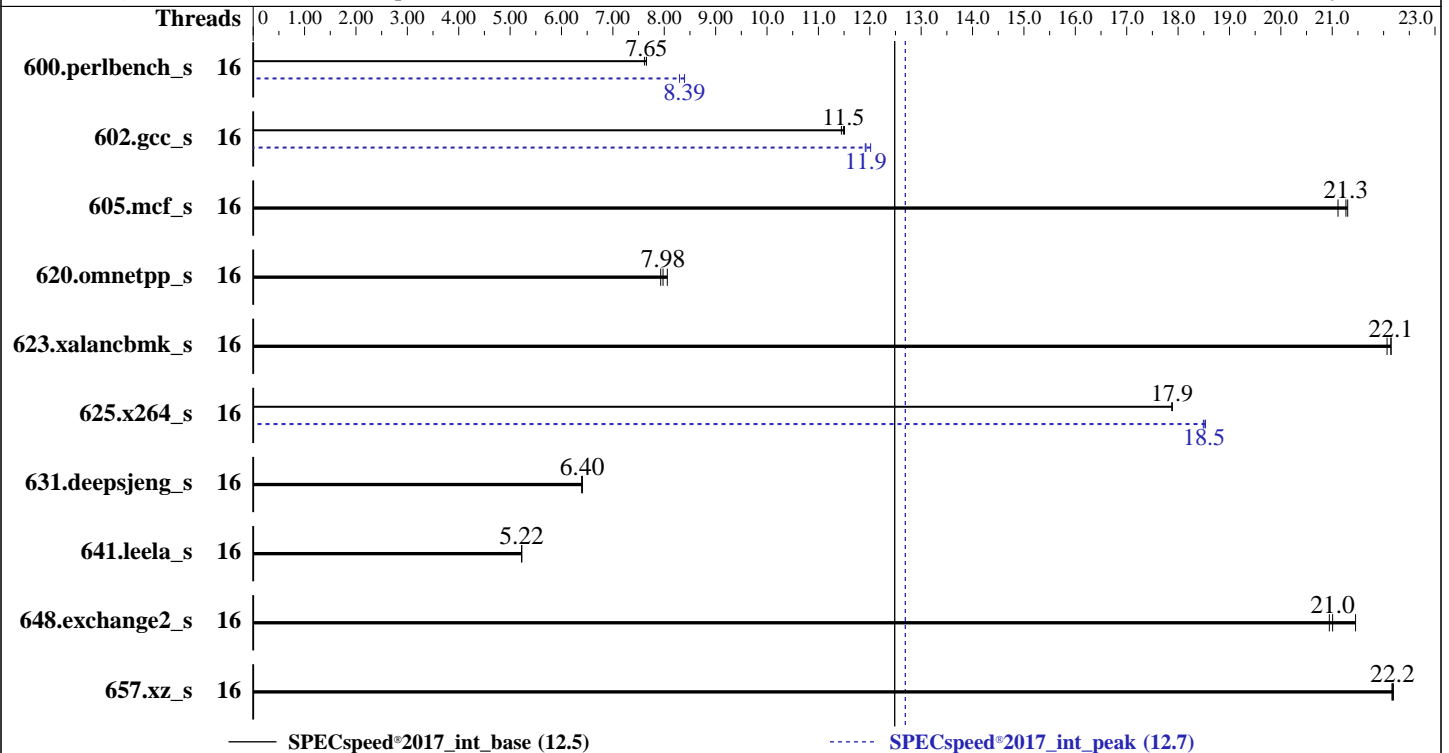
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Oct-2022

Hardware Availability: Apr-2022

Software Availability: May-2022



Hardware

CPU Name: Intel Xeon Gold 6334
 Max MHz: 3700
 Nominal: 3600
 Enabled: 16 cores, 2 chips
 Orderable: 1, 2 chip(s)
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1.25 MB I+D on chip per core
 L3: 18 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)
 Storage: 1 x 1 TB SATA SSD
 Other: None

Software

OS: Red Hat Enterprise Linux release 8.4 (Ootpa)
 4.18.0-305.25.1.el8_4.x86_64
 Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2022.1 of Intel Fortran Compiler
 for Linux;
 Parallel: Yes
 Firmware: Version 0802 released Apr-2022
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance
 at the cost of additional power usage.



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|-----------------|---------|------------|-------------|-------------|-------------|-------------|-------------|---------|-------------|-------------|-------------|-------------|------------|-------------|
| | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 600.perlbench_s | 16 | 232 | 7.65 | <u>232</u> | <u>7.65</u> | 233 | 7.61 | 16 | 211 | 8.39 | 214 | 8.30 | <u>211</u> | <u>8.39</u> |
| 602.gcc_s | 16 | 348 | 11.4 | <u>347</u> | <u>11.5</u> | 346 | 11.5 | 16 | 331 | 12.0 | 334 | 11.9 | <u>334</u> | <u>11.9</u> |
| 605.mcf_s | 16 | 224 | 21.1 | 222 | 21.3 | <u>222</u> | <u>21.3</u> | 16 | 224 | 21.1 | 222 | 21.3 | <u>222</u> | <u>21.3</u> |
| 620.omnetpp_s | 16 | 206 | 7.93 | <u>204</u> | <u>7.98</u> | 202 | 8.06 | 16 | 206 | 7.93 | <u>204</u> | <u>7.98</u> | 202 | 8.06 |
| 623.xalancbmk_s | 16 | 64.2 | 22.1 | <u>64.0</u> | <u>22.1</u> | 64.0 | 22.1 | 16 | 64.2 | 22.1 | <u>64.0</u> | <u>22.1</u> | 64.0 | 22.1 |
| 625.x264_s | 16 | 98.6 | 17.9 | 98.6 | 17.9 | <u>98.6</u> | <u>17.9</u> | 16 | <u>95.3</u> | <u>18.5</u> | 95.2 | 18.5 | 95.4 | 18.5 |
| 631.deepsjeng_s | 16 | 224 | 6.39 | 224 | 6.41 | <u>224</u> | <u>6.40</u> | 16 | 224 | 6.39 | 224 | 6.41 | <u>224</u> | <u>6.40</u> |
| 641.leela_s | 16 | 327 | 5.22 | <u>327</u> | <u>5.22</u> | 326 | 5.23 | 16 | 327 | 5.22 | <u>327</u> | <u>5.22</u> | 326 | 5.23 |
| 648.exchange2_s | 16 | 140 | 20.9 | <u>140</u> | <u>21.0</u> | 137 | 21.5 | 16 | 140 | 20.9 | <u>140</u> | <u>21.0</u> | 137 | 21.5 |
| 657.xz_s | 16 | <u>279</u> | <u>22.2</u> | 279 | 22.2 | 279 | 22.2 | 16 | <u>279</u> | <u>22.2</u> | 279 | 22.2 | 279 | 22.2 |

SPECspeed®2017_int_base = **12.5**

SPECspeed®2017_int_peak = **12.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
OS set to performance mode via cpupower frequency-set -g performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/ic22u1/lib/intel64:/home/ic22u1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default

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General Notes (Continued)

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration:

VT-d = Disabled

Patrol Scrub = Disabled

Hyper-Threading = Disable

Engine Boost = Aggressive

SR-IOV Support = Disabled

BMC Configuration:

Fan mode = Full speed mode

Sysinfo program /home/ic22ul/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Tue Oct 4 21:59:14 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz

2 "physical id"s (chips)

16 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 8

siblings : 8

physical 0: cores 0 1 2 3 4 5 6 7

physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 16

On-line CPU(s) list: 0-15

Thread(s) per core: 1

Core(s) per socket: 8

Socket(s): 2

NUMA node(s): 2

Vendor ID: GenuineIntel

BIOS Vendor ID: Intel

CPU family: 6

Model: 106

Model name: Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz

BIOS Model name: Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz

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Platform Notes (Continued)

```
Stepping: 6
CPU MHz: 2932.727
CPU max MHz: 3700.0000
CPU min MHz: 800.0000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 18432K
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdc m pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a
avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts hwp hwp_act_window
hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig
flush_lld arch_capabilities
```

```
/proc/cpuinfo cache data
cache size : 18432 KB
```

```
From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 515632 MB
node 0 free: 513522 MB
node 1 cpus: 8 9 10 11 12 13 14 15
node 1 size: 516091 MB
node 1 free: 514940 MB
node distances:
node 0 1
0: 10 20
1: 20 10
```

```
From /proc/meminfo
MemTotal: 1056485532 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/sbin/tuned-adm active
Current active profile: throughput-performance
```

```
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
```

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Platform Notes (Continued)

```

ID_LIKE="fedora"
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

```

```

uname -a:
Linux localhost.localdomain 4.18.0-305.25.1.el8_4.x86_64 #1 SMP Mon Oct 18 14:34:11
EDT 2021 x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

| | |
|--|---|
| CVE-2018-12207 (iTLB Multihit): | Not affected |
| CVE-2018-3620 (L1 Terminal Fault): | Not affected |
| Microarchitectural Data Sampling: | Not affected |
| CVE-2017-5754 (Meltdown): | Not affected |
| CVE-2018-3639 (Speculative Store Bypass): | Mitigation: Speculative Store Bypass disabled via prctl and seccomp |
| CVE-2017-5753 (Spectre variant 1): | Mitigation: usercopy/swaps barriers and __user pointer sanitization |
| CVE-2017-5715 (Spectre variant 2): | Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling |
| CVE-2020-0543 (Special Register Buffer Data Sampling): | Not affected |
| CVE-2019-11135 (TSX Asynchronous Abort): | Not affected |

run-level 3 Oct 4 10:48

```

SPEC is set to: /home/ic22u1
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   878G  119G  760G  14% /home

```

```

From /sys/devices/virtual/dmi/id
Vendor:          ASUSTeK COMPUTER INC.
Product:         RS720-E10-RS12
Product Family:  Server
Serial:          012345678901

```

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200

```

```

BIOS:
BIOS Vendor:      American Megatrends Inc.
BIOS Version:     0802
BIOS Date:        04/29/2022
BIOS Revision:    8.2

```

(End of data from sysinfo program)



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Compiler Version Notes

```

-----
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
      | 657.xz_s(base, peak)
-----

```

```

-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----

```

```

-----
C++   | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
      | 641.leela_s(base, peak)
-----

```

```

-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----

```

```

-----
Fortran | 648.exchange2_s(base, peak)
-----

```

```

-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----

```

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

```

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

```



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Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -O3  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP  
-fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib
```

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Peak Optimization Flags (Continued)

600.perlbench_s (continued):

-ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)

-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3

-ffast-math -flto -mfpmath=sse -funroll-loops

-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP

-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3

-ffast-math -flto -mfpmath=sse -funroll-loops

-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP

-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.html>

http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.2022-10-12.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.xml>

http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.2022-10-12.xml



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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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