



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_int\_base = 453

PowerEdge R7525 (AMD EPYC 74F3 24-Core Processor)

SPECrate®2017\_int\_peak = 467

CPU2017 License: 55

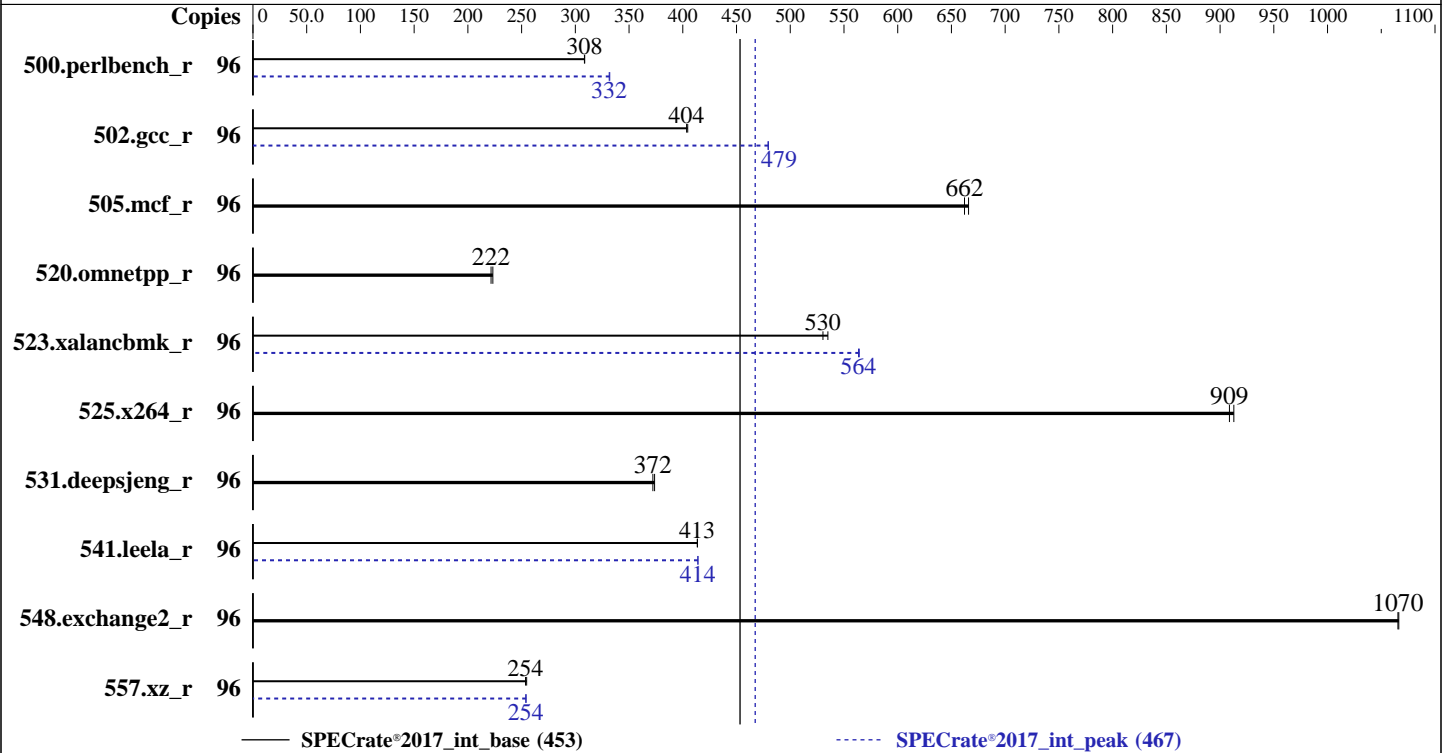
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2021

Hardware Availability: Jun-2021

Software Availability: Mar-2021



### Hardware

CPU Name: AMD EPYC 74F3  
 Max MHz: 4000  
 Nominal: 3200  
 Enabled: 48 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 32 MB shared / 3 cores  
 Other: None  
 Memory: 2 TB (16 x 128 GB 4Rx4 PC4-3200AA-L)  
 Storage: 225 GB on tmpfs  
 Other: None

### Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)  
 4.18.0-240.10.1.el8\_3.x86\_64  
 Compiler: C/C++/Fortran: Version 3.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 2.0.3 released Jan-2021  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.1.0  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	<b>495</b>	<b>308</b>	495	309			96	<b>461</b>	<b>332</b>	460	332		
502.gcc_r	96	<b>337</b>	<b>404</b>	336	405			96	<b>284</b>	<b>479</b>	283	480		
505.mcf_r	96	233	666	<b>234</b>	<b>662</b>			96	233	666	<b>234</b>	<b>662</b>		
520.omnetpp_r	96	<b>568</b>	<b>222</b>	564	223			96	<b>568</b>	<b>222</b>	564	223		
523.xalancbmk_r	96	189	535	<b>191</b>	<b>530</b>			96	180	564	<b>180</b>	<b>564</b>		
525.x264_r	96	184	913	<b>185</b>	<b>909</b>			96	184	913	<b>185</b>	<b>909</b>		
531.deepsjeng_r	96	294	374	<b>296</b>	<b>372</b>			96	294	374	<b>296</b>	<b>372</b>		
541.leela_r	96	<b>385</b>	<b>413</b>	384	414			96	384	414	<b>384</b>	<b>414</b>		
548.exchange2_r	96	236	1070	<b>236</b>	<b>1070</b>			96	236	1070	<b>236</b>	<b>1070</b>		
557.xz_r	96	407	255	<b>409</b>	<b>254</b>			96	408	254	<b>408</b>	<b>254</b>		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu  
ASLR is disabled to reduce run-to-run issues.

dirty\_ratio, swappiness, zone\_reclaim\_mode, drop\_caches and ASLR were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

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## Operating System Notes (Continued)

Transparent huge pages set to 'always' for Int Peak, FP Base/Peak (OS default)  
Transparent huge pages set to 'madvise' for Int Base

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/mnt/ramdisk/cpu2017-1.1.5/amd\_rate\_aocc300\_milan\_A\_lib/64:/mnt/ramdisk  
/cpu2017-1.1.5/amd\_rate\_aocc300\_milan\_A\_lib/32:"  
MALLOC\_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalanbmk\_r peak run:  
MALLOC\_CONF = "thp:never"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 512GiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

NUMA Nodes per Socket : 4

L3 Cache as NUMA Domain : Enabled

Virtualization Technology : Disabled

DRAM Refresh Delay : Performance

System Profile : Custom

CPU Power Management : Maximum Performance

(Continued on next page)



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## Platform Notes (Continued)

Memory Patrol Scrub : Disabled  
PCI ASPM L1 Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5/bin/sysinfo  
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c  
running on localhost.localdomain Mon Mar 22 15:49:13 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : AMD EPYC 74F3 24-Core Processor  
2 "physical id"s (chips)  
96 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 24  
siblings : 48  
physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30  
physical 1: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 96  
On-line CPU(s) list: 0-95  
Thread(s) per core: 2  
Core(s) per socket: 24  
Socket(s): 2  
NUMA node(s): 16  
Vendor ID: AuthenticAMD  
CPU family: 25  
Model: 1  
Model name: AMD EPYC 74F3 24-Core Processor  
Stepping: 1  
CPU MHz: 1836.148  
BogoMIPS: 6388.21  
Virtualization: AMD-V  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 512K  
L3 cache: 32768K  
NUMA node0 CPU(s): 0-2,48-50  
NUMA node1 CPU(s): 3-5,51-53

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## Platform Notes (Continued)

```

NUMA node2 CPU(s): 6-8,54-56
NUMA node3 CPU(s): 9-11,57-59
NUMA node4 CPU(s): 12-14,60-62
NUMA node5 CPU(s): 15-17,63-65
NUMA node6 CPU(s): 18-20,66-68
NUMA node7 CPU(s): 21-23,69-71
NUMA node8 CPU(s): 24-26,72-74
NUMA node9 CPU(s): 27-29,75-77
NUMA node10 CPU(s): 30-32,78-80
NUMA node11 CPU(s): 33-35,81-83
NUMA node12 CPU(s): 36-38,84-86
NUMA node13 CPU(s): 39-41,87-89
NUMA node14 CPU(s): 42-44,90-92
NUMA node15 CPU(s): 45-47,93-95

```

```

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_l3 cdp_l3 invpcid_single hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall
fsgsbase bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap clflushopt clwb
sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local clzero irperf xsaveerptr wbnoinvd amd_ppin arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
v_vmsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

```

```

/proc/cpuinfo cache data
cache size : 512 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 16 nodes (0-15)
node 0 cpus: 0 1 2 48 49 50
node 0 size: 128518 MB
node 0 free: 128427 MB
node 1 cpus: 3 4 5 51 52 53
node 1 size: 128942 MB
node 1 free: 128728 MB
node 2 cpus: 6 7 8 54 55 56
node 2 size: 128966 MB
node 2 free: 128963 MB
node 3 cpus: 9 10 11 57 58 59
node 3 size: 128958 MB
node 3 free: 128947 MB
node 4 cpus: 12 13 14 60 61 62
node 4 size: 128972 MB

```

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## Platform Notes (Continued)

```

node 4 free: 128951 MB
node 5 cpus: 15 16 17 63 64 65
node 5 size: 128938 MB
node 5 free: 128877 MB
node 6 cpus: 18 19 20 66 67 68
node 6 size: 128942 MB
node 6 free: 124016 MB
node 7 cpus: 21 22 23 69 70 71
node 7 size: 128950 MB
node 7 free: 128943 MB
node 8 cpus: 24 25 26 72 73 74
node 8 size: 128954 MB
node 8 free: 128963 MB
node 9 cpus: 27 28 29 75 76 77
node 9 size: 128952 MB
node 9 free: 128961 MB
node 10 cpus: 30 31 32 78 79 80
node 10 size: 128936 MB
node 10 free: 128971 MB
node 11 cpus: 33 34 35 81 82 83
node 11 size: 128938 MB
node 11 free: 128962 MB
node 12 cpus: 36 37 38 84 85 86
node 12 size: 128926 MB
node 12 free: 128972 MB
node 13 cpus: 39 40 41 87 88 89
node 13 size: 128976 MB
node 13 free: 128924 MB
node 14 cpus: 42 43 44 90 91 92
node 14 size: 128964 MB
node 14 free: 128971 MB
node 15 cpus: 45 46 47 93 94 95
node 15 size: 128952 MB
node 15 free: 128960 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11 12 13 14 15
0:  10 11 12 12 12 12 12 12 32 32 32 32 32 32 32 32
1:  11 10 12 12 12 12 12 12 32 32 32 32 32 32 32 32
2:  12 12 10 11 12 12 12 12 32 32 32 32 32 32 32 32
3:  12 12 11 10 12 12 12 12 32 32 32 32 32 32 32 32
4:  12 12 12 12 10 11 12 12 32 32 32 32 32 32 32 32
5:  12 12 12 12 11 10 12 12 32 32 32 32 32 32 32 32
6:  12 12 12 12 12 12 10 11 32 32 32 32 32 32 32 32
7:  12 12 12 12 12 12 11 10 32 32 32 32 32 32 32 32
8:  32 32 32 32 32 32 32 32 10 11 12 12 12 12 12 12
9:  32 32 32 32 32 32 32 32 11 10 12 12 12 12 12 12
10: 32 32 32 32 32 32 32 32 12 12 10 11 12 12 12 12

```

(Continued on next page)



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## Platform Notes (Continued)

11:	32	32	32	32	32	32	32	32	32	12	12	11	10	12	12	12	12
12:	32	32	32	32	32	32	32	32	32	12	12	12	12	10	11	12	12
13:	32	32	32	32	32	32	32	32	32	12	12	12	12	11	10	12	12
14:	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	10	11
15:	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	11	10

From /proc/meminfo

MemTotal: 2113387868 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/sbin/tuned-adm active

Current active profile: throughput-performance

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux"

VERSION="8.3 (Ootpa)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="8.3"

PLATFORM\_ID="platform:el8"

PRETTY\_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"

ANSI\_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.3:ga

uname -a:

Linux localhost.localdomain 4.18.0-240.10.1.el8\_3.x86\_64 #1 SMP Wed Dec 16 03:30:52

EST 2020 x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):

Not affected

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

Not affected

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass):

Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: usercopy/swapgs barriers and \_\_user pointer sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Full AMD retpoline, IBPB: conditional, IBRS\_FW, STIBP: always-on, RSB filling

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## Platform Notes (Continued)

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected  
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Mar 22 10:42 last=5

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	225G	4.8G	221G	3%	/mnt/ramdisk

```

From /sys/devices/virtual/dmi/id
Vendor:          Dell Inc.
Product:         PowerEdge R7525
Product Family: PowerEdge
Serial:          48LN333

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
16x 802C8632802C 72ASS16G72LZ-3G2B3 128 GB 4 rank 3200
16x Not Specified Not Specified

```

```

BIOS:
BIOS Vendor:      Dell Inc.
BIOS Version:     2.0.3
BIOS Date:        01/15/2021
BIOS Revision:    2.0

```

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C          | 502.gcc_r(peak)
-----
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
LLVM Mirror.Version.12.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----
=====
C          | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
          | 525.x264_r(base, peak) 557.xz_r(base, peak)

```

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## Compiler Version Notes (Continued)

```
-----
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----
```

```
====
C      | 502.gcc_r(peak)
-----
```

```
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----
```

```
====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base, peak)
-----
```

```
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----
```

```
====
C++    | 523.xalancbmk_r(peak)
-----
```

```
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----
```

```
====
C++    | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
      | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
-----
```

```
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
-----
```

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## Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

C++ | 523.xalanbmk\_r(peak)

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

C++ | 520.omnetpp\_r(base, peak) 523.xalanbmk\_r(base)  
| 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

Fortran | 548.exchange2\_r(base, peak)

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang



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## Base Portability Flags

```

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502 gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Base Optimization Flags

### C benchmarks:

```

-m64 -Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver3 -fveclib=AMDLIBM -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-lamdlibm -ljemalloc -lflang -lflangrti

```

### C++ benchmarks:

```

-m64 -std=c++98 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver3 -fveclib=AMDLIBM -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-z muldefs -mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm
-ljemalloc -lflang -lflangrti

```

### Fortran benchmarks:

```

-m64 -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split

```

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# SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017\_int\_base = 453

PowerEdge R7525 (AMD EPYC 74F3 24-Core Processor)

SPECrate®2017\_int\_peak = 467

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2021

Hardware Availability: Jun-2021

Software Availability: Mar-2021

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-flto -Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math  
-march=znver3 -fveclib=AMDLIBM -z muldefs -mllvm -unroll-aggressive  
-mllvm -unroll-threshold=500 -lamdlibm -ljemalloc -lflang -lflangrti
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64
```



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## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver3
-fveclib=AMDLIBM -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=false
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

```
502.gcc_r: -m32 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -fgnu89-inline
-ljemalloc
```

505.mcf\_r: basepeak = yes

525.x264\_r: basepeak = yes

```
557.xz_r: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

C++ benchmarks:

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## Peak Optimization Flags (Continued)

520.omnetpp\_r: basepeak = yes

```

523.xalancbmk_r: -m32 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -finline-aggressive
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-licm-vrp -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-ljemalloc

```

531.deepsjeng\_r: basepeak = yes

```

541.leela_r: -m64 -std=c++98
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -finline-aggressive
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-licm-vrp -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -ljemalloc

```

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

```

502.gcc_r: -L/usr/lib -Wno-unused-command-line-argument
-L/spgo/bin/cpu2017v115aocc3/amd_rate_aocc300_milan_A_lib/32

```

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## Peak Other Flags (Continued)

C++ benchmarks (except as noted below):

-Wno-unused-command-line-argument

523.xalancbmk\_r: -L/usr/lib -Wno-unused-command-line-argument

-L/sppo/bin/cpu2017v115aocc3/amd\_rate\_aocc300\_milan\_A\_lib/32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.2.xml>

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