



SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017_int_base = 222

Huawei 9008 V5 (Intel Xeon Platinum 8156)

SPECrate2017_int_peak = 236

CPU2017 License: 3175

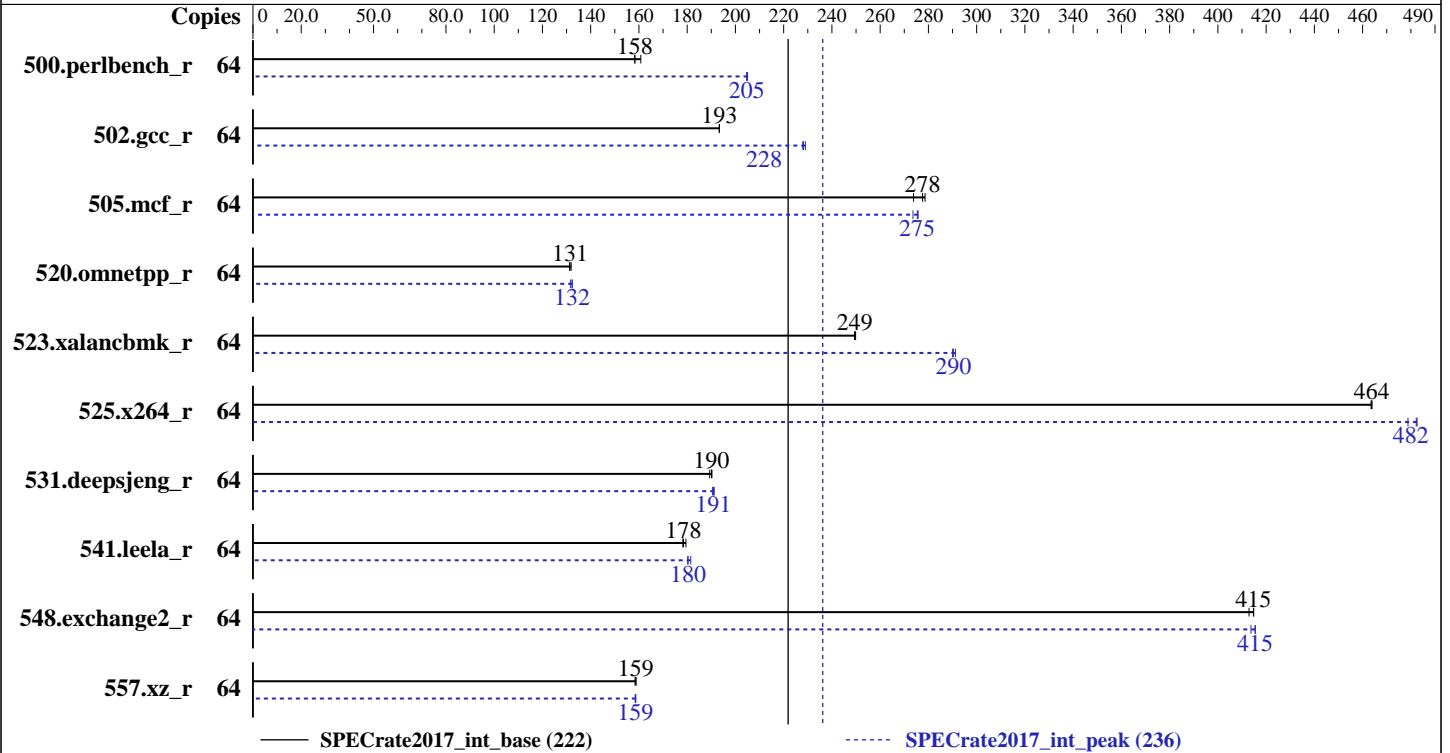
Test Sponsor: Huawei

Tested by: Huawei

Test Date: Jun-2018

Hardware Availability: Jul-2018

Software Availability: Mar-2018



Hardware

CPU Name: Intel Xeon Platinum 8156
 Max MHz.: 3700
 Nominal: 3600
 Enabled: 32 cores, 8 chips, 2 threads/core
 Orderable: 2,4,6,8 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 16.5 MB I+D on chip per chip
 Other: None
 Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
 Storage: 2 x 900 GB SAS HDD 10K RPM, RAID 0
 Other: None

Software

OS: SUSE Linux Enterprise Server for SAP Applications
 12 SP2
 4.4.120-92.70-default
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran
 Compiler for Linux
 Parallel: No
 Firmware: Version 8.92 released May-2018
 File System: btrfs
 System State: Run level 5 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc memory allocator library V5.0.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	64	634	161	643	158	644	158	64	498	205	498	205	497	205
502.gcc_r	64	469	193	469	193	469	193	64	397	228	396	229	398	228
505.mcf_r	64	371	279	378	274	373	278	64	378	274	376	275	375	276
520.omnetpp_r	64	639	131	640	131	637	132	64	634	132	635	132	638	132
523.xalancbmk_r	64	271	249	271	250	271	249	64	232	291	233	290	233	290
525.x264_r	64	242	464	242	464	242	464	64	232	482	232	483	234	479
531.deepsjeng_r	64	387	189	386	190	386	190	64	384	191	385	191	384	191
541.leela_r	64	590	179	594	178	595	178	64	584	181	588	180	588	180
548.exchange2_r	64	406	413	404	415	404	415	64	404	416	404	415	405	414
557.xz_r	64	435	159	436	158	435	159	64	435	159	436	158	436	159

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for
32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4,

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General Notes (Continued)

and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or
<https://github.com/jemalloc/jemalloc/releases>;
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Sub NUMA Cluster (SNC) set to enabled
IMC (Integrated memory controller) Interleaving set to 1 way interleave
Xtended Prediction Table (XPT) Prefetch set to Enable
Memory Patrol Scrub set to Disable
Last Level Cache (LLC) Prefetch set to Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-0mnb Tue Oct 16 07:10:32 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
8 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 1 5 9 13
physical 1: cores 1 5 9 13
physical 2: cores 0 1 12 13
physical 3: cores 1 5 9 13
physical 4: cores 1 5 9 13
physical 5: cores 2 3 4 10
physical 6: cores 2 3 4 10
physical 7: cores 1 5 9 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit

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Platform Notes (Continued)

```

Byte Order:                Little Endian
CPU(s):                    64
On-line CPU(s) list:      0-63
Thread(s) per core:       2
Core(s) per socket:      4
Socket(s):                 8
NUMA node(s):             16
Vendor ID:                 GenuineIntel
CPU family:                6
Model:                     85
Model name:                Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
Stepping:                  4
CPU MHz:                   3601.000
CPU max MHz:               3601.0000
CPU min MHz:               1200.0000
BogoMIPS:                  7199.96
Virtualization:           VT-x
L1d cache:                 32K
L1i cache:                 32K
L2 cache:                  1024K
L3 cache:                  16896K
NUMA node0 CPU(s):        0, 2, 32, 34
NUMA node1 CPU(s):        1, 3, 33, 35
NUMA node2 CPU(s):        4, 6, 36, 38
NUMA node3 CPU(s):        5, 7, 37, 39
NUMA node4 CPU(s):        8, 9, 40, 41
NUMA node5 CPU(s):        10, 11, 42, 43
NUMA node6 CPU(s):        12, 14, 44, 46
NUMA node7 CPU(s):        13, 15, 45, 47
NUMA node8 CPU(s):        16, 18, 48, 50
NUMA node9 CPU(s):        17, 19, 49, 51
NUMA node10 CPU(s):       20, 23, 52, 55
NUMA node11 CPU(s):       21, 22, 53, 54
NUMA node12 CPU(s):       24, 27, 56, 59
NUMA node13 CPU(s):       25, 26, 57, 58
NUMA node14 CPU(s):       28, 30, 60, 62
NUMA node15 CPU(s):       29, 31, 61, 63
Flags:                     fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdc mpcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm
intel_pt rsb_ctxsw spec_ctrl stibp retpoline kaiser tpr_shadow vmmi flexpriority ept
vpid fsgsbase tsc_adjust bml hle avx2 smep bmi2 erms invpcid rtm cqm mpx avx512f
avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec
xgetbv1 cqm_llc cqm_occup_llc

```

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Platform Notes (Continued)

```
/proc/cpuinfo cache data
cache size : 16896 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 16 nodes (0-15)
node 0 cpus: 0 2 32 34
node 0 size: 95025 MB
node 0 free: 94419 MB
node 1 cpus: 1 3 33 35
node 1 size: 96762 MB
node 1 free: 96462 MB
node 2 cpus: 4 6 36 38
node 2 size: 96762 MB
node 2 free: 96518 MB
node 3 cpus: 5 7 37 39
node 3 size: 96762 MB
node 3 free: 96494 MB
node 4 cpus: 8 9 40 41
node 4 size: 96762 MB
node 4 free: 96477 MB
node 5 cpus: 10 11 42 43
node 5 size: 96762 MB
node 5 free: 96575 MB
node 6 cpus: 12 14 44 46
node 6 size: 96762 MB
node 6 free: 96553 MB
node 7 cpus: 13 15 45 47
node 7 size: 96762 MB
node 7 free: 96588 MB
node 8 cpus: 16 18 48 50
node 8 size: 96762 MB
node 8 free: 96564 MB
node 9 cpus: 17 19 49 51
node 9 size: 96762 MB
node 9 free: 96562 MB
node 10 cpus: 20 23 52 55
node 10 size: 96762 MB
node 10 free: 96506 MB
node 11 cpus: 21 22 53 54
node 11 size: 96762 MB
node 11 free: 96480 MB
node 12 cpus: 24 27 56 59
node 12 size: 96762 MB
node 12 free: 96552 MB
node 13 cpus: 25 26 57 58
```

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Platform Notes (Continued)

```

node 13 size: 96762 MB
node 13 free: 96570 MB
node 14 cpus: 28 30 60 62
node 14 size: 96762 MB
node 14 free: 96522 MB
node 15 cpus: 29 31 61 63
node 15 size: 96605 MB
node 15 free: 96382 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11 12 13 14 15
0:  10 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
1:  20 10 20 20 20 20 20 20 20 20 20 20 20 20 20 20
2:  20 20 10 20 20 20 20 20 20 20 20 20 20 20 20 20
3:  20 20 20 10 20 20 20 20 20 20 20 20 20 20 20 20
4:  20 20 20 20 10 20 20 20 20 20 20 20 20 20 20 20
5:  20 20 20 20 20 10 20 20 20 20 20 20 20 20 20 20
6:  20 20 20 20 20 20 10 20 20 20 20 20 20 20 20 20
7:  20 20 20 20 20 20 20 10 20 20 20 20 20 20 20 20
8:  20 20 20 20 20 20 20 20 10 20 20 20 20 20 20 20
9:  20 20 20 20 20 20 20 20 20 10 20 20 20 20 20 20
10: 20 20 20 20 20 20 20 20 20 20 10 20 20 20 20 20
11: 20 20 20 20 20 20 20 20 20 20 20 10 20 20 20 20
12: 20 20 20 20 20 20 20 20 20 20 20 20 10 20 20 20
13: 20 20 20 20 20 20 20 20 20 20 20 20 20 10 20 20
14: 20 20 20 20 20 20 20 20 20 20 20 20 20 20 10 20
15: 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 10

```

```

From /proc/meminfo
MemTotal:      1583410940 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"

```

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Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-0mnb 4.4.120-92.70-default #1 SMP Wed Mar 14 15:59:43 UTC 2018 (52a83de)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Oct 15 23:40
```

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        btrfs 1.5T   23G  1.5T   2% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS INSYDE Corp. 8.92 05/02/2018
Memory:
48x NO DIMM NO DIMM
48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666
```

(End of data from sysinfo program)

Compiler Version Notes

```
=====  
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
525.x264_r(base, peak) 557.xz_r(base, peak)  
-----
```

```
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----
```

```
=====  
CC 500.perlbench_r(peak) 502.gcc_r(peak)  
-----
```

```
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----
```

```
=====  
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
541.leela_r(base)  
-----
```

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Compiler Version Notes (Continued)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
541.leela_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
FC 548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502 gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64



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Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-gopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-gopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-gopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

Peak Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Fortran benchmarks:

```
ifort
```

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64
```

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Peak Portability Flags (Continued)

```
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

```
505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
557.xz_r: Same as 505.mcf_r
```

C++ benchmarks:

```
520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

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Peak Optimization Flags (Continued)

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Peak Other Flags

C benchmarks (except as noted below):

```
-m64 -std=c11
```

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):

```
-m64
```

523.xalancbmk_r: -m32

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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