



# SPEC® CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen10

(2.60 GHz, Intel Xeon Gold 6132)

**SPECfp®\_rate2006 = Not Run**

**SPECfp\_rate\_base2006 = 1180**

**CPU2006 license:** 3

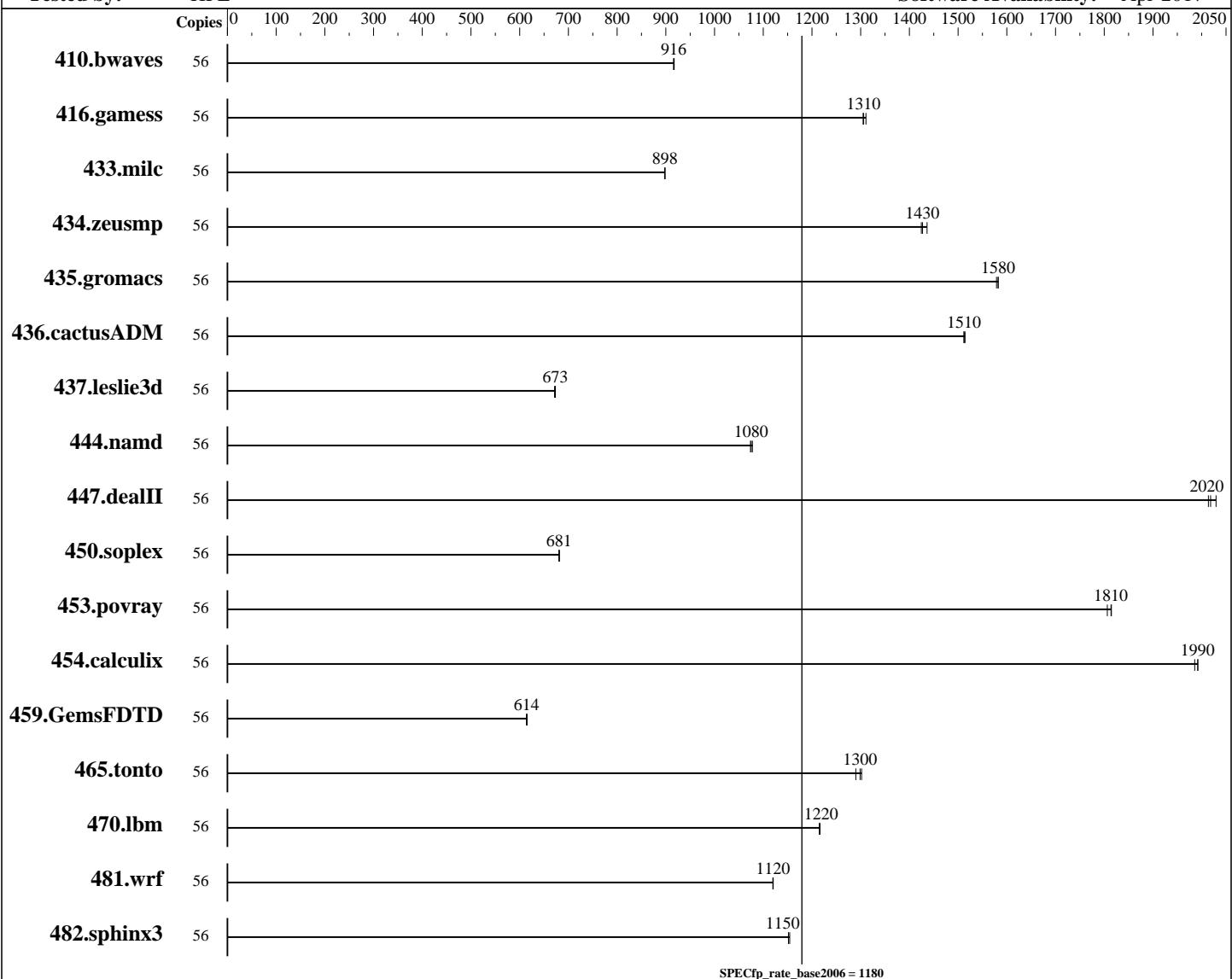
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Dec-2017

**Hardware Availability:** Nov-2017

**Software Availability:** Apr-2017



## Hardware

CPU Name: Intel Xeon Gold 6132  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chip(s)  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP3  
 Compiler: Kernel 4.4.73-5-default  
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 19.25 MB I+D on chip per chip  
Other Cache: None  
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)  
Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: Not Applicable  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	830	917	831	916	<b>831</b>	<b>916</b>							
416.gamess	56	836	1310	<b>839</b>	<b>1310</b>	840	1300							
433.milc	56	573	898	572	898	<b>572</b>	<b>898</b>							
434.zeusmp	56	355	1440	358	1420	<b>357</b>	<b>1430</b>							
435.gromacs	56	<b>253</b>	<b>1580</b>	253	1580	253	1580							
436.cactusADM	56	442	1510	443	1510	<b>442</b>	<b>1510</b>							
437.leslie3d	56	<b>782</b>	<b>673</b>	782	673	784	672							
444.namd	56	<b>418</b>	<b>1080</b>	417	1080	418	1070							
447.dealII	56	316	2030	318	2010	<b>317</b>	<b>2020</b>							
450.soplex	56	686	681	<b>686</b>	<b>681</b>	685	682							
453.povray	56	164	1810	165	1810	<b>164</b>	<b>1810</b>							
454.calculix	56	232	1990	233	1990	<b>232</b>	<b>1990</b>							
459.GemsFDTD	56	966	615	<b>967</b>	<b>614</b>	968	614							
465.tonto	56	427	1290	<b>424</b>	<b>1300</b>	423	1300							
470.lbm	56	<b>633</b>	<b>1220</b>	632	1220	633	1220							
481.wrf	56	558	1120	559	1120	<b>558</b>	<b>1120</b>							
482.sphinx3	56	946	1150	948	1150	<b>947</b>	<b>1150</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

irqbalance disabled with "systemctl stop irqbalance"

tuned profile set with "tuned-adm profile throughput-performance"

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## Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty\_ratio"  
Numa balancing was disabled using "echo 0 > /proc/sys/kernel numa\_balancing"

## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Workload Profile set to General Throughput Compute  
Minimum Processor Idle Power Core C-State set to C1E State  
Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on bl460c16suse Tue Dec 19 16:43:05 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6132 CPU @ 2.60GHz
        2 "physical id"s (chips)
        56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 14
        siblings : 28
        physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
        physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
        cache size : 19712 KB
```

```
From /proc/meminfo
MemTotal:      197745424 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 3
        # This file is deprecated and will be removed in a future service pack or
        release.
        # Please check /etc/os-release for details about this release.
os-release:
        NAME="SLES"
        VERSION="12-SP3"
        VERSION_ID="12.3"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
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```



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## Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
Linux bl460c16suse 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017
(b7ce4e4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 19 16:42
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda4        xfs   852G  231G  622G  28% /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS HPE I41 11/14/2017
```

Memory:

```
4x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the

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## General Notes (Continued)

SPEC OSG Policy document, <http://www.spec.org/osg/policy.htm>.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

## Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
 -qopt-mem-layout-trans=3`

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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