



SPEC® CINT2006 Result

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Sugon

SPECint®_rate2006 = 997

Sugon A320-G30 (AMD EPYC 7401P)

SPECint_rate_base2006 = 899

CPU2006 license: 9046

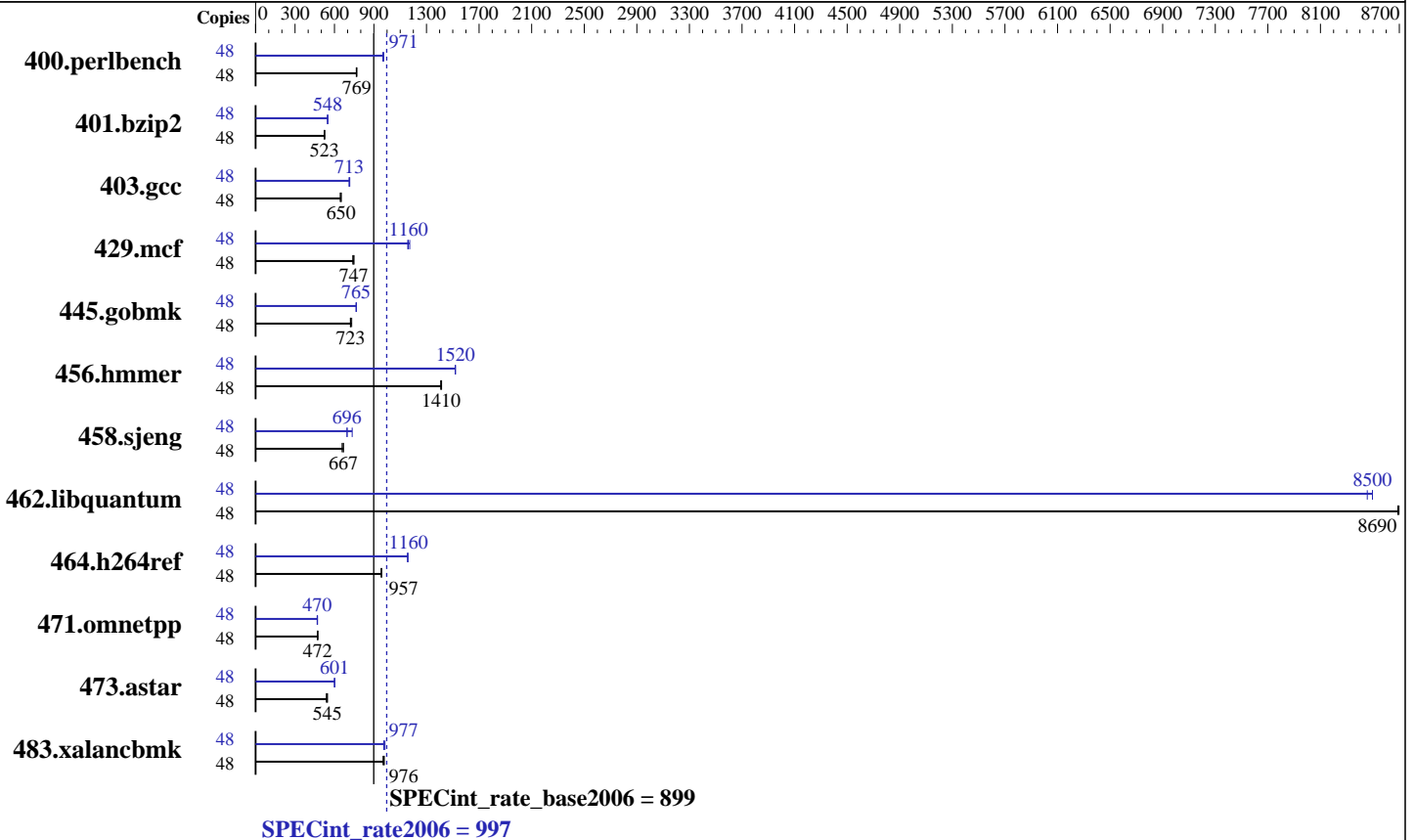
Test sponsor: Sugon

Tested by: Sugon

Test date: Dec-2017

Hardware Availability: Dec-2017

Software Availability: Oct-2017



Hardware

CPU Name: AMD EPYC 7401P
 CPU Characteristics: AMD Turbo CORE technology up to 3.00 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 24 cores, 1 chip, 24 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 64 KB I + 32 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 64 MB I+D on chip per chip, 8 MB shared / 3 cores
 Other Cache: None
 Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2667V-R, running at 2400)
 Disk Subsystem: 1 x 800 GB SATA, SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server 7.4
 Kernel 3.10.0-693.2.2
 Compiler: C/C++: Version 4.5.2.1 of x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (Multi User)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 10.0 32-bit Library for Linux



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	609	770	612	766	610	769	48	481	974	484	968	483	971
401.bzip2	48	885	523	885	523	880	526	48	846	547	844	549	845	548
403.gcc	48	594	650	594	651	600	644	48	543	712	542	713	540	716
429.mcf	48	586	748	592	739	586	747	48	373	1170	377	1160	378	1160
445.gobmk	48	689	731	697	723	698	721	48	656	767	658	765	659	764
456.hammer	48	317	1410	318	1410	317	1410	48	295	1520	295	1520	295	1520
458.sjeng	48	871	667	870	668	883	658	48	790	735	835	696	835	696
462.libquantum	48	114	8690	114	8700	114	8690	48	118	8460	117	8500	117	8500
464.h264ref	48	1110	957	1111	956	1109	958	48	918	1160	917	1160	918	1160
471.omnetpp	48	638	470	636	472	632	474	48	639	470	641	468	637	471
473.astar	48	618	545	626	538	617	546	48	561	601	561	600	561	601
483.xalancbmk	48	339	978	342	969	339	976	48	337	984	339	977	340	975

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

Transparent huge pages were enabled for this run (OS default)

Set vm/nr_hugepages=43008 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

Platform Notes

BIOS settings:
Determinism Slider = Power
cTDP Control = Manual

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Platform Notes (Continued)

cTDP = 200

General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "896"

LD_LIBRARY_PATH = "/home/cpu2006/amd1603-rate-libs-revB/32:/home/cpu2006/amd1603-rate-libs-revB/64"

The binaries were built with the AMD supported x86 Open64 Compiler Suite, which is only available from AMD at

<http://developer.amd.com/tools-and-sdks/cpu-development/x86-open64-compiler-suite/>

Binaries were compiled on a system with 2 x AMD Opteron 6378 chips + 128 GB Memory using RHEL 6.3

Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-Ofast -CG:local_sched_alg=1 -INLINE:aggressive=ON -IPA:plimit=8000
-IPA:small_pu=100 -HP:bd=2m:heap=2m -mso -LNO:prefetch=2
-march=bdver1 -mno-fma4 -mno-xop -mno-tbm

C++ benchmarks:

-Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on -D__OPEN64_FAST_SET
-march=bdver1 -mno-fma4 -mno-xop -mno-tbm
-L/root/work/libraries/SmartHeap-10/lib -lsmartheap



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Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:opt=0 -IPA:plimit=20000
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-WOPT:sib=on -CG:local_sched_alg=1 -CG:unroll_fb_req=on
-CG:movext_icmp=off -HP:bd=2m:heap=2m -march=bdver1
-mno-fma4 -GRA:aggr_loop_splitting=off
-GRA:loop_splitting=off

401.bzip2: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:pf2=0 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m
-march=bdver2 -WB, -mno-fma4 -mno-tbm -mno-xop

403.gcc: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:trip_count=256 -CG:cmp_peep=on -CG:pre_minreg_level=2
-m32 -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on -march=bdver2 -mno-fma4 -WB, -mno-tbm
-mno-xop

429.mcf: -O3 -OPT:unroll_times_max=5 -ipa -INLINE:aggressive=on
-CG:gcm=off -CG:dsched=on -GRA:prioritize_by_density=on
-m32 -HP:bdt=2m:heap=2m -mso -march=bdver1 -mno-fma4

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Peak Optimization Flags (Continued)

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
 -OPT:unroll_size=256 -OPT:unroll_times_max=8
 -OPT:keep_ext=on -IPA:plimit=750 -IPA:min_hotness=300
 -IPA:pu_reorder=1 -LNO:ignore_feedback=off -WOPT:if_conv=2
 -HP:bd=2m:heap=2m -march=bdver1 -mno-fma4

456.hmmcr: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
 -LNO:prefetch=2 -OPT:alias=disjoint
 -OPT:unroll_times_max=16 -OPT:unroll_size=512
 -OPT:unroll_level=2 -OPT:keep_ext=on -CG:cflow=0
 -CG:cmp_peep=on -CG:pre_local_sched=off -HP:bd=2m:heap=2m
 -CG:p2align=0 -CG:load_exe=3 -CG:dsched=on -march=bdver1
 -mno-fma4

458.sjeng: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
 -CG:ptr_load_use=0 -CG:divrem_opt=on -CG:movext_icmp=off
 -CG:locs_best=on -LNO:full_unroll=10 -IPA:pu_reorder=2
 -HP:heap=2m:bd=2m -WOPT:sib=on -march=bdver1 -mno-fma4

462.libquantum: -Ofast -mso -OPT:unroll_size=512 -OPT:unroll_times_max=16
 -LNO:prefetch=2 -LNO:prefetch_ahead=4 -LNO:pf2=0
 -CG:local_sched_alg=1 -CG:p2align=0 -INLINE:aggressive=ON
 -IPA:plimit=15000 -IPA:small_pu=100
 -HP:bd=2m:heap=2m,limit=300 -march=bdver2 -mno-fma4

464.h264ref: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
 -OPT:unroll_size=256 -OPT:unroll_times_max=2
 -IPA:plimit=20000 -OPT:alias=disjoint -CG:ptr_load_use=0
 -CG:local_sched_alg=1 -HP:bd=2m:heap=2m -march=bdver1
 -mno-fma4

C++ benchmarks:

471.omnetpp: -Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on
 -WOPT:sib=on -D__OPEN64_FAST_SET -march=bdver2 -mno-fma4
 -L/root/work/libraries/SmartHeap-10/lib -lsmarheap

473.astar: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
 -WOPT:if_conv=0 -WOPT:sib=on -CG:divrem_opt=on
 -CG:p2align=1 -CG:dsched=on -GRA:optimize_boundary=on
 -OPT:alias=disjoint -INLINE:aggressive=on
 -IPA:small_pu=3000 -IPA:plimit=3000 -HP:bd=2m:heap=2m
 -march=bdver1 -mno-fma4

483.xalancbmk: -Ofast -LNO:prefetch=2 -OPT:unroll_size=512
 -OPT:unroll_times_max=8 -D__OPEN64_FAST_SET
 -INLINE:aggressive=on -m32 -CG:cmp_peep=on
 -CG:local_sched=off -CG:p2align=1 -GRA:unspill=on
 -TENV:frame_pointer=off -fno-emit-exceptions -march=bdver2
 -mno-fma4
 -L/root/work/libraries/SmartHeap-10/lib -lsmarheap



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.html>

<http://www.spec.org/cpu2006/flags/Sugon-Naples-Platform-Settings-revC-I.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Naples-Platform-Settings-revC-I.xml>

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For other inquiries, please contact webmaster@spec.org.

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