



# SPEC<sup>®</sup> CFP2006 Result

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## Cisco Systems

Cisco UCS B200 M5 (Intel Xeon Platinum 8153, 2.00GHz)

SPECfp<sup>®</sup>2006 = **Not Run**

SPECfp\_base2006 = **121**

CPU2006 license: 9019

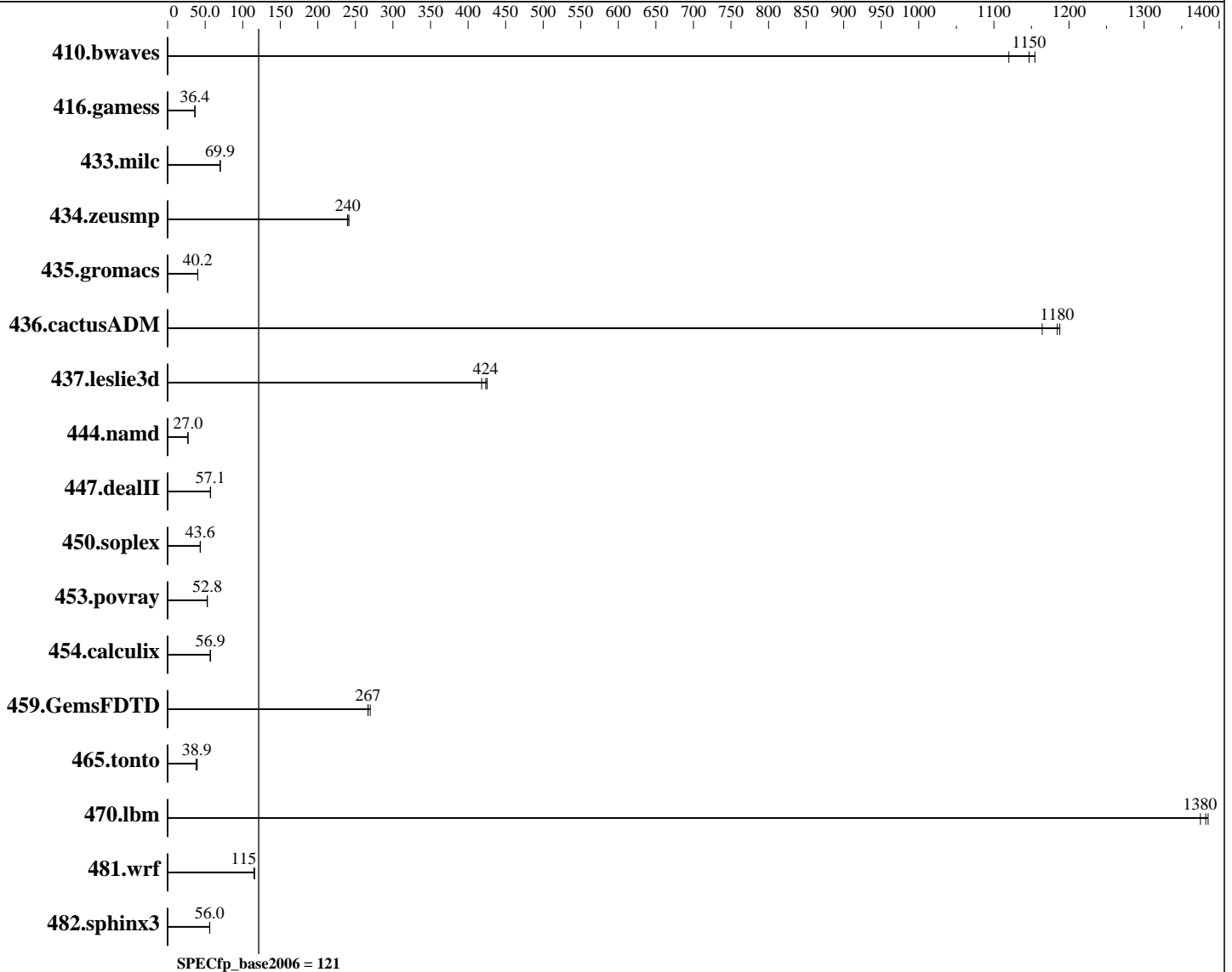
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Aug-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Platinum 8153  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64) 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 22 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 1 x 2 TB SAS 7.5K RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	11.8	1150	<b><u>11.8</u></b>	<b><u>1150</u></b>	12.1	1120						
416.gamess	538	36.4	538	36.4	<b><u>538</u></b>	<b><u>36.4</u></b>						
433.milc	131	69.9	131	70.1	<b><u>131</u></b>	<b><u>69.9</u></b>						
434.zeusmp	38.0	239	37.7	241	<b><u>37.9</u></b>	<b><u>240</u></b>						
435.gromacs	178	40.2	<b><u>178</u></b>	<b><u>40.2</u></b>	178	40.2						
436.cactusADM	10.3	1160	10.1	1190	<b><u>10.1</u></b>	<b><u>1180</u></b>						
437.leslie3d	22.1	425	22.5	418	<b><u>22.2</u></b>	<b><u>424</u></b>						
444.namd	<b><u>297</u></b>	<b><u>27.0</u></b>	297	27.0	297	27.0						
447.dealII	200	57.3	<b><u>200</u></b>	<b><u>57.1</u></b>	201	57.0						
450.soplex	193	43.3	<b><u>191</u></b>	<b><u>43.6</u></b>	191	43.7						
453.povray	101	52.9	<b><u>101</u></b>	<b><u>52.8</u></b>	101	52.7						
454.calculix	145	56.9	<b><u>145</u></b>	<b><u>56.9</u></b>	145	56.8						
459.GemsFDTD	<b><u>39.8</u></b>	<b><u>267</u></b>	39.3	270	39.8	267						
465.tonto	260	37.9	250	39.4	<b><u>253</u></b>	<b><u>38.9</u></b>						
470.lbm	9.92	1390	<b><u>9.94</u></b>	<b><u>1380</u></b>	9.99	1370						
481.wrf	97.1	115	96.5	116	<b><u>97.0</u></b>	<b><u>115</u></b>						
482.sphinx3	<b><u>348</u></b>	<b><u>56.0</u></b>	347	56.1	348	56.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Intel HyperThreading Technology set to Disabled  
CPU performance set to Enterprise  
Power Performance Tuning set to OS  
SNC set to Disabled  
IMC Interleaving set to Auto  
Patrol Scrub set to Disabled  
Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-oj3w Sun Jan 3 00:59:49 2010

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### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 16
  siblings     : 16
  physical 0   : cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1   : cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size     : 22528 KB

```

From /proc/meminfo

```

MemTotal:      394669064 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

uname -a:

```

Linux linux-oj3w 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 3 00:54

SPEC is set to: /home/cpu2006-1.2

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        xfs   1.9T  11G  1.9T   1% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program  
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## Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017

Memory:

24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"

OMP\_NUM\_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch`

Fortran benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch`

Benchmarks using both Fortran and C:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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