



SPEC[®] CINT2006 Result

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NEC Corporation

SPECint[®]2006 = 47.7

Express5800/T110i (Intel Celeron G3930)

SPECint_base2006 = 46.0

CPU2006 license: 9006

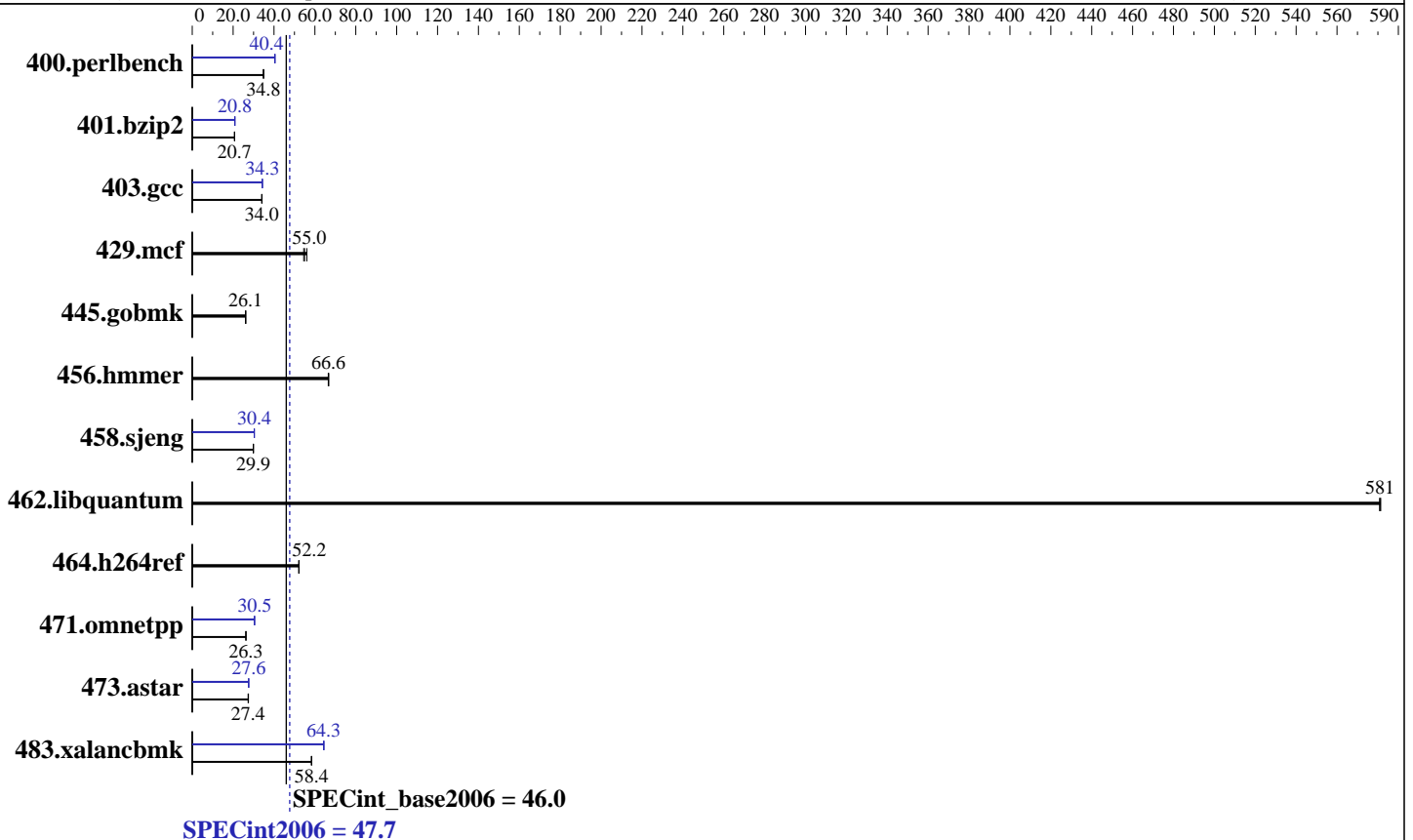
Test date: May-2017

Test sponsor: NEC Corporation

Hardware Availability: Apr-2017

Tested by: NEC Corporation

Software Availability: Jan-2017



Hardware

CPU Name: Intel Celeron G3930
 CPU Characteristics:
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2400T-E, running at 2133 MHz)
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 Kernel 3.10.0-514.6.1.el7.x86_64
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	279	35.0	<u>280</u>	<u>34.8</u>	281	34.8	242	40.4	242	40.5	<u>242</u>	<u>40.4</u>
401.bzip2	466	20.7	<u>466</u>	<u>20.7</u>	467	20.7	462	20.9	<u>463</u>	<u>20.8</u>	463	20.8
403.gcc	236	34.1	<u>237</u>	<u>34.0</u>	237	34.0	235	34.3	<u>235</u>	<u>34.3</u>	235	34.3
429.mcf	<u>166</u>	<u>55.0</u>	163	56.1	167	54.5	<u>166</u>	<u>55.0</u>	163	56.1	167	54.5
445.gobmk	401	26.2	402	26.1	<u>402</u>	<u>26.1</u>	401	26.2	402	26.1	<u>402</u>	<u>26.1</u>
456.hammer	140	66.5	140	66.8	<u>140</u>	<u>66.6</u>	140	66.5	140	66.8	<u>140</u>	<u>66.6</u>
458.sjeng	404	29.9	405	29.9	<u>404</u>	<u>29.9</u>	<u>398</u>	<u>30.4</u>	399	30.3	398	30.4
462.libquantum	35.6	581	<u>35.7</u>	<u>581</u>	35.7	581	35.6	581	<u>35.7</u>	<u>581</u>	35.7	581
464.h264ref	424	52.2	<u>424</u>	<u>52.2</u>	424	52.2	424	52.2	<u>424</u>	<u>52.2</u>	424	52.2
471.omnetpp	<u>238</u>	<u>26.3</u>	238	26.2	238	26.3	<u>205</u>	<u>30.5</u>	205	30.5	205	30.4
473.astar	257	27.4	255	27.5	<u>256</u>	<u>27.4</u>	255	27.6	254	27.6	<u>254</u>	<u>27.6</u>
483.xalancbmk	<u>118</u>	<u>58.4</u>	118	58.4	118	58.3	<u>107</u>	<u>64.3</u>	107	64.4	107	64.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Power Management Policy: Custom

Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -DSPEC_CPU_LP64
 483.xalanbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
 -qopt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -unroll4

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -qopt-ra-region-strategy=block
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110i-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110i-RevA.xml>

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For questions about this result, please contact the tester.
 For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
 Report generated on Wed Jun 14 10:46:09 2017 by SPEC CPU2006 PS/PDF formatter v6932.
 Originally published on 13 June 2017.