



# SPEC<sup>®</sup> CFP2006 Result

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## HITACHI

SPECfp<sup>®</sup>\_rate2006 = 4250

BladeSymphony BS2500 (Intel Xeon E7-8880 v4)

SPECfp\_rate\_base2006 = 4140

CPU2006 license: 35

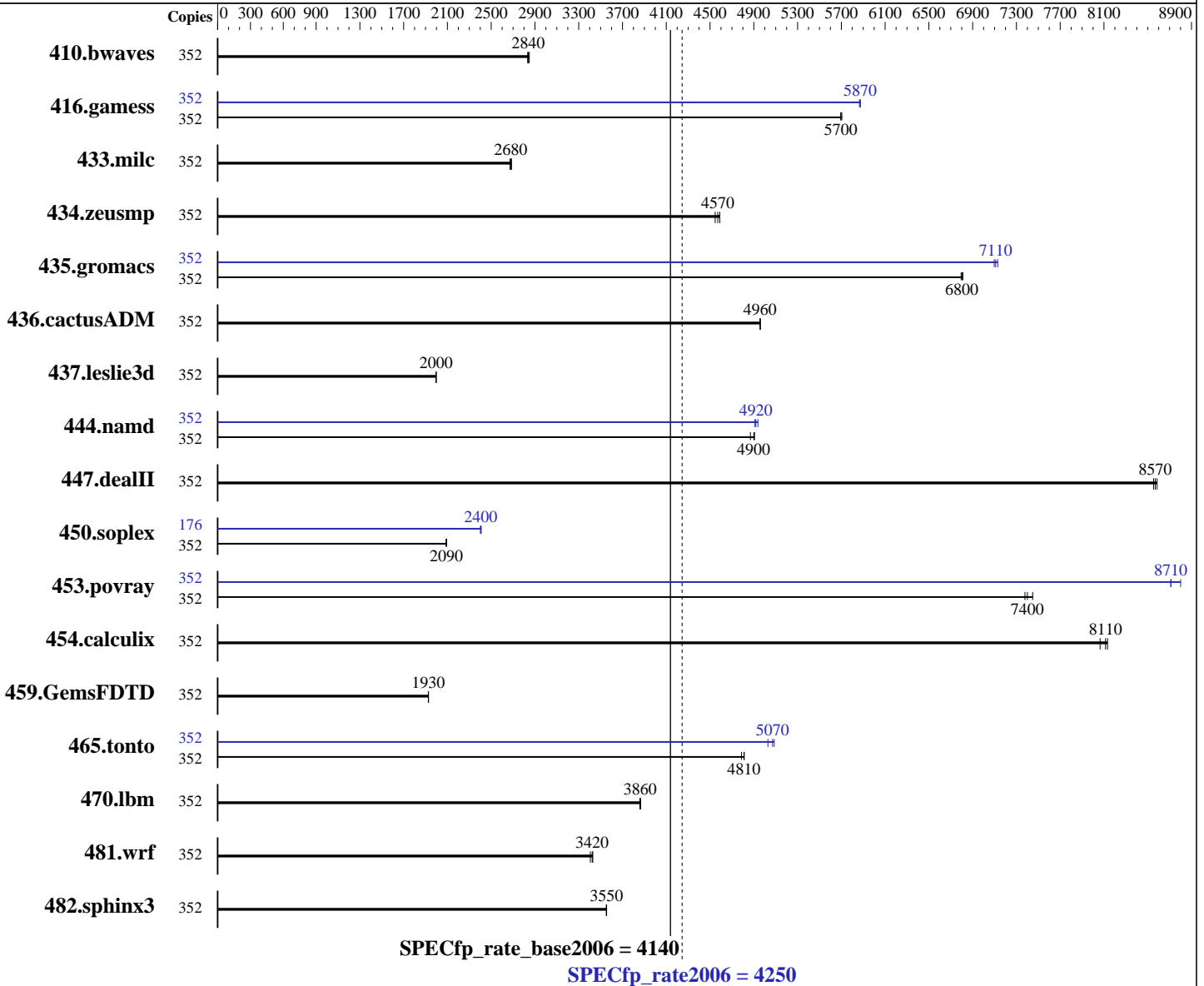
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Sep-2016

Hardware Availability: Sep-2016

Software Availability: Mar-2016



### Hardware

CPU Name: Intel Xeon E7-8880 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 176 cores, 8 chips, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4,8 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: tmpfs

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L3 Cache: 55 MB I+D on chip per chip  
Other Cache: None  
Memory: 2 TB (128 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
Disk Subsystem: 2 x 600 GB SAS, 15000 RPM, RAID1  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: none

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	352	<b>1685</b>	<b>2840</b>	1688	2830	1679	2850	352	<b>1685</b>	<b>2840</b>	1688	2830	1679	2850
416.gamess	352	<b>1210</b>	<b>5700</b>	1210	5690	1208	5700	352	<b>1174</b>	<b>5870</b>	1173	5870	1175	5870
433.milc	352	1203	2690	<b>1206</b>	<b>2680</b>	1209	2670	352	1203	2690	<b>1206</b>	<b>2680</b>	1209	2670
434.zeusmp	352	698	4590	704	4550	<b>701</b>	<b>4570</b>	352	698	4590	704	4550	<b>701</b>	<b>4570</b>
435.gromacs	352	<b>369</b>	<b>6800</b>	370	6790	369	6810	352	354	7100	353	7130	<b>353</b>	<b>7110</b>
436.cactusADM	352	848	4960	848	4960	<b>848</b>	<b>4960</b>	352	848	4960	848	4960	<b>848</b>	<b>4960</b>
437.leslie3d	352	1656	2000	1658	2000	<b>1657</b>	<b>2000</b>	352	1656	2000	1658	2000	<b>1657</b>	<b>2000</b>
444.namd	352	<b>576</b>	<b>4900</b>	575	4910	580	4870	352	572	4940	<b>574</b>	<b>4920</b>	575	4910
447.dealII	352	<b>470</b>	<b>8570</b>	469	8590	471	8550	352	<b>470</b>	<b>8570</b>	469	8590	471	8550
450.soplex	352	1403	2090	1407	2090	<b>1403</b>	<b>2090</b>	176	<b>611</b>	<b>2400</b>	611	2400	609	2410
453.povray	352	254	7380	251	7450	<b>253</b>	<b>7400</b>	352	213	8800	215	8710	<b>215</b>	<b>8710</b>
454.calculix	352	360	8060	<b>358</b>	<b>8110</b>	357	8130	352	360	8060	<b>358</b>	<b>8110</b>	357	8130
459.GemsFDTD	352	1937	1930	<b>1939</b>	<b>1930</b>	1939	1930	352	1937	1930	<b>1939</b>	<b>1930</b>	1939	1930
465.tonto	352	<b>720</b>	<b>4810</b>	720	4810	723	4790	352	681	5090	<b>683</b>	<b>5070</b>	689	5030
470.lbm	352	<b>1253</b>	<b>3860</b>	1251	3870	1253	3860	352	<b>1253</b>	<b>3860</b>	1251	3870	1253	3860
481.wrf	352	<b>1149</b>	<b>3420</b>	1154	3410	1147	3430	352	<b>1149</b>	<b>3420</b>	1154	3410	1147	3430
482.sphinx3	352	1930	3550	1931	3550	<b>1931</b>	<b>3550</b>	352	1930	3550	1931	3550	<b>1931</b>	<b>3550</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Memory Power Management = Automatic

Active Energy Manager = "Capping Disabled"

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CPU2006 license: 35

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Test date: Sep-2016

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Software Availability: Mar-2016

### Platform Notes (Continued)

```
Platform Controlled Type = "Maximum Performance"
C1 Enhanced Mode = Disable
C-States = Disable
ProcessorPerformanceStates = Disable
Sysinfo program /home/shm/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on rhel7264 Fri Sep 30 20:00:58 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8880 v4 @ 2.20GHz
 8 "physical id"s (chips)
352 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 22
  siblings  : 44
 physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
 physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
 physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
 physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
 physical 4: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
 physical 5: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
 physical 6: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
 physical 7: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
cache size : 56320 KB
```

```
From /proc/meminfo
MemTotal:      2112861176 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
```

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### Platform Notes (Continued)

```

ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux rhel7264 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 29 14:21

SPEC is set to: /home/shm/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs           tmpfs    1000G  7.9G  993G   1% /home/shm
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HITACHI 11-04 08/29/2016
Memory:
52x 0x0000 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
6x 0x0003 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0004 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0201 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x5C00 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
64x NO DIMM Unknown
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

```

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/shm/cpu2006/libs/32:/home/shm/cpu2006/libs/64:/home/shm/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
Hitachi Compute Blade 520X and BladeSymphony BS2500 are electronically equivalent.  
The results have been measured on a Hitachi Compute Blade 520X.



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.7.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.7.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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