



SPEC® CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL580 Gen9

(3.20 GHz, Intel Xeon E7-8893 v4)

SPECfp®2006 =

125

SPECfp_base2006 =

120

CPU2006 license: 3

Test sponsor: HPE

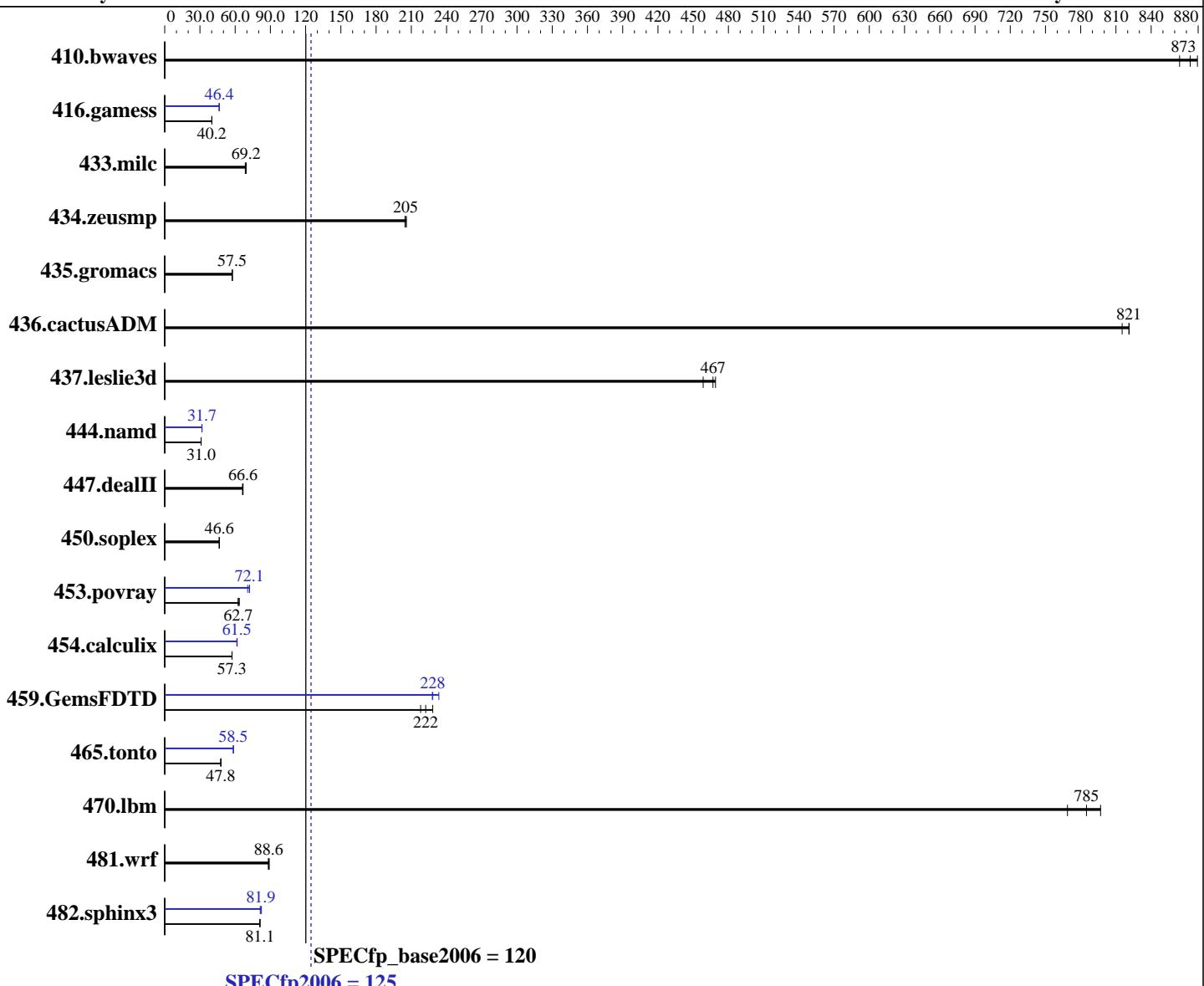
Tested by: HPE

Test date:

Jul-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E7-8893 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP1, Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache:	60 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)
Disk Subsystem:	1 x 800 GB NVMe PCIe SSD, RAID 0
Other Hardware:	DL580 Gen9 NVMe SSD Express Bay Enablement Kit

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	15.7	864	<u>15.6</u>	<u>873</u>	15.5	879	15.7	864	<u>15.6</u>	<u>873</u>	15.5	879
416.gamess	489	40.1	<u>487</u>	<u>40.2</u>	487	40.2	422	46.4	<u>422</u>	<u>46.4</u>	422	46.4
433.milc	132	69.3	134	68.6	<u>133</u>	<u>69.2</u>	132	69.3	134	68.6	<u>133</u>	<u>69.2</u>
434.zeusmp	44.5	205	<u>44.4</u>	<u>205</u>	44.2	206	44.5	205	<u>44.4</u>	<u>205</u>	44.2	206
435.gromacs	124	57.5	124	57.6	<u>124</u>	<u>57.5</u>	124	57.5	124	57.6	<u>124</u>	<u>57.5</u>
436.cactusADM	14.7	815	<u>14.6</u>	<u>821</u>	14.6	821	14.7	815	<u>14.6</u>	<u>821</u>	14.6	821
437.leslie3d	20.0	469	20.5	459	<u>20.1</u>	<u>467</u>	20.0	469	20.5	459	<u>20.1</u>	<u>467</u>
444.namd	259	31.0	259	31.0	<u>259</u>	<u>31.0</u>	253	31.7	253	31.7	<u>253</u>	<u>31.7</u>
447.dealII	173	66.2	<u>172</u>	<u>66.6</u>	171	66.7	173	66.2	<u>172</u>	<u>66.6</u>	171	66.7
450.soplex	179	46.6	180	46.3	<u>179</u>	<u>46.6</u>	179	46.6	180	46.3	<u>179</u>	<u>46.6</u>
453.povray	<u>84.8</u>	<u>62.7</u>	83.7	63.6	85.1	62.5	73.6	72.2	75.3	70.6	<u>73.8</u>	<u>72.1</u>
454.calculix	144	57.4	<u>144</u>	<u>57.3</u>	144	57.3	<u>134</u>	<u>61.5</u>	134	61.4	134	61.7
459.GemsFDTD	48.7	218	46.5	228	<u>47.7</u>	<u>222</u>	45.4	233	46.6	228	<u>46.5</u>	<u>228</u>
465.tonto	206	47.9	<u>206</u>	<u>47.8</u>	206	47.7	<u>168</u>	<u>58.5</u>	168	58.5	168	58.4
470.lbm	17.2	797	17.9	769	<u>17.5</u>	<u>785</u>	17.2	797	17.9	769	<u>17.5</u>	<u>785</u>
481.wrf	<u>126</u>	<u>88.6</u>	127	88.2	125	89.0	<u>126</u>	<u>88.6</u>	127	88.2	125	89.0
482.sphinx3	241	80.9	<u>240</u>	<u>81.1</u>	240	81.1	<u>238</u>	<u>81.9</u>	237	82.3	240	81.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C6 State

Minimum Processor Idle Power Package C-State set to No Package State

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

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Jun-2016

Software Availability:

Dec-2015

Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Intel Hyperthreading set to Disabled

```
Sysinfo program /home/experiment/fp/new/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$
running on linux-vi0i Tue Jul 12 11:11:54 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8893 v4 @ 3.20GHz
        4 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings   : 4
physical 0: cores 12 13 25 26
physical 1: cores 12 13 25 26
physical 2: cores 12 13 25 26
physical 3: cores 12 13 25 26
cache size : 61440 KB
```

```
From /proc/meminfo
MemTotal:      529324312 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-vi0i 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
Continued on next page
```



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Platform Notes (Continued)

run-level 3 Jul 12 10:56

```
SPEC is set to: /home/experiment/fp/new/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/nvme0n1p4  xfs   703G  93G  611G  14% /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U17 05/16/2016

Memory:

```
64x UNKNOWN NOT AVAILABLE
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

OMP_NUM_THREADS = "16"

LD_LIBRARY_PATH = "/home/experiment/fp/new/cpu2006/libs/32:/home/experiment/fp/new/cpu2006/libs/64:/home/experiment/fp/new/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint -auto-ilp32

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-qopt-calloc

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint -auto-ilp32
-fp-model fast=2

```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
 -opt-prefetch -ansi-alias
 -fp-model fast=2
 -qopt-prefetch-issue-excl-hint -funroll-all-loops

-nofor-main

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14
 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
 -inline-level=0 -scalar-rep

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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