



SPEC[®] CFP2006 Result

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Lenovo Group Limited

Lenovo System x3950 X6
(Intel Xeon E7-8860 v4, 2.20 GHz)

SPECfp[®]_rate2006 = 3930

SPECfp_rate_base2006 = 3830

CPU2006 license: 9017

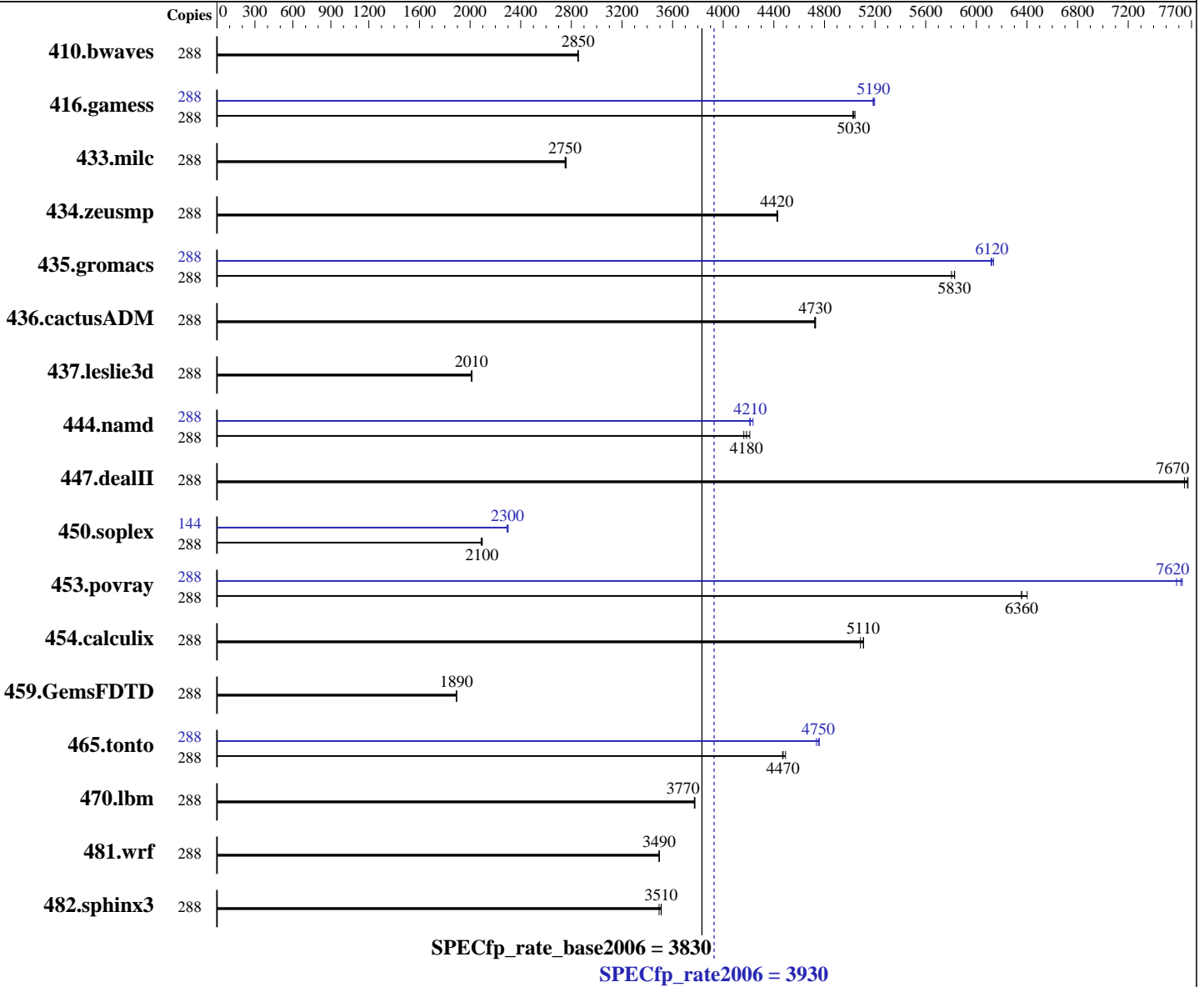
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Aug-2016

Hardware Availability: Jun-2016

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E7-8860 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 144 cores, 8 chips, 18 cores/chip, 2 threads/core
 CPU(s) orderable: 4,8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	288	1373	2850	<u>1371</u>	<u>2850</u>	1371	2860	288	1373	2850	<u>1371</u>	<u>2850</u>	1371	2860
416.gamess	288	1119	5040	<u>1121</u>	<u>5030</u>	1122	5020	288	1088	5180	1085	5200	<u>1087</u>	<u>5190</u>
433.milc	288	958	2760	961	2750	<u>961</u>	<u>2750</u>	288	958	2760	961	2750	<u>961</u>	<u>2750</u>
434.zeusmp	288	<u>592</u>	<u>4420</u>	592	4420	591	4430	288	<u>592</u>	<u>4420</u>	592	4420	591	4430
435.gromacs	288	353	5830	354	5800	<u>353</u>	<u>5830</u>	288	336	6120	<u>336</u>	<u>6120</u>	335	6140
436.cactusADM	288	<u>728</u>	<u>4730</u>	729	4720	728	4730	288	<u>728</u>	<u>4730</u>	729	4720	728	4730
437.leslie3d	288	1343	2020	<u>1346</u>	<u>2010</u>	1346	2010	288	1343	2020	<u>1346</u>	<u>2010</u>	1346	2010
444.namd	288	549	4210	<u>552</u>	<u>4180</u>	555	4160	288	545	4240	<u>548</u>	<u>4210</u>	549	4210
447.dealII	288	429	7670	<u>430</u>	<u>7670</u>	431	7640	288	429	7670	<u>430</u>	<u>7670</u>	431	7640
450.soplex	288	1146	2100	<u>1146</u>	<u>2100</u>	1151	2090	144	522	2300	524	2290	<u>523</u>	<u>2300</u>
453.povray	288	239	6400	<u>241</u>	<u>6360</u>	241	6360	288	<u>201</u>	<u>7620</u>	201	7630	202	7580
454.calculix	288	467	5080	465	5110	<u>465</u>	<u>5110</u>	288	467	5080	465	5110	<u>465</u>	<u>5110</u>
459.GemsFDTD	288	1614	1890	<u>1614</u>	<u>1890</u>	1615	1890	288	1614	1890	<u>1614</u>	<u>1890</u>	1615	1890
465.tonto	288	634	4470	631	4490	<u>633</u>	<u>4470</u>	288	598	4740	595	4760	<u>596</u>	<u>4750</u>
470.lbm	288	<u>1049</u>	<u>3770</u>	1049	3770	1048	3780	288	<u>1049</u>	<u>3770</u>	1049	3770	1048	3780
481.wrf	288	<u>921</u>	<u>3490</u>	921	3490	920	3500	288	<u>921</u>	<u>3490</u>	921	3490	920	3500
482.sphinx3	288	1607	3490	<u>1599</u>	<u>3510</u>	1599	3510	288	1607	3490	<u>1599</u>	<u>3510</u>	1599	3510

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches



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Platform Notes

BIOS Configuration:

Operating Mode set to "Maximum Performance"
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on X3950-01-SLES12SP1 Thu Aug 18 07:34:43 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E7-8860 v4 @ 2.20GHz
 8 "physical id"s (chips)
288 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores      : 18
siblings       : 36
physical 0:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 4:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 5:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 6:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 7:    : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size     : 46080 KB
```

From /proc/meminfo

```
MemTotal:      1058539016 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

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Platform Notes (Continued)

Linux X3950-01-SLES12SP1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 17 18:13

SPEC is set to: /home/cpu2006-1.2-ic16.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	512G	7.0G	505G	2%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[A9E135CUS-3.10]- 06/16/2016

Memory:

128x NO DIMM Unknown

64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

Continued on next page



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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

Continued on next page



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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>



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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Sep 6 16:58:20 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 September 2016.