



SPEC® CINT2006 Result

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Supermicro

Supermicro X11SAT-F motherboard
(X11SAT-F, Intel Core i5-6400T)

SPECint®_rate2006 = 157

SPECint_rate_base2006 = 152

CPU2006 license: 001176

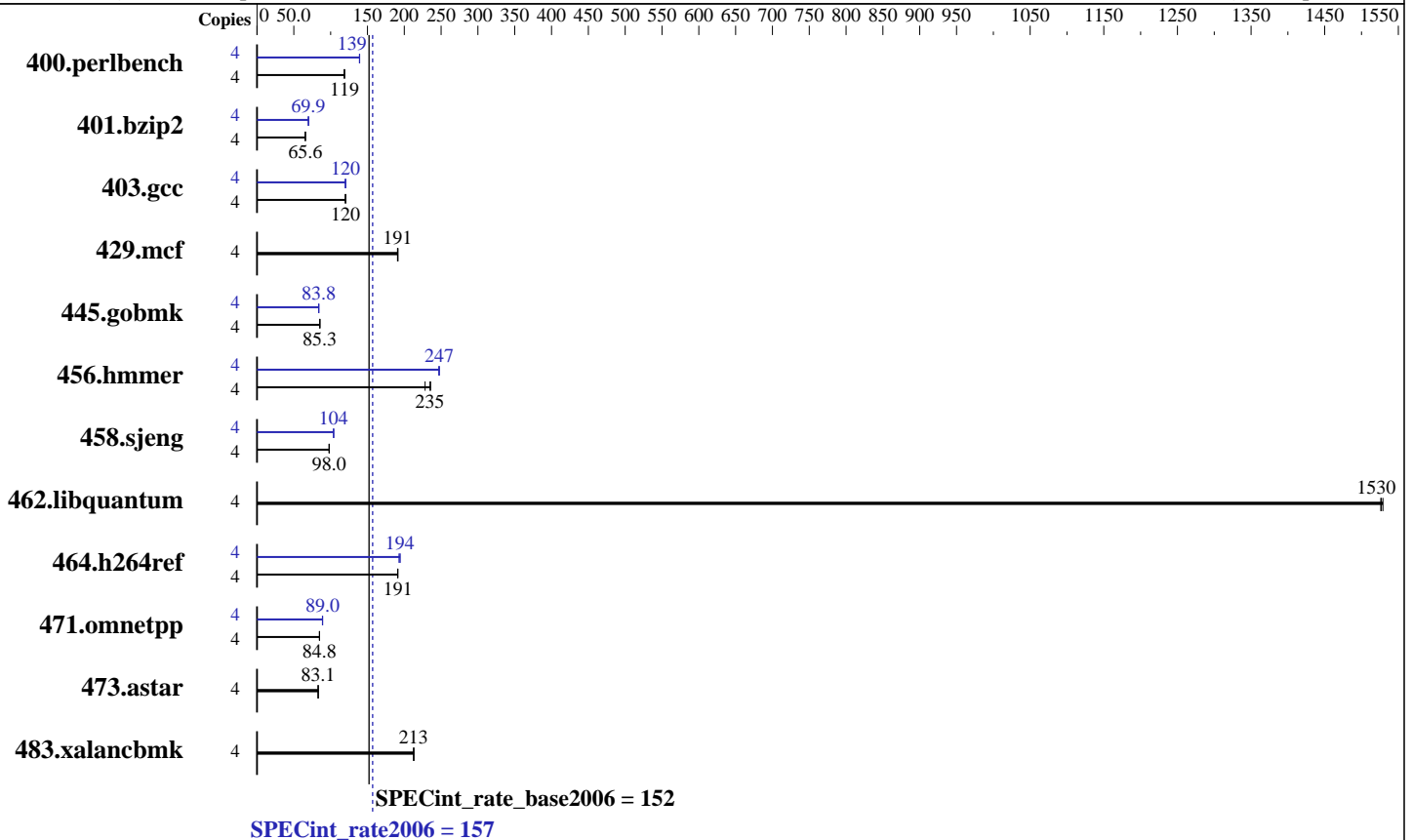
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015



Hardware

CPU Name: Intel Core i5-6400T
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2133P-U)
 Disk Subsystem: 1 x 200 GB SATA III SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	329	119	<u>329</u>	<u>119</u>	330	118	4	280	139	281	139	<u>281</u>	<u>139</u>
401.bzip2	4	<u>588</u>	<u>65.6</u>	587	65.8	589	65.6	4	554	69.7	552	69.9	<u>552</u>	<u>69.9</u>
403.gcc	4	267	121	269	120	<u>268</u>	<u>120</u>	4	269	120	267	121	<u>268</u>	<u>120</u>
429.mcf	4	191	191	191	191	<u>191</u>	<u>191</u>	4	191	191	191	191	<u>191</u>	<u>191</u>
445.gobmk	4	492	85.3	491	85.4	<u>492</u>	<u>85.3</u>	4	501	83.8	499	84.0	<u>501</u>	<u>83.8</u>
456.hammer	4	<u>159</u>	<u>235</u>	158	236	163	228	4	<u>151</u>	<u>247</u>	151	247	151	248
458.sjeng	4	<u>494</u>	<u>98.0</u>	493	98.1	494	98.0	4	465	104	465	104	<u>465</u>	<u>104</u>
462.libquantum	4	54.2	1530	54.3	1530	<u>54.3</u>	<u>1530</u>	4	54.2	1530	54.3	1530	<u>54.3</u>	<u>1530</u>
464.h264ref	4	464	191	463	191	<u>463</u>	<u>191</u>	4	455	194	<u>456</u>	<u>194</u>	459	193
471.omnetpp	4	<u>295</u>	<u>84.8</u>	295	84.8	294	85.0	4	281	88.8	<u>281</u>	<u>89.0</u>	280	89.2
473.astar	4	338	83.0	<u>338</u>	<u>83.1</u>	338	83.2	4	338	83.0	<u>338</u>	<u>83.1</u>	338	83.2
483.xalancbmk	4	<u>130</u>	<u>213</u>	130	213	130	213	4	<u>130</u>	<u>213</u>	130	213	130	213

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

As tested, the system used a Supermicro CSE-732D4-903B chassis. The chassis is configured with a PWS-903-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0124L4 chassis fan.

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1
running on X11SAT-01 Thu Jan 7 12:41:59 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Core(TM) i5-6400T CPU @ 2.20GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
Continued on next page



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Platform Notes (Continued)

```

caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  cache size : 6144 KB

```

```

From /proc/meminfo
MemTotal:      16248396 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

```

```

uname -a:
Linux X11SAT-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 7 01:36

```

SPEC is set to: /usr/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda2        xfs       183G   45G  139G  25% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS American Megatrends Inc. 1.0 12/18/2015
Memory:
  4x Micron 8ATF51264AZ-2G1A2 4 GB 1 rank 2133 MHz

```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/sh -lsmartheap

Base Other Flags

C benchmarks:

Continued on next page



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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

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Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

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For other inquiries, please contact webmaster@spec.org.

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