SPEC® CINT2006 Result

NEC Corporation

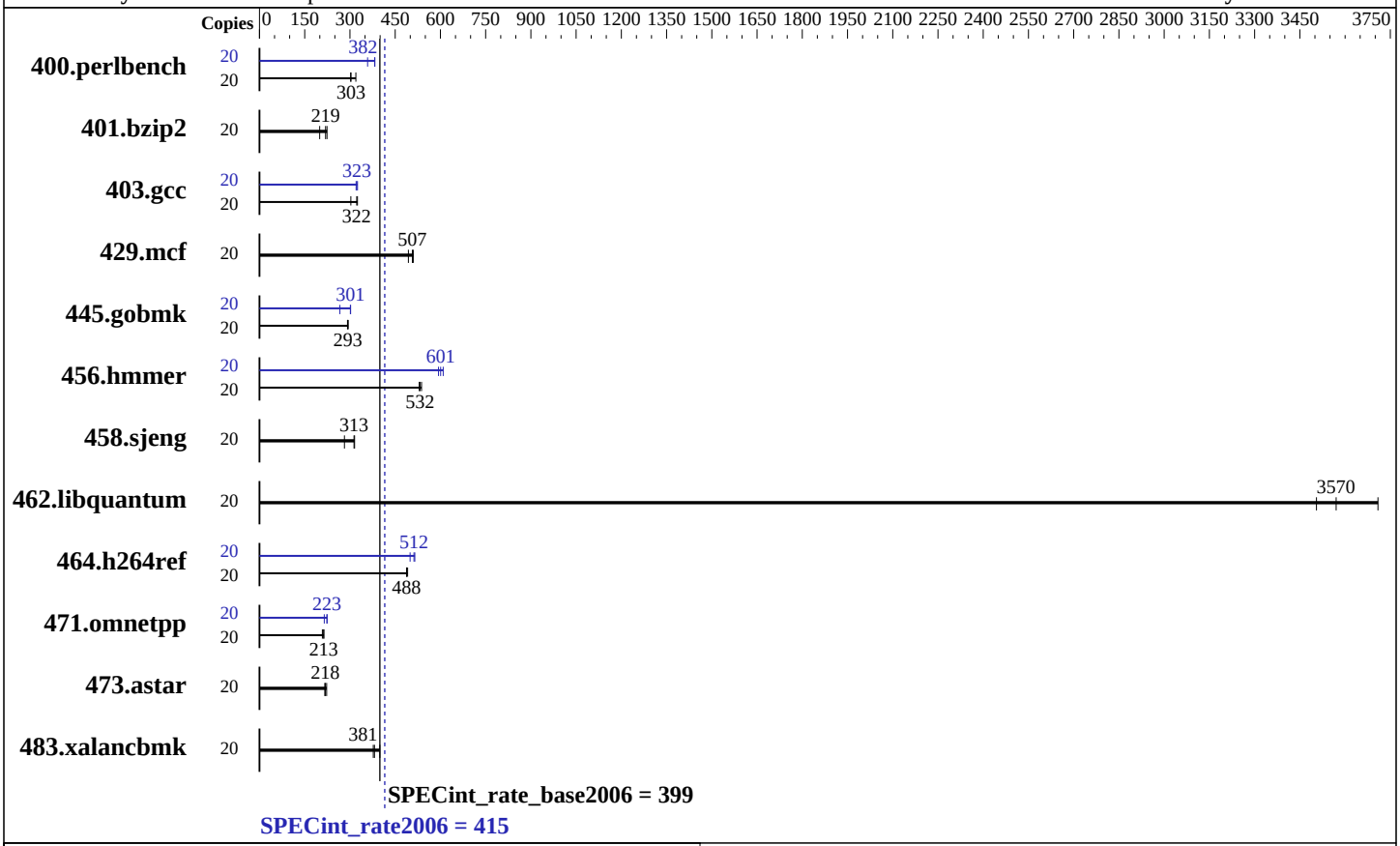
Express5800/B120f (Intel Xeon E5-2660 v3)

SPECint®_rate2006 = 415

SPECint_rate_base2006 = 399

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Dec-2014
Hardware Availability: Apr-2015
Software Availability: Jul-2014



Hardware

CPU Name:	Intel Xeon E5-2660 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	10 cores, 1 chip, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (3 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 300 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) Kernel 2.6.32-431.20.3.el6.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

SPEC CINT2006 Result

NEC Corporation

Express5800/B120f (Intel Xeon E5-2660 v3)

SPECint_rate2006 = 415

SPECint_rate_base2006 = 399

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Dec-2014
Hardware Availability: Apr-2015
Software Availability: Jul-2014

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	20	611	320	**644**	**303**	646	302	20	545	359	511	383	**511**	**382**
401.bzip2	20	**881**	**219**	967	200	862	224	20	**881**	**219**	967	200	862	224
403.gcc	20	531	303	496	325	**499**	**322**	20	495	325	502	320	**499**	**323**
429.mcf	20	369	494	358	510	**360**	**507**	20	369	494	358	510	**360**	**507**
445.gobmk	20	713	294	719	292	**717**	**293**	20	694	302	**696**	**301**	787	267
456.hmmer	20	**351**	**532**	347	537	352	530	20	314	594	306	609	**311**	**601**
458.sjeng	20	**772**	**313**	859	282	767	315	20	**772**	**313**	859	282	767	315
462.libquantum	20	112	3710	**116**	**3570**	118	3510	20	112	3710	**116**	**3570**	118	3510
464.h264ref	20	**907**	**488**	901	491	908	488	20	**864**	**512**	859	515	886	499
471.omnetpp	20	599	209	**588**	**213**	585	214	20	582	215	557	224	**559**	**223**
473.astar	20	630	223	648	217	**644**	**218**	20	630	223	648	217	**644**	**218**
483.xalancbmk	20	**362**	**381**	346	399	366	377	20	**362**	**381**	346	399	366	377

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Processor C6 Report: Enabled
 Energy Performance: Performance
 Patrol Scrub: Disabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:
```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

400.perlbench: `icc -m64`

456.hmmer: `icc -m64`

C++ benchmarks:
```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: basepeak = yes

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120f-RevB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120f-RevB.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 2 13:46:20 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 June 2015.