



# SPEC® CINT2006 Result

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## Lenovo Group Limited

Lenovo Flex System x880 X6  
(Intel Xeon E7-8890 v3, 2.50 GHz)

**SPECint\_rate2006 = 2670**

**SPECint\_rate\_base2006 = 2570**

CPU2006 license: 9017

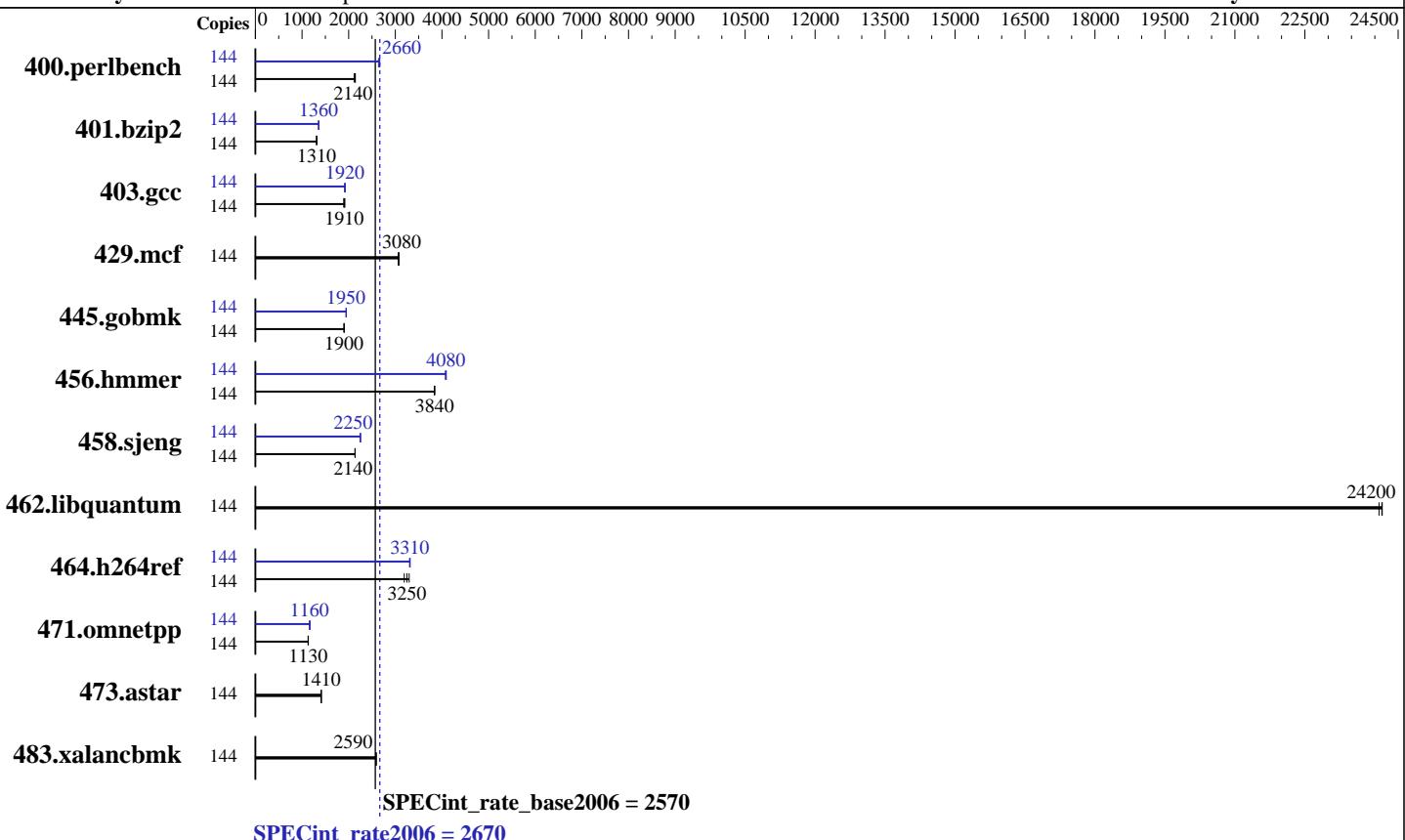
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

**Test date:** May-2015

**Hardware Availability:** May-2015

**Software Availability:** Mar-2015



### Hardware

CPU Name: Intel Xeon E7-8890 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
CPU MHz: 2500  
FPU: Integrated  
CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core  
CPU(s) orderable: 2,4,8 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 45 MB I+D on chip per chip  
Other Cache: None  
Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz)  
Disk Subsystem: 1 x 200 GB SSD  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
Compiler: 3.10.0-229.el7.x86\_64  
Auto Parallel: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	144	656	2140	665	2120	<b>658</b>	<b>2140</b>	144	533	2640	528	2670	<b>528</b>	<b>2660</b>
401.bzip2	144	1055	1320	<b>1059</b>	<b>1310</b>	1060	1310	144	1026	1350	1023	1360	<b>1024</b>	<b>1360</b>
403.gcc	144	<b>608</b>	<b>1910</b>	612	1890	604	1920	144	606	1910	<b>603</b>	<b>1920</b>	602	1930
429.mcf	144	<b>427</b>	<b>3080</b>	429	3060	427	3080	144	<b>427</b>	<b>3080</b>	429	3060	427	3080
445.gobmk	144	793	1900	<b>794</b>	<b>1900</b>	795	1900	144	776	1950	<b>776</b>	<b>1950</b>	776	1950
456.hammer	144	350	3840	349	3850	<b>350</b>	<b>3840</b>	144	330	4070	<b>329</b>	<b>4080</b>	328	4090
458.sjeng	144	815	2140	816	2130	<b>815</b>	<b>2140</b>	144	775	2250	773	2260	<b>774</b>	<b>2250</b>
462.libquantum	144	<b>124</b>	<b>24200</b>	124	24200	124	24100	144	<b>124</b>	<b>24200</b>	124	24200	124	24100
464.h264ref	144	<b>981</b>	<b>3250</b>	998	3190	966	3300	144	963	3310	960	3320	<b>963</b>	<b>3310</b>
471.omnetpp	144	<b>794</b>	<b>1130</b>	796	1130	793	1140	144	<b>774</b>	<b>1160</b>	768	1170	776	1160
473.astar	144	716	1410	715	1410	<b>716</b>	<b>1410</b>	144	716	1410	715	1410	<b>716</b>	<b>1410</b>
483.xalancbmk	144	383	2590	385	2580	<b>384</b>	<b>2590</b>	144	383	2590	385	2580	<b>384</b>	<b>2590</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Custom in BIOS

Cstates disabled in BIOS

Maximum OS Cstate set to zero

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1

running on SPECCpu-Newport Fri May 1 12:34:10 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-8890 v3 @ 2.50GHz

4 "physical id"s (chips)

144 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

Continued on next page



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## Platform Notes (Continued)

```
caution.)  
    cpu cores : 18  
    siblings   : 36  
    physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
    physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
    physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
    physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
    cache size : 46080 KB  
  
From /proc/meminfo  
MemTotal:      1056424224 kB  
HugePages_Total:        0  
Hugepagesize:     2048 kB  
  
From /etc/*release* /etc/*version*  
os-release:  
  NAME="Red Hat Enterprise Linux Server"  
  VERSION="7.1 (Maipo)"  
  ID="rhel"  
  ID_LIKE="fedora"  
  VERSION_ID="7.1"  
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"  
  ANSI_COLOR="0;31"  
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server  
  
uname -a:  
Linux SPECcpu-Newport 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST  
2015 x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 May 1 11:17
```

```
SPEC is set to: /cpu2006.1.2  
Filesystem           Type  Size  Used Avail Use% Mounted on  
/dev/mapper/rhel-root xfs   182G  5.6G  176G   4% /  
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS IBM -[N3E121SUS-1.00]- 03/13/2015  
Memory:  
 30x 0x0000 HMT42GR7AFR4A-PB 16 GB 2 rank 1600 MHz  
 2x 0x0000 MT42GR7AFR4A-PB 16 GB 2 rank 1600 MHz  
 32x NO DIMM Unknown  
 32x Samsung M393B2G70QH0-YK0 16 GB 2 rank 1600 MHz, configured at 1333 MHz
```

(End of data from sysinfo program) Continued on next page



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## Platform Notes (Continued)

Due to a dmidecode issue, some memory speeds are listed at the highest speed the DIMMs are capable of rather than the actual lower run speed.

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hummer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-CC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-CC.xml>



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