# SPEC® CINT2006 Result

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## NEC Corporation

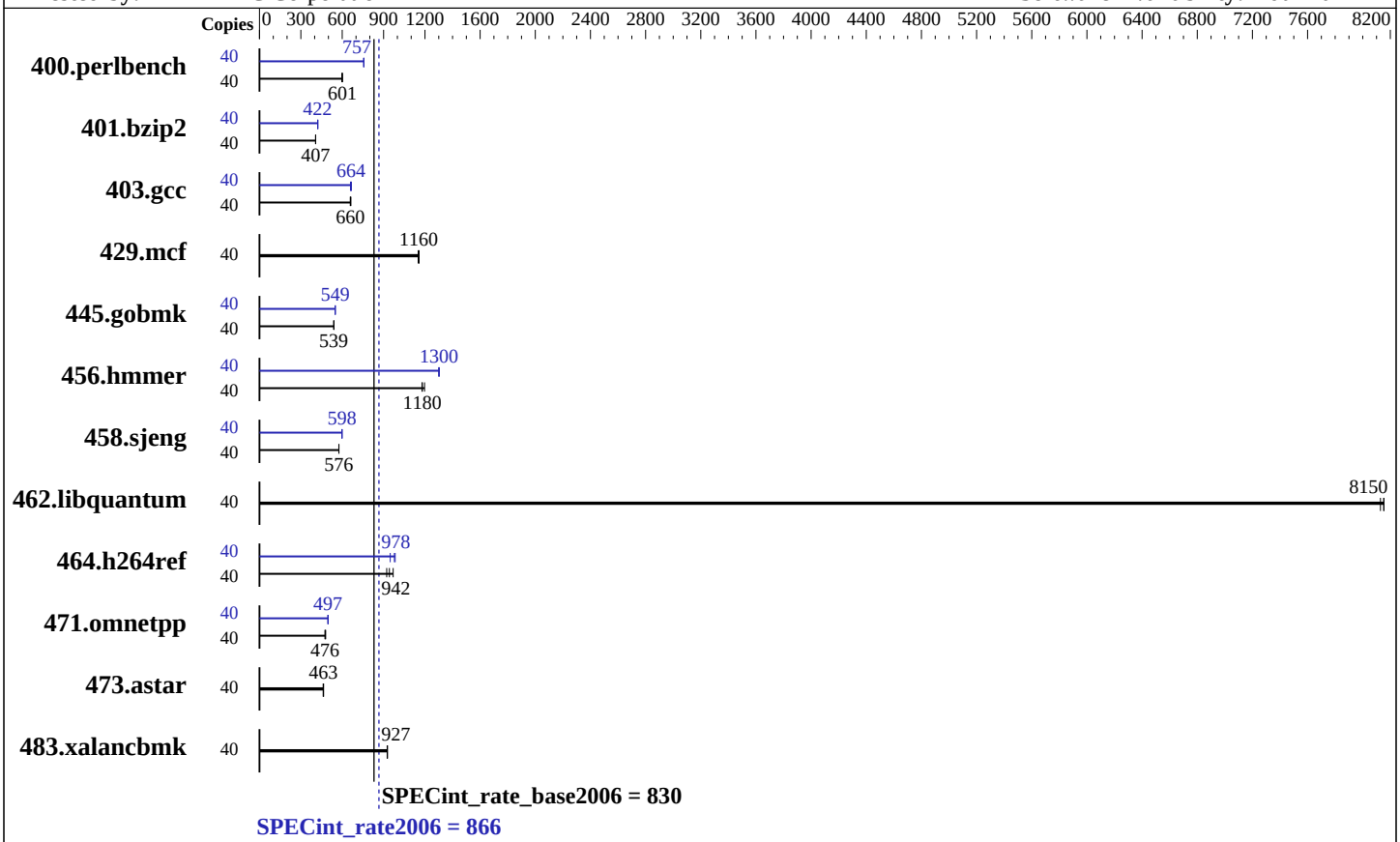
Express5800/E120f-M (Intel Xeon E5-2650 v3)

SPECint®_rate2006 = 866

SPECint_rate_base2006 = 830

**CPU2006 license:** 9006
**Test sponsor:** NEC Corporation
**Tested by:** NEC Corporation

**Test date:** Feb-2015
**Hardware Availability:** Feb-2015
**Software Availability:** Jul-2014




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2650 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.00 GHz |
| CPU MHz: | 2300 |
| FPU: | Integrated |
| CPU(s) enabled: | 20 cores, 2 chips, 10 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 25 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R) |
| Disk Subsystem: | 1 x 250 GB SATA, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.5 (Santiago)<br>Kernel 2.6.32-431.20.3.el6.x86_64 |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 40 | 650 | 602 | **650** | **601** | 655 | 597 | 40 | 516 | 757 | 517 | 756 | **517** | **757** |
| 401.bzip2 | 40 | **948** | **407** | 947 | 408 | 950 | 406 | 40 | **914** | **422** | 912 | 423 | 915 | 422 |
| 403.gcc | 40 | 489 | 658 | 486 | 663 | **488** | **660** | 40 | **485** | **664** | 483 | 666 | 488 | 660 |
| 429.mcf | 40 | 317 | 1150 | **316** | **1160** | 315 | 1160 | 40 | 317 | 1150 | **316** | **1160** | 315 | 1160 |
| 445.gobmk | 40 | **778** | **539** | 778 | 539 | 778 | 540 | 40 | 764 | 549 | 764 | 549 | **764** | **549** |
| 456.hmmer | 40 | 317 | 1180 | **316** | **1180** | 312 | 1200 | 40 | **287** | **1300** | 287 | 1300 | 286 | 1300 |
| 458.sjeng | 40 | **841** | **576** | 841 | 575 | 840 | 576 | 40 | **809** | **598** | 809 | 598 | 809 | 599 |
| 462.libquantum | 40 | 102 | 8150 | **102** | **8150** | 102 | 8130 | 40 | 102 | 8150 | **102** | **8150** | 102 | 8130 |
| 464.h264ref | 40 | 960 | 923 | **940** | **942** | 913 | 969 | 40 | **905** | **978** | 934 | 948 | 900 | 984 |
| 471.omnetpp | 40 | 521 | 480 | **525** | **476** | 526 | 476 | 40 | 502 | 498 | **503** | **497** | 504 | 496 |
| 473.astar | 40 | **607** | **463** | 604 | 465 | 608 | 462 | 40 | **607** | **463** | 604 | 465 | 608 | 462 |
| 483.xalancbmk | 40 | 298 | 926 | 297 | 928 | **298** | **927** | 40 | 298 | 926 | 297 | 928 | **298** | **927** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS Settings:
 Power Management Policy: Custom
 Energy Performance: Performance
 Patrol Scrub: Disabled
 Cluster on Die: Enabled
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
```

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## General Notes (Continued)

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
```
icc  -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:
```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

Continued on next page

## Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevC.xml


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For other inquiries, please contact webmaster@spec.org.

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