



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 2170

ProLiant DL580 Gen8
(2.50 GHz, Intel Xeon E5-4880 v2)

SPECint_rate_base2006 = 2110

CPU2006 license: 3

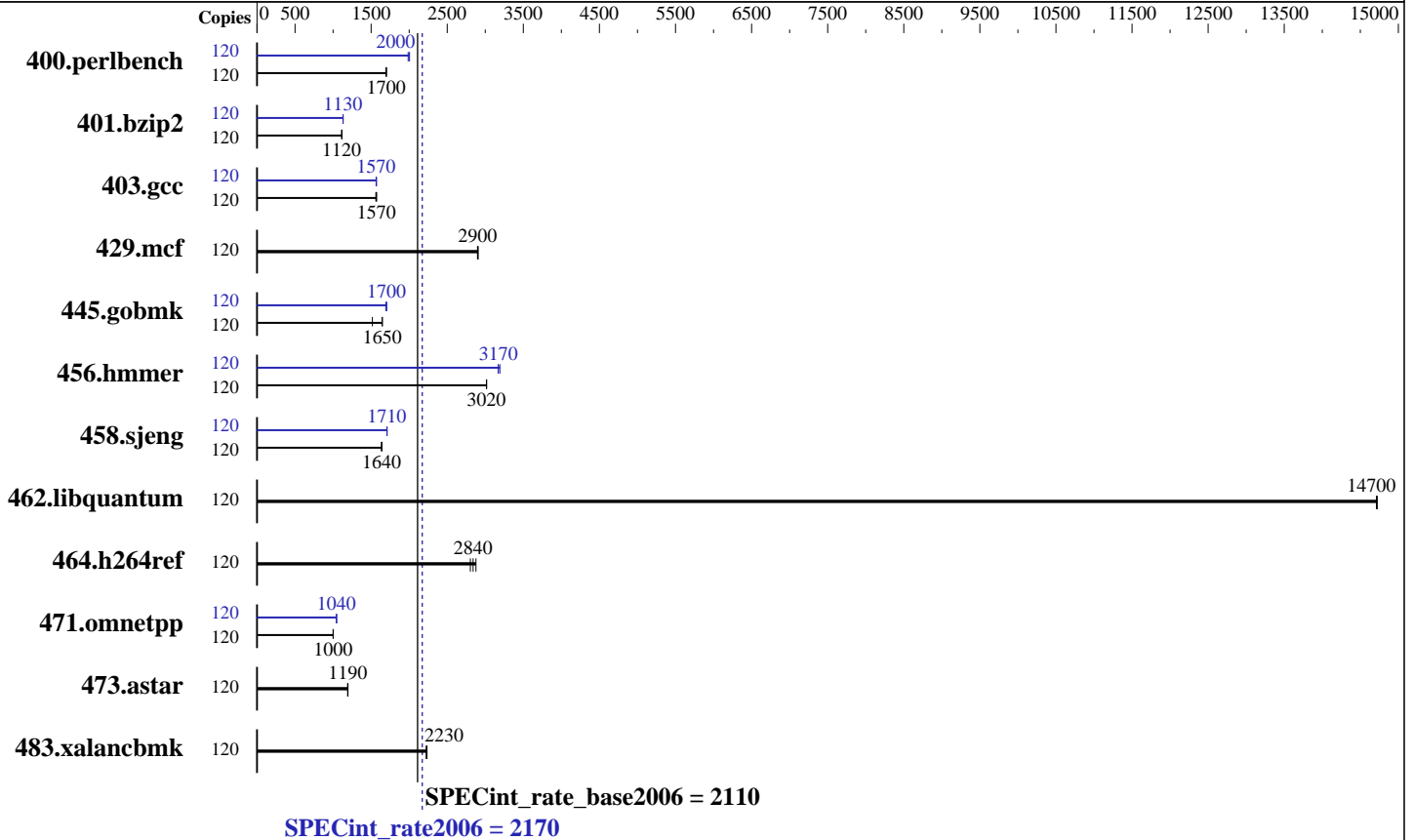
Test date: Jun-2014

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2014

Tested by: Hewlett-Packard Company

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E7-4880 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 60 cores, 4 chips, 15 cores/chip, 2 threads/core
 CPU(s) orderable: 2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 37.5 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (64 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 2 x 300 GB SAS, RAID 1
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5, (Santiago)
 Kernel 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	120	692	1690	689	1700	<u>691</u>	<u>1700</u>	120	585	2000	590	1990	<u>587</u>	<u>2000</u>
401.bzip2	120	1041	1110	1037	1120	<u>1039</u>	<u>1120</u>	120	1025	1130	1023	1130	<u>1024</u>	<u>1130</u>
403.gcc	120	616	1570	615	1570	618	1560	120	616	1570	617	1570	615	1570
429.mcf	120	376	2910	378	2900	<u>377</u>	<u>2900</u>	120	376	2910	378	2900	<u>377</u>	<u>2900</u>
445.gobmk	120	830	1520	765	1650	764	1650	120	739	1700	739	1700	744	1690
456.hammer	120	371	3010	<u>371</u>	<u>3020</u>	371	3020	120	<u>353</u>	<u>3170</u>	351	3190	353	3170
458.sjeng	120	887	1640	886	1640	885	1640	120	849	1710	850	1710	850	1710
462.libquantum	120	<u>169</u>	<u>14700</u>	169	14700	169	14700	120	<u>169</u>	<u>14700</u>	169	14700	169	14700
464.h264ref	120	<u>935</u>	<u>2840</u>	947	2800	923	2880	120	<u>935</u>	<u>2840</u>	947	2800	923	2880
471.omnetpp	120	749	1000	<u>749</u>	<u>1000</u>	749	1000	120	718	1050	<u>718</u>	<u>1040</u>	718	1040
473.astar	120	707	1190	705	1200	<u>706</u>	<u>1190</u>	120	707	1190	705	1200	<u>706</u>	<u>1190</u>
483.xalancbmk	120	371	2230	<u>372</u>	<u>2230</u>	372	2220	120	371	2230	<u>372</u>	<u>2230</u>	372	2220

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration:
HP Power Profile set to Maximum Performance
Collaborative Power Control set to Disabled
Thermal Configuration set so Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Fri Jun 13 11:48:38 2014

Continued on next page



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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E7-4880 v2 @ 2.50GHz
 4 "physical id"s (chips)
120 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 15
siblings       : 30
physical 0:    cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 1:    cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 2:    cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 3:    cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
cache size     : 38400 KB

```

```

From /proc/meminfo
MemTotal:      1058653980 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jun 13 11:46

```

SPEC is set to: /home/cpu
Filesystem          Type  Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_home ext4  222G  5.7G  205G   3% /home

```

```

Additional information from dmidecode:
BIOS HP P79 03/19/2014
Memory:
64x HP 712383-081 16 GB 1333 MHz 2 rank
32x UNKNOWN NOT AVAILABLE

```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 1 TB and the dmidecode description should have one line reading as:
64x HP 712383-081 16 GB 1333 MHz 2 rank



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu/libs/32:/home/cpu/libs/64:/home/cpu/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32`

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.xml>

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For other inquiries, please contact webmaster@spec.org.

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