



SPEC[®] CFP2006 Result

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HITACHI

SPECfp[®]2006 = 100

BladeSymphony BS520H (Intel Xeon E5-2697 v2)

SPECfp_base2006 = 95.6

CPU2006 license: 35

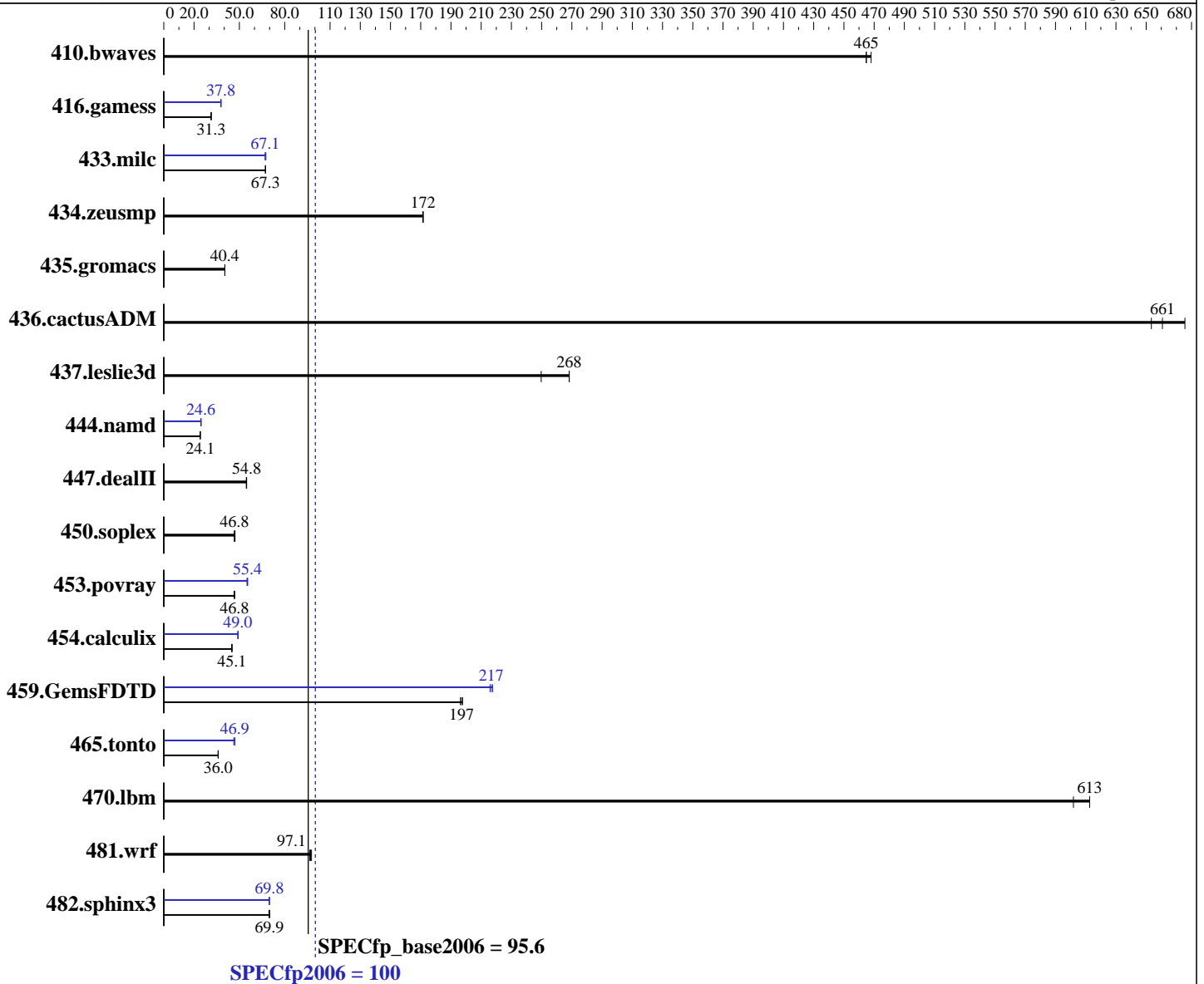
Test date: Mar-2014

Test sponsor: HITACHI

Hardware Availability: Feb-2014

Tested by: HITACHI

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2697 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2700
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.23.2.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: none

Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
Other Hardware: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>29.2</u>	<u>465</u>	29.0	468	29.2	465	<u>29.2</u>	<u>465</u>	29.0	468	29.2	465
416.gamess	<u>625</u>	<u>31.3</u>	624	31.4	626	31.3	519	37.8	517	37.9	<u>518</u>	<u>37.8</u>
433.milc	<u>136</u>	<u>67.3</u>	136	67.4	137	67.2	137	67.1	<u>137</u>	<u>67.1</u>	136	67.6
434.zeusmp	<u>53.1</u>	<u>172</u>	53.0	172	53.1	172	<u>53.1</u>	<u>172</u>	53.0	172	53.1	172
435.gromacs	177	40.4	177	40.3	<u>177</u>	<u>40.4</u>	177	40.4	177	40.3	<u>177</u>	<u>40.4</u>
436.cactusADM	<u>18.1</u>	<u>661</u>	18.3	653	17.7	676	<u>18.1</u>	<u>661</u>	18.3	653	17.7	676
437.leslie3d	<u>35.0</u>	<u>268</u>	35.0	268	37.6	250	<u>35.0</u>	<u>268</u>	35.0	268	37.6	250
444.namd	332	24.1	<u>333</u>	<u>24.1</u>	333	24.1	326	24.6	<u>326</u>	<u>24.6</u>	325	24.7
447.dealII	210	54.6	<u>209</u>	<u>54.8</u>	209	54.8	210	54.6	<u>209</u>	<u>54.8</u>	209	54.8
450.soplex	<u>178</u>	<u>46.8</u>	177	47.1	179	46.7	<u>178</u>	<u>46.8</u>	177	47.1	179	46.7
453.povray	113	46.9	<u>114</u>	<u>46.8</u>	114	46.5	<u>96.1</u>	<u>55.4</u>	96.7	55.0	95.9	55.5
454.calculix	<u>183</u>	<u>45.1</u>	183	45.2	183	45.0	168	49.0	<u>168</u>	<u>49.0</u>	168	49.1
459.GemsFDTD	<u>53.9</u>	<u>197</u>	53.7	198	54.1	196	<u>49.0</u>	<u>217</u>	48.8	218	49.2	216
465.tonto	273	36.0	274	35.9	<u>273</u>	<u>36.0</u>	<u>210</u>	<u>46.9</u>	213	46.3	210	46.9
470.lbm	<u>22.4</u>	<u>613</u>	22.8	602	22.4	613	<u>22.4</u>	<u>613</u>	22.8	602	22.4	613
481.wrf	<u>115</u>	<u>97.1</u>	116	96.6	114	97.7	<u>115</u>	<u>97.1</u>	116	96.6	114	97.7
482.sphinx3	278	70.1	<u>279</u>	<u>69.9</u>	280	69.6	279	69.9	<u>279</u>	<u>69.8</u>	280	69.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Thu Mar 20 06:28:59 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

```

```

From /proc/meminfo
MemTotal:      132192944 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-358.23.2.el6.x86_64 #1 SMP Sat Sep 14
05:32:37 EDT 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 20 00:30

```

SPEC is set to: /home/cpu2006
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      133G      17G  110G   13% /

```

```

Additional information from dmidecode:
BIOS HITACHI EP1043 02/04/2014
Memory:
16x Not Specified Not Specified
8x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

```

(End of data from sysinfo program)

General Notes

```

Environment variables set by runspec before the start of the run:
KMP_AFFINITY="granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "24"

```

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 BladeSymphony BS520H and Hitachi Compute Blade 520H are electronically equivalent.
 The results have been measured on a BladeSymphony BS520H

Base Compiler Invocation

C benchmarks:
 icc -m64

C++ benchmarks:
 icpc -m64

Fortran benchmarks:
 ifort -m64

Benchmarks using both Fortran and C:
 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel`

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>



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