# SPEC® CFP2006 Result



## Huawei

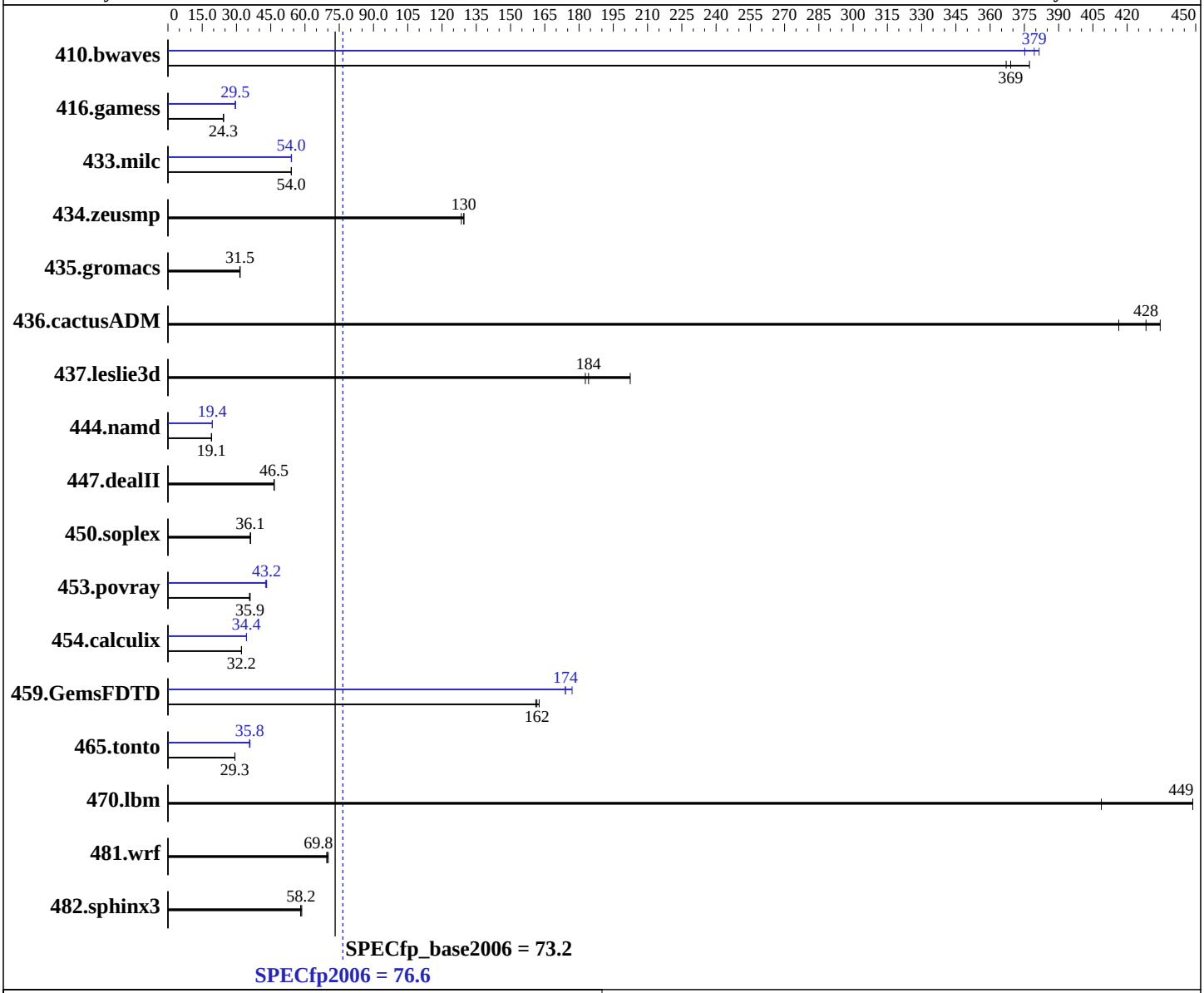
Huawei RH1288 V2 (Intel Xeon E5-2650 2.0 GHz)

SPECfp®2006 = 76.6

SPECfp_base2006 = 73.2

**CPU2006 license:** 3175
**Test sponsor:** Huawei
**Tested by:** Huawei

**Test date:** Jun-2013
**Hardware Availability:** May-2012
**Software Availability:** Feb-2013




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2650 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.80 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.4 (Santiago)<br>2.6.32-358.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;<br>Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext4 |

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| | | | |
|---|---|---|---|
| L3 Cache: | 20 MB I+D on chip per chip | System State: | Run level 3 (multi-user) |
| Other Cache: | None | Base Pointers: | 64-bit |
| Memory: | 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC) | Peak Pointers: | 32/64-bit |
| Disk Subsystem: | 1 x 500 GB SATA, 7200 RPM | Other Software: | None |
| Other Hardware: | None | | |

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 37.0 | 367 | **36.8** | **369** | 36.0 | 377 | 36.2 | 375 | **35.8** | **379** | 35.6 | 381 |
| 416.gamess | **806** | **24.3** | 804 | 24.4 | 807 | 24.3 | 663 | 29.5 | **663** | **29.5** | 663 | 29.5 |
| 433.milc | **170** | **54.0** | 170 | 54.0 | 170 | 54.1 | 170 | 54.1 | **170** | **54.0** | 170 | 54.0 |
| 434.zeusmp | 70.8 | 128 | 70.2 | 130 | **70.2** | **130** | 70.8 | 128 | 70.2 | 130 | **70.2** | **130** |
| 435.gromacs | 227 | 31.5 | **226** | **31.5** | 226 | 31.6 | 227 | 31.5 | **226** | **31.5** | 226 | 31.6 |
| 436.cactusADM | 28.7 | 416 | **27.9** | **428** | 27.5 | 434 | 28.7 | 416 | **27.9** | **428** | 27.5 | 434 |
| 437.leslie3d | 51.4 | 183 | **51.0** | **184** | 46.4 | 202 | 51.4 | 183 | **51.0** | **184** | 46.4 | 202 |
| 444.namd | 421 | 19.1 | **421** | **19.1** | 421 | 19.1 | **413** | **19.4** | 413 | 19.4 | 413 | 19.4 |
| 447.dealII | 246 | 46.6 | 246 | 46.4 | **246** | **46.5** | 246 | 46.6 | 246 | 46.4 | **246** | **46.5** |
| 450.soplex | 230 | 36.2 | 231 | 36.0 | **231** | **36.1** | 230 | 36.2 | 231 | 36.0 | **231** | **36.1** |
| 453.povray | 148 | 36.0 | **148** | **35.9** | 149 | 35.7 | 123 | 43.2 | **123** | **43.2** | 124 | 42.7 |
| 454.calculix | **256** | **32.2** | 256 | 32.2 | 257 | 32.2 | 240 | 34.4 | **240** | **34.4** | 240 | 34.4 |
| 459.GemsFDTD | 65.9 | 161 | 65.3 | 163 | **65.7** | **162** | 61.0 | 174 | 60.0 | 177 | **61.0** | **174** |
| 465.tonto | 335 | 29.3 | 336 | 29.3 | **336** | **29.3** | 274 | 35.9 | **275** | **35.8** | 276 | 35.7 |
| 470.lbm | 33.6 | 409 | 30.6 | 449 | **30.6** | **449** | 33.6 | 409 | 30.6 | 449 | **30.6** | **449** |
| 481.wrf | 161 | 69.5 | **160** | **69.8** | 159 | 70.2 | 161 | 69.5 | **160** | **69.8** | 159 | 70.2 |
| 482.sphinx3 | 336 | 58.1 | 333 | 58.6 | **335** | **58.2** | 336 | 58.1 | 333 | 58.6 | **335** | **58.2** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
Select only test related files when installing the operating system
```

## Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Baseboard Management Controller used to adjust the fan speed to 100%
Intel Hyper Threading Technology = Disable
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost Sun Jun 30 00:11:35 2013
```

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## Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650 0 @ 2.00GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       132117844 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 29 17:06

SPEC is set to: /spec
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/sda2      ext4    193G   81G  102G  45% /

Additional information from dmidecode:

(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "16"
```

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## General Notes (Continued)

```
Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory
using RHEL 6.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

## Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

`Same as Base Portability Flags`

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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