



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 437

Servidor Itaotec LX205 (Intel Xeon E5-2630)

SPECint\_rate\_base2006 = 419

CPU2006 license: 9001

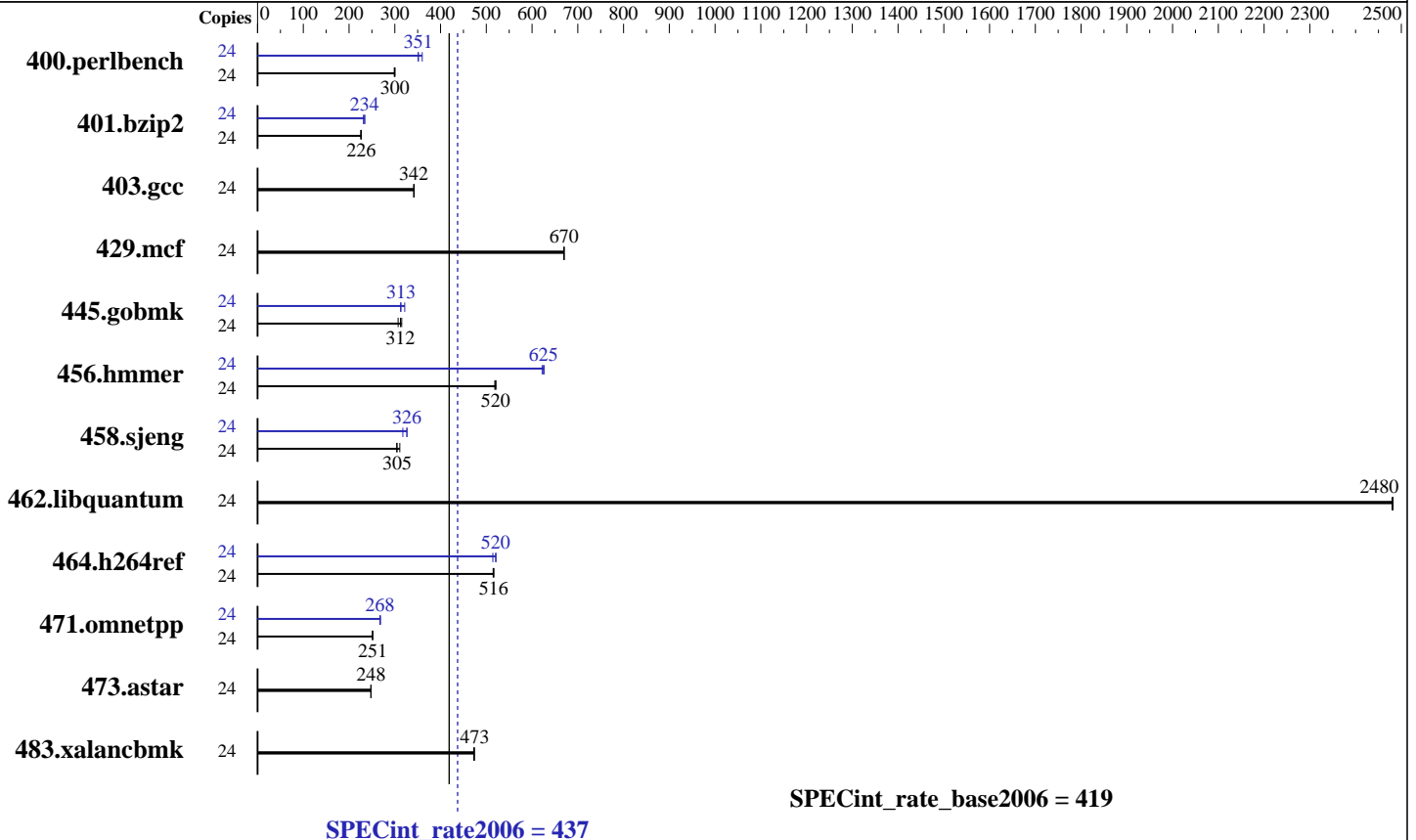
Test date: Dec-2011

Test sponsor: Itaotec

Hardware Availability: Jun-2012

Tested by: Itaotec

Software Availability: Dec-2011



## Hardware

CPU Name: Intel Xeon E5-2630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (16 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 500 GB, SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>782</b>	<b>300</b>	781	300	785	299	24	651	360	667	351	<b>667</b>	<b>351</b>
401.bzip2	24	1020	227	1028	225	<b>1023</b>	<b>226</b>	24	989	234	1001	231	<b>990</b>	<b>234</b>
403.gcc	24	564	342	566	341	<b>565</b>	<b>342</b>	24	564	342	566	341	<b>565</b>	<b>342</b>
429.mcf	24	327	670	327	670	<b>327</b>	<b>670</b>	24	327	670	327	670	<b>327</b>	<b>670</b>
445.gobmk	24	799	315	<b>806</b>	<b>312</b>	819	307	24	805	313	783	322	<b>803</b>	<b>313</b>
456.hammer	24	432	519	<b>431</b>	<b>520</b>	430	521	24	360	622	<b>358</b>	<b>625</b>	358	626
458.sjeng	24	<b>952</b>	<b>305</b>	934	311	955	304	24	889	327	<b>890</b>	<b>326</b>	915	318
462.libquantum	24	<b>200</b>	<b>2480</b>	200	2480	201	2480	24	<b>200</b>	<b>2480</b>	200	2480	201	2480
464.h264ref	24	1028	516	1031	515	<b>1029</b>	<b>516</b>	24	1033	514	1019	521	<b>1021</b>	<b>520</b>
471.omnetpp	24	<b>597</b>	<b>251</b>	596	252	597	251	24	558	269	<b>560</b>	<b>268</b>	561	267
473.astar	24	<b>679</b>	<b>248</b>	680	248	679	248	24	<b>679</b>	<b>248</b>	680	248	679	248
483.xalancbmk	24	<b>350</b>	<b>473</b>	351	472	349	474	24	<b>350</b>	<b>473</b>	351	472	349	474

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on localhost Wed Dec 14 16:47:42 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Genuine Intel(R) CPU @ 2.30GHz  
model name : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz  
2 "physical id"s (chips)  
24 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6

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## Platform Notes (Continued)

```
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      65944204 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 14 16:40
```

```
SPEC is set to: /home/rcaneca/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_it5rh-lv_home
ext4            193G    2.0G  181G   2% /home
```

(End of data from sysinfo program)

## General Notes

This result was measured on the Servidor Itaotec MX205. The Servidor Itaotec MX205, the Servidor Itaotec MX225+ and the Servidor Itaotec LX205 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itautec-Servidor\\_Itautec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itautec-Servidor\\_Itautec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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