



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 468

PRIMERGY CX270 S1, Intel Xeon E5-2665, 2.40 GHz

SPECfp_rate_base2006 = 455

CPU2006 license: 19

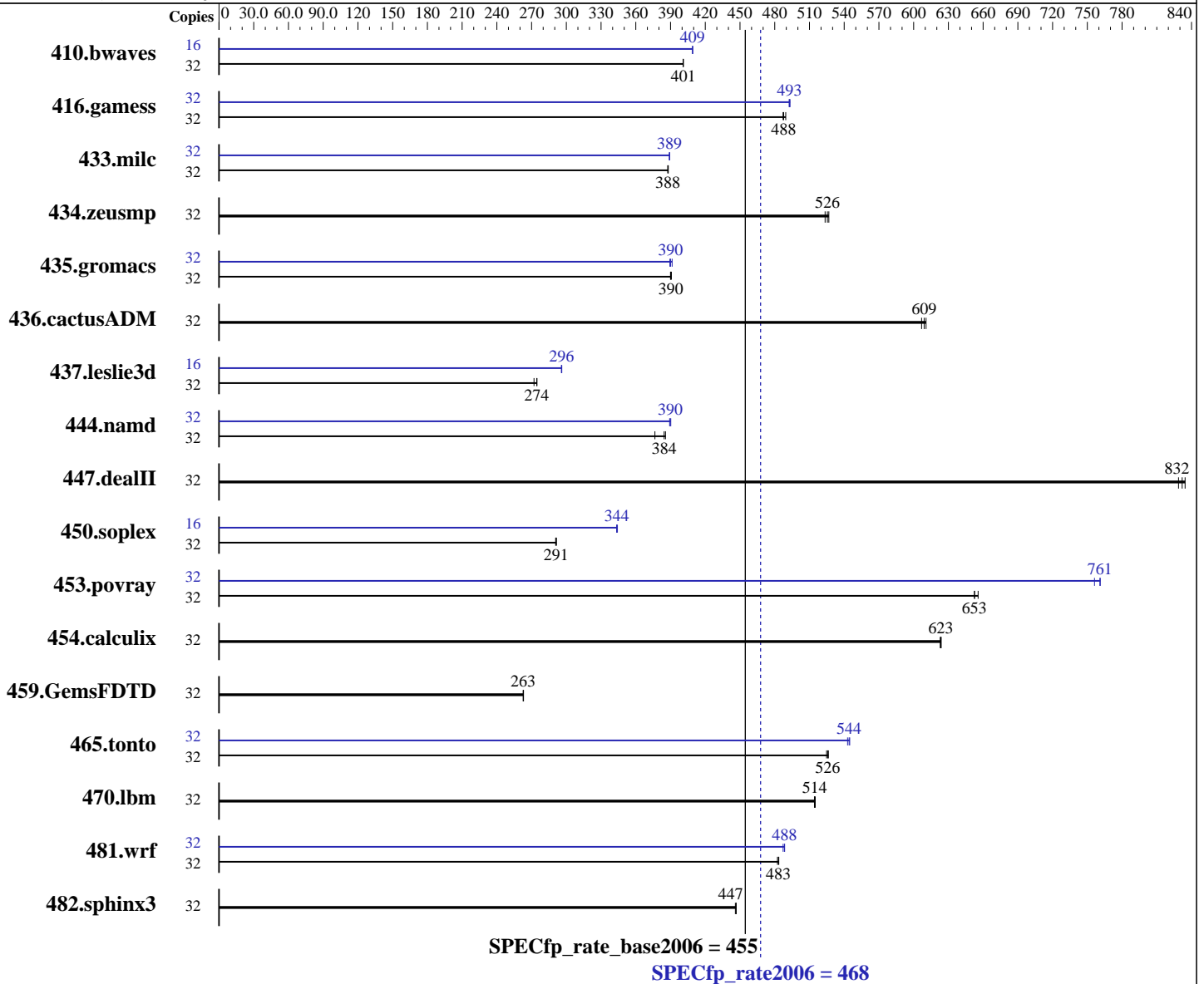
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2012

Hardware Availability: Jun-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2665
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	32	<u>1085</u>	<u>401</u>	1084	401	1085	401	16	532	409	<u>532</u>	<u>409</u>	531	409		
416.gamess	32	1280	490	<u>1285</u>	<u>488</u>	1286	487	32	1270	493	<u>1272</u>	<u>493</u>	1272	492		
433.milc	32	757	388	<u>758</u>	<u>388</u>	758	388	32	<u>755</u>	<u>389</u>	755	389	755	389		
434.zeusmp	32	553	527	556	524	<u>554</u>	<u>526</u>	32	553	527	556	524	<u>554</u>	<u>526</u>		
435.gromacs	32	586	390	<u>586</u>	<u>390</u>	585	391	32	<u>586</u>	<u>390</u>	584	391	587	389		
436.cactusADM	32	626	611	630	607	<u>628</u>	<u>609</u>	32	626	611	630	607	<u>628</u>	<u>609</u>		
437.leslie3d	32	<u>1096</u>	<u>274</u>	1095	275	1106	272	16	509	296	<u>508</u>	<u>296</u>	508	296		
444.namd	32	<u>667</u>	<u>384</u>	682	377	665	386	32	659	389	658	390	<u>659</u>	<u>390</u>		
447.dealII	32	<u>440</u>	<u>832</u>	439	834	442	829	32	<u>440</u>	<u>832</u>	439	834	442	829		
450.soplex	32	918	291	<u>916</u>	<u>291</u>	916	291	16	<u>388</u>	<u>344</u>	388	344	389	343		
453.povray	32	<u>261</u>	<u>653</u>	260	656	261	653	32	225	756	<u>224</u>	<u>761</u>	224	761		
454.calculix	32	423	624	424	623	<u>423</u>	<u>623</u>	32	423	624	424	623	<u>423</u>	<u>623</u>		
459.GemsFDTD	32	<u>1292</u>	<u>263</u>	1292	263	1291	263	32	<u>1292</u>	<u>263</u>	1292	263	1291	263		
465.tonto	32	<u>599</u>	<u>526</u>	600	525	598	526	32	<u>579</u>	<u>544</u>	580	543	578	545		
470.lbm	32	855	514	854	515	<u>855</u>	<u>514</u>	32	855	514	854	515	<u>855</u>	<u>514</u>		
481.wrf	32	741	482	739	484	<u>740</u>	<u>483</u>	32	734	487	<u>732</u>	<u>488</u>	732	489		
482.sphinx3	32	1396	447	<u>1396</u>	<u>447</u>	1398	446	32	1396	447	<u>1396</u>	<u>447</u>	1398	446		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

This result was measured on the PRIMERGY CX250 S1. The PRIMERGY CX250 S1 and the PRIMERGY CX270 S1 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex: icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.xml>

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For other inquiries, please contact webmaster@spec.org.

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