SPEC® CINT2006 Result

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Intel Corporation

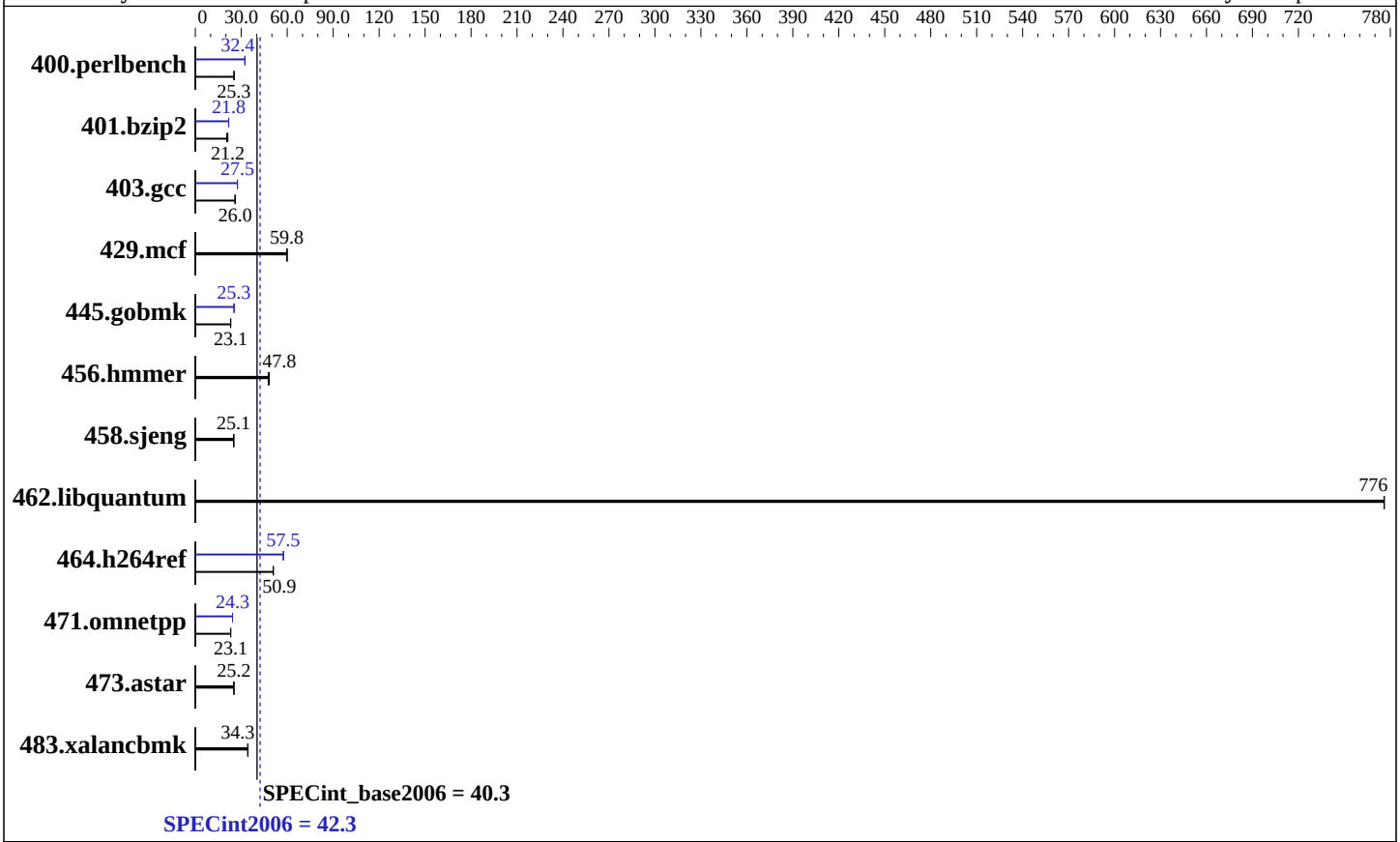
Intel DH61WW motherboard (Intel Core i5-2310)

SPECint®2006 = 42.3

SPECint_base2006 = 40.3

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2011
Hardware Availability: May-2011
Software Availability: Sep-2011



Hardware

CPU Name:	Intel Core i5-2310
CPU Characteristics:	Intel Turbo Boost Technology up to 3.2 GHz
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	1 TB Seagate SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows 7 Ultimate 6.1.7601 Service Pack 1 Build 7601
Compiler:	C/C++: Version 12.1.0.229 of Intel C++ Studio XE for Windows; Libraries: Version 15.00.30729.01 of Microsoft Visual Studio 2008 Professional SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**386**	**25.3**	386	25.3	386	25.3	302	32.4	**302**	**32.4**	302	32.4
401.bzip2	470	20.5	456	21.2	**456**	**21.2**	443	21.8	443	21.8	**443**	**21.8**
403.gcc	309	26.0	**309**	**26.0**	310	26.0	**293**	**27.5**	293	27.5	293	27.5
429.mcf	**152**	**59.8**	152	59.8	152	60.0	**152**	**59.8**	152	59.8	152	60.0
445.gobmk	**455**	**23.1**	455	23.1	455	23.1	415	25.3	**415**	**25.3**	415	25.3
456.hmmer	**195**	**47.8**	193	48.3	196	47.6	**195**	**47.8**	193	48.3	196	47.6
458.sjeng	482	25.1	**482**	**25.1**	482	25.1	482	25.1	**482**	**25.1**	482	25.1
462.libquantum	26.7	776	**26.7**	**776**	26.7	776	26.7	776	**26.7**	**776**	26.7	776
464.h264ref	435	50.9	**435**	**50.9**	435	50.9	**385**	**57.5**	385	57.5	385	57.5
471.omnetpp	270	23.1	**270**	**23.1**	270	23.1	257	24.3	257	24.3	**257**	**24.3**
473.astar	279	25.2	**279**	**25.2**	279	25.2	279	25.2	**279**	**25.2**	279	25.2
483.xalancbmk	**201**	**34.3**	201	34.3	201	34.3	**201**	**34.3**	201	34.3	201	34.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
ipsxe-comp-vars batch file invoked with intel64
```

Platform Notes

```
Sysinfo program C:\SPEC12.1/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ \8787f7622badcf24e01c368b1db4377c
running on CltE06995A30CB5 Wed Dec 21 07:54:38 2011

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

Trying 'systeminfo'
OS Name       : Microsoft Windows 7 Ultimate
OS Version    : 6.1.7601 Service Pack 1 Build 7601
System Manufacturer: INTEL_
System Model  : DH61WW__
Processor(s)  : 1 Processor(s) Installed.
     [01]: Intel64 Family 6 Model 42 Stepping 7 GenuineIntel ~2901 Mhz
BIOS Version  : Intel Corp. BEH6110H.86A.0016.2011.0118.1128, 1/18/2011
Total Physical Memory: 4,004 MB

Trying 'wmic cpu get /value'
DeviceID      : CPU0
L2CacheSize   : 1024
L3CacheSize   : 6144
MaxClockSpeed : 2901
```

Continued on next page

Platform Notes (Continued)

```
Name          : Intel(R) Core(TM) i5-2310 CPU @ 2.90GHz
NumberOfCores : 4
NumberOfLogicalProcessors: 4

(End of data from sysinfo program)
```

Component Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

General Notes

```
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
Binaries compiled on a system with 1x Intel Core i7-860 CPU
+ 8GB memory using Windows 7 Enterprise 64-bit
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
  401.bzip2: -DSPEC_CPU_P64
    403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
    429.mcf: -DSPEC_CPU_P64
  445.gobmk: -DSPEC_CPU_P64
  456.hmmer: -DSPEC_CPU_P64
  458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
  464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
  471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
    473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32
/F512000000
```

C++ benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlW64M.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE`

401.bzip2: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32 /F512000000`

403.gcc: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-windows.20120117.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-windows.20120117.xml

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